

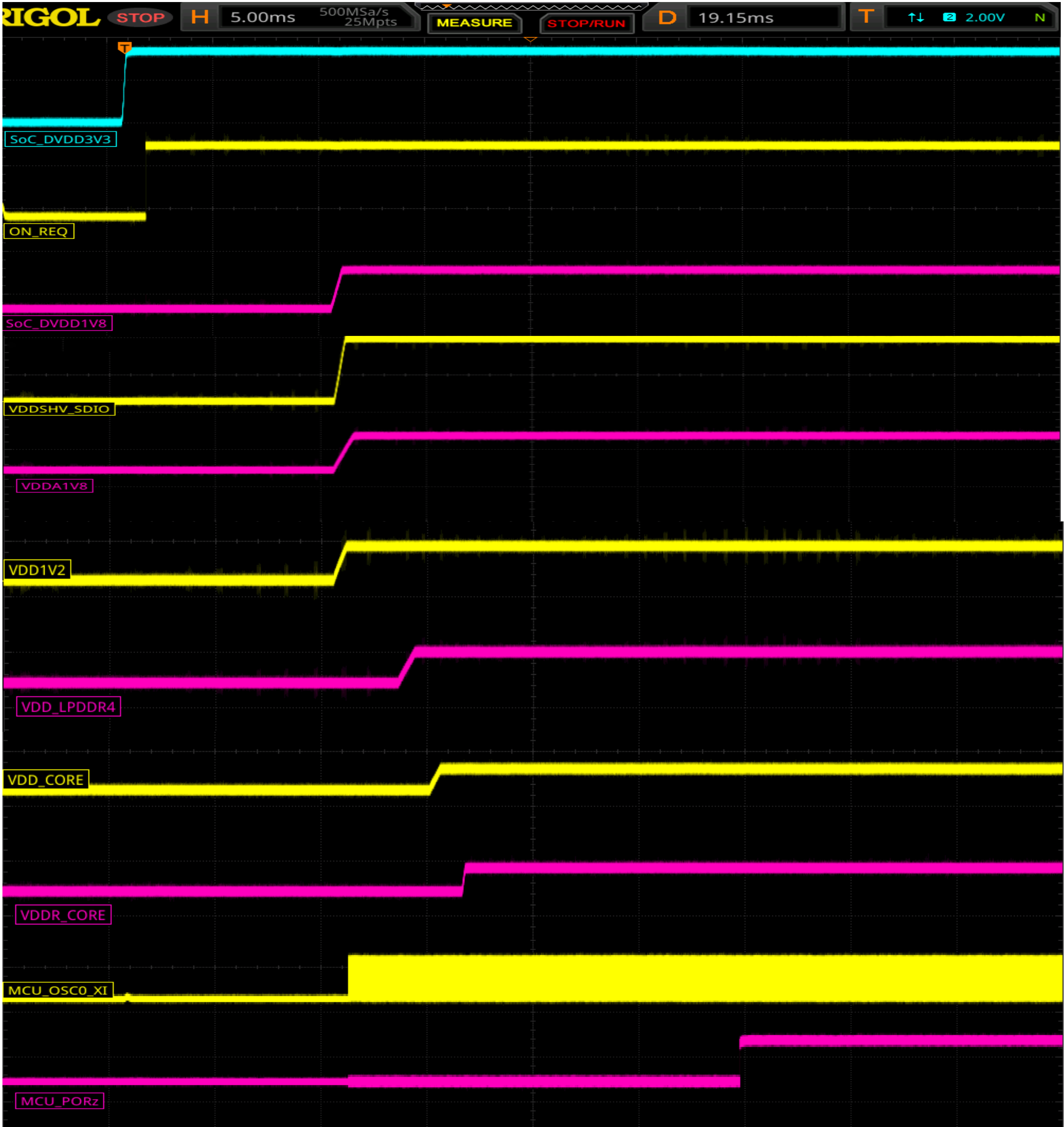


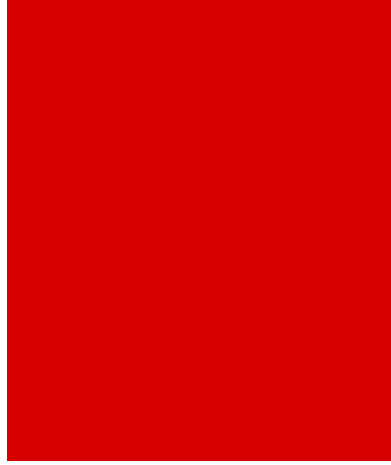
SK-AM62-LP SKEVM
REV E2
PROC124E2A
Power up Sequence

Power Up sequence timing measurements

Power Supply	Voltage (V)	Elapsed Time (mS)
SoC_DVDD3V3	3.3	0
EN_REQ	3.3	0.995
SoC_DVDD1V8	1.8	10.05
VDDSHV_SDIO	3.3	10.05
VDDA_1V8	1.8	10.05
VDD_1V2	1.2	10.05
VDD_LPDDR4	1.1	12.95
VDD_CORE	0.75	14.45
VDDR_CORE	0.85	15.95
MCU_OSC0_XI	1.8	10.6
MCU_PORz (Reset)	1.8	28.9

Power Up sequence Diagram





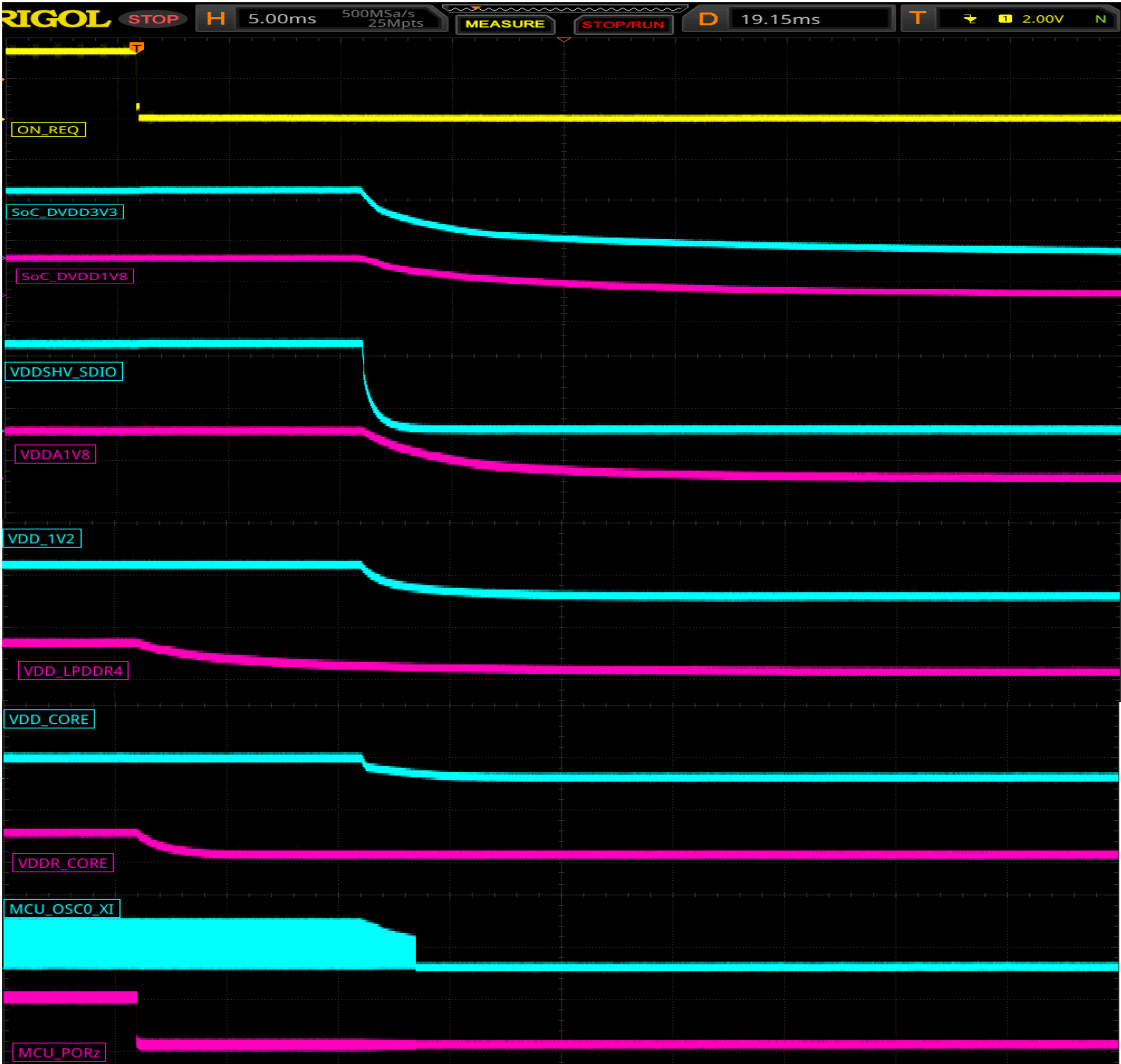
SK-AM62-LP SKEVM
REV E2
PROC124E2A
Power Down Sequence

Power Down sequence timing measurements



Power Supply	Voltage (V)	Elapsed Time (mS)
SoC_DVDD3V3	3.3	10
EN_REQ	3.3	0
SoC_DVDD1V8	1.8	10
VDDSHV_SDIO	3.3	10
VDDA_1V8	1.8	10
VDD_1V2	1.2	10
VDD_LPDDR4	1.1	0.4
VDD_CORE	0.75	10
VDDR_CORE	0.85	0.45
MCU_OSC0_XI	1.8	10
MCU_PORz (Reset)	1.8	0.3

Power Down sequence diagram



Combined Power sequence diagram

