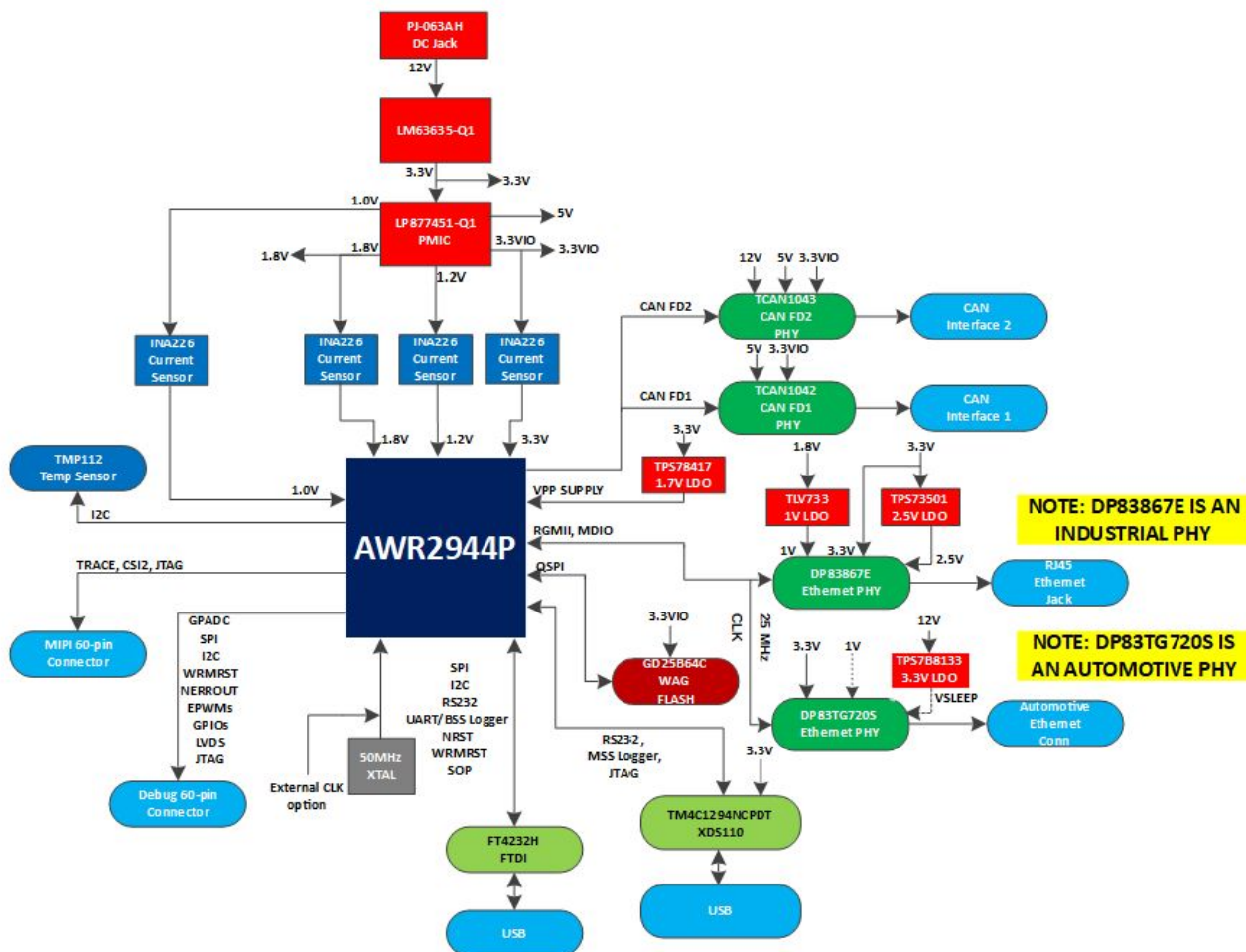


### BLOCK DIAGRAM



## Revision History

Rev	ECN #	Approved Date	Approved by	Notes
REV B	01	16-04-2021		Implemented PMIC review comments from TI
REV B	02	20-04-2021		Implemented Automotive Ethernet review comments from TI
REV B	03	20-05-2021		Updated assembly property of R196 to Fitted. Updated assembly property of R371 & R379 to Not Fitted.
REV B	04	21-05-2021		BSS_UARTA_TX signal is removed from XD0S110 and connected to C port of FTDI Updated assembly property of R160 & R164 to Not Fitted. Updated assembly property of R131 to Fitted.
REV B	05	25-05-2021		Part number of R260, R270, R275, R281 & C167 changed Added 0 ohm resistors in the LVDS path Option path for LVDS data lanes TX2 and TX3 added
REV B	06	03-06-2021		Auto Ethernet ESD Diodes (D18, D19) part number changed to TPD1E05
REV B	07	07-06-2021		10uF decap (C10) moved to I18, CLK supply from I18_VCO supply
REV B	08	14-06-2021		0 ohm resistor (R72) added in J19.13
REV B	09	16-06-2021		R20 2 net name changed to I1V0_RF2
REV B	10	17-06-2021		Updated assembly property of R244 to Not Fitted
REV B	11	21-06-2021		Removed surface circuitry from the PMIC Added provision for LC filter on 1.0V and 1.8V supplies Combined I1V0_RF1 & I1V0_RF2 into a single I1V0 supply and removed one of the current sensor
REV B	12	22-06-2021		GPADC2 input changed to I1V2
REV B	13	23-06-2021		Updated assembly property of C179, C181, J1, J5, C127, R71, R20, C55 & C56 Removed C122 and C143. Added provision for 10uF cap on VDDA supply R259 changed to BLM118G6001SH1. C154 & C182 replaced with 0.01uF
REV B	14	24-06-2021		AWR 3.3V supply changed to pre-regulator output by default (REGOUT_3V3) Added resistor option to take AWR 3.3V supply from Chariot VIO
REV B	15	06-07-2021		Updated R331 and R332 to 1k ohm resistor Updated R347, R262, R261, R263 to 510 ohm resistor VDDIO supply of Auto Ethernet PHY (U4_34) changed to 3V3_VIO Populated R116 by default and R199 changed to DNI
REV B	16	09-07-2021		FB1 changed to BLM118AG102SHID
REV B	17	13-07-2021		Updated assembly property of C7, R14, R291, R67 & R70
REV B	18	16-07-2021		Block diagram updated
REV B	19	17-09-2021		R13 & R17 are made mountable to control CAN STB from PMIC INT
REV C	20	19-04-2022		Added I20 Inductor@4.7uH in PMIC Input
REV D	21	29-06-2022		Added R200, L110, C143, C199, C200, C201, C202, R210 Changed PMIC Enable Pullup, VCCA and Boost input supply to PVIN3V3
REV D	22	02-08-2023		Updated C106 & C108 to 3pF GCM1553C1H39B9A16D R6 & R8 are made DNP and R7 as mountable
REV A	23	21-05-2024		Updated to AWR2945C device with support for OSC, CLK_OUT, ETH signal & Changed U4 to IG05-AUTO ETH PHY with BOM updates and P06 provision
REV A	24	25-06-2024		Changed Y3 to 50MHz, R301, R309 made mountable and R303, R312 made DNP
REV A	25	26-08-2024		Updated L6 D11 part numbers

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10	VPP_LDO
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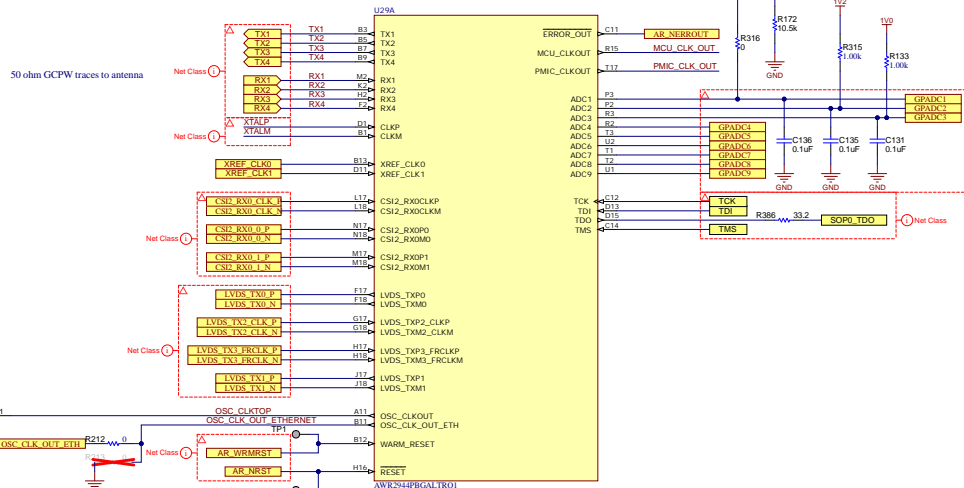
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Drawn By:	File: <a href="#">PROC194A_CoverSheet_SchDoc</a>	Size: B
Engineer: <a href="#">James MURDOCK</a>	Contact: <a href="#">http://www.ti.com/support</a>	



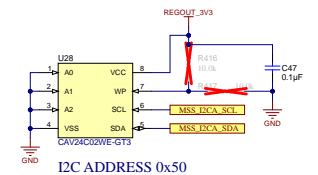
## AWR2944P IO REFERENCE

## JTAG, RESET, ERROR, CLKOUT, LVDS, CSI, GPADC, CLK

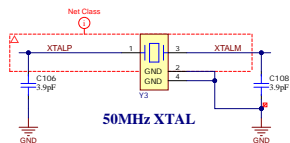
NOMINAL VOLTAGE FOR ADC1 = 01.142V



## BOARD ID EEPROM

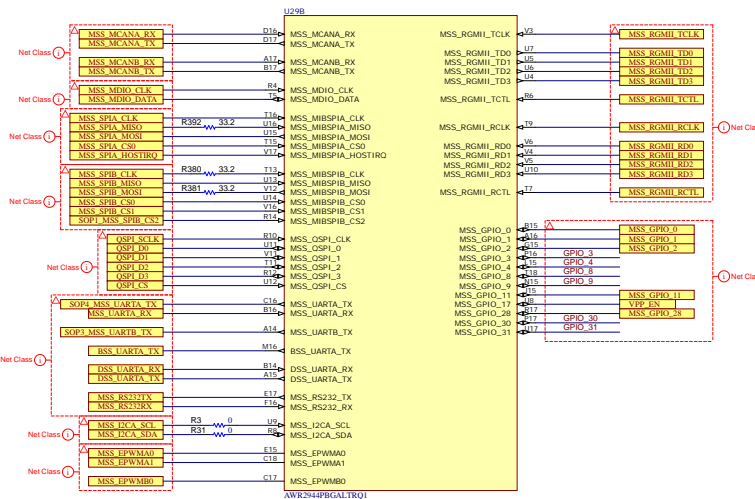


## 50MHZ CLOCK SOURCES

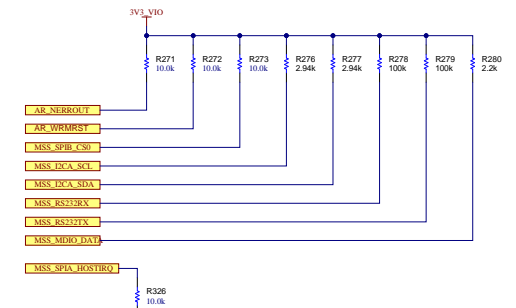


NOTE: PLACE R269 SUCH THAT NO STUBS OCCUR ON 40MHz PATH

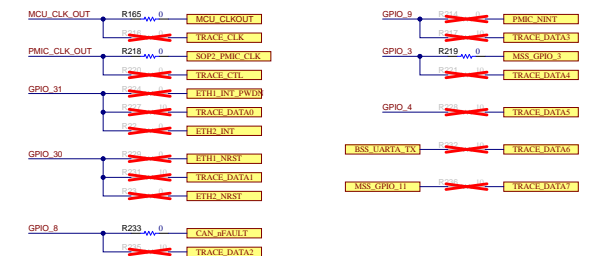
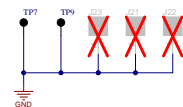
## CAN, MDIO, SPI, QSPI, UART, EPWM, RGMII, GPIO



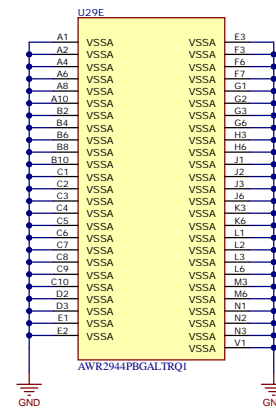
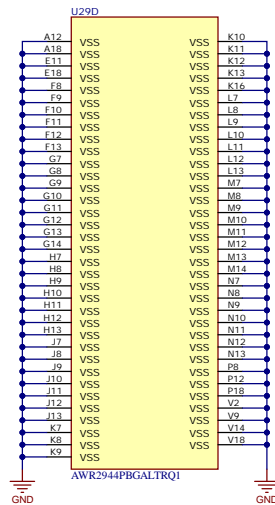
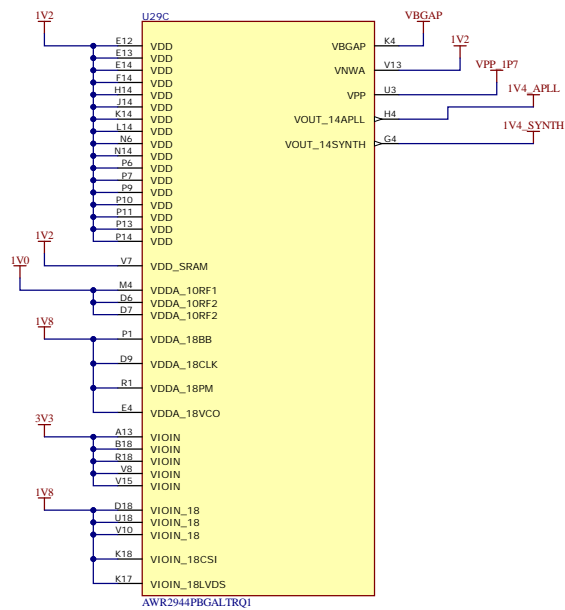
## PULLUPS/DOWNS



## GND TEST POINTS



# AWR2944P POWER REFERENCE

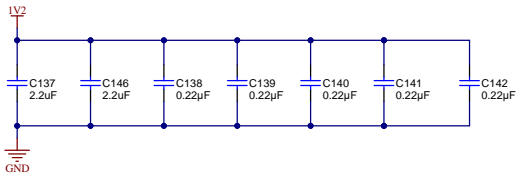


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Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 3 of 25
Drawn By:	File: PROC194A_PWR_Reference.SchDoc	Size: B
Engineer: James MURDOCK	Contact: http://www.ti.com/support	http://www.ti.com

DECOUPLING REFERENCE

1.2V DIGITAL SUPPLY



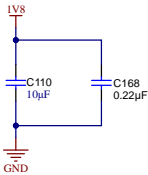
SRAM SUPPLY



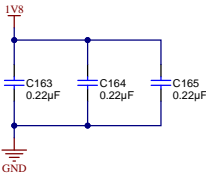
VNWA SUPPLY



1.8V CLOCK SUPPLY



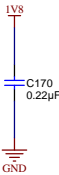
1.8V IO SUPPLY



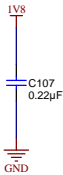
1.8V LVDS SUPPLY



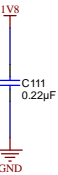
1.8V CSI SUPPLY



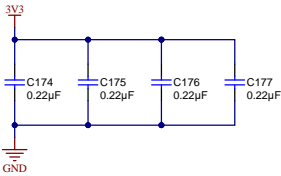
1.8V PM SUPPLY



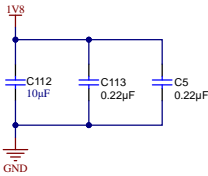
1.8V VCO SUPPLY



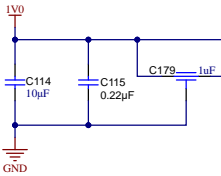
3.3V IO SUPPLY



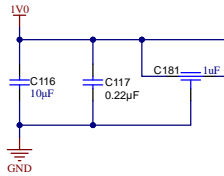
1.8V BB SUPPLY



RF1 SUPPLY



RF2 SUPPLY



BANDGAP SUPPLY



VPP SUPPLY



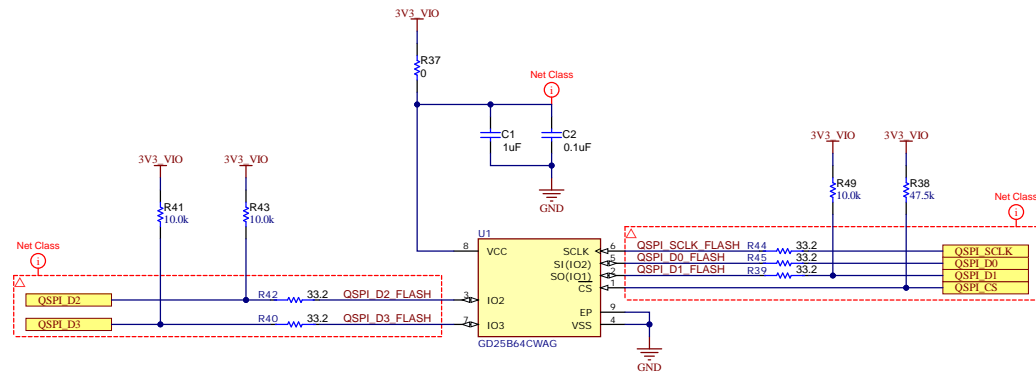
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Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

## References

[GD25B64CWAG Datasheet](#)

## QSPI FLASH REFERENCE



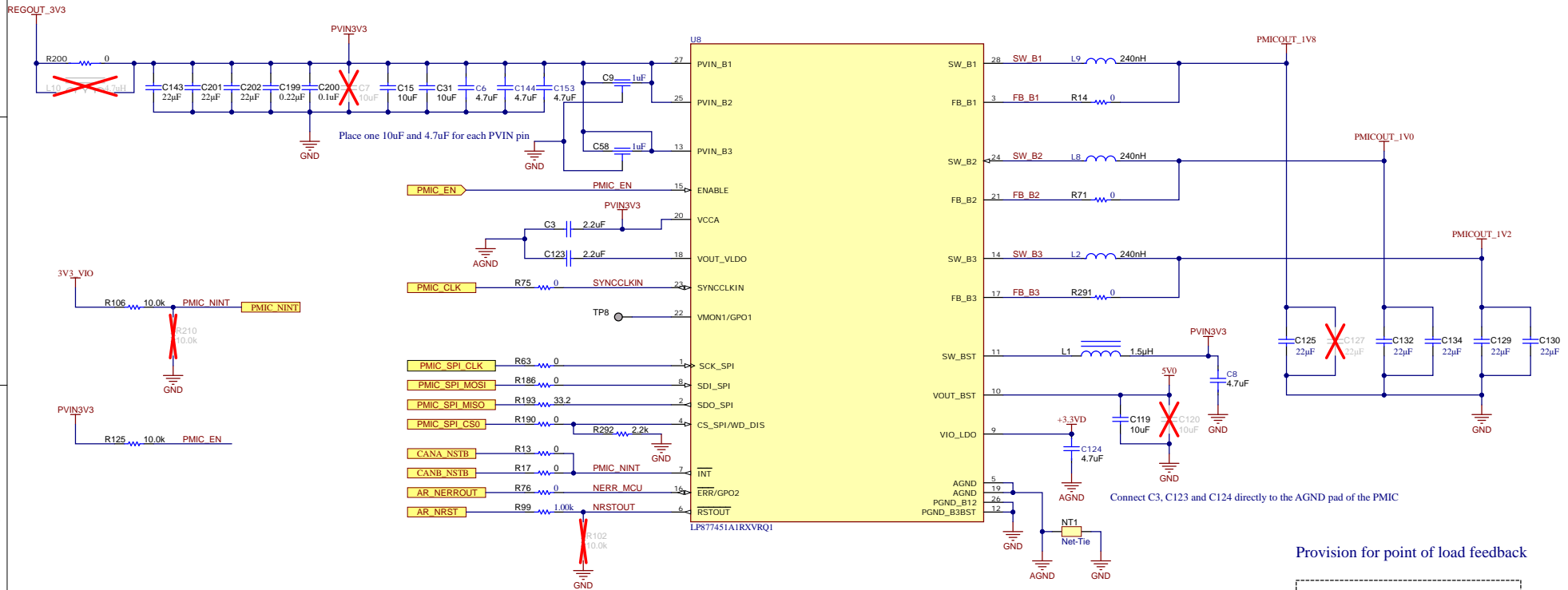
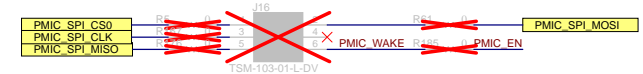
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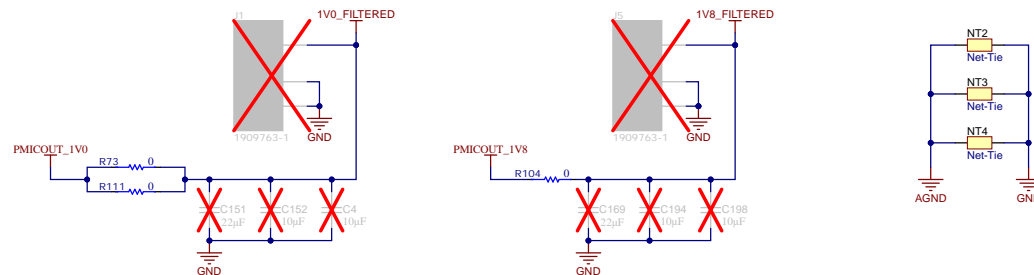
## References

## PMIC REFERENCE

### DEBUG TEST PINS

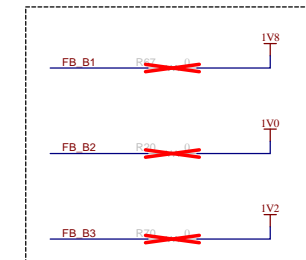


## PMIC LC FILTER



Connect AGND to PGND in inner layer GND. In any case, PGND should not be connected to power pad on layer on which PMIC is placed

### Provision for point of load feedback

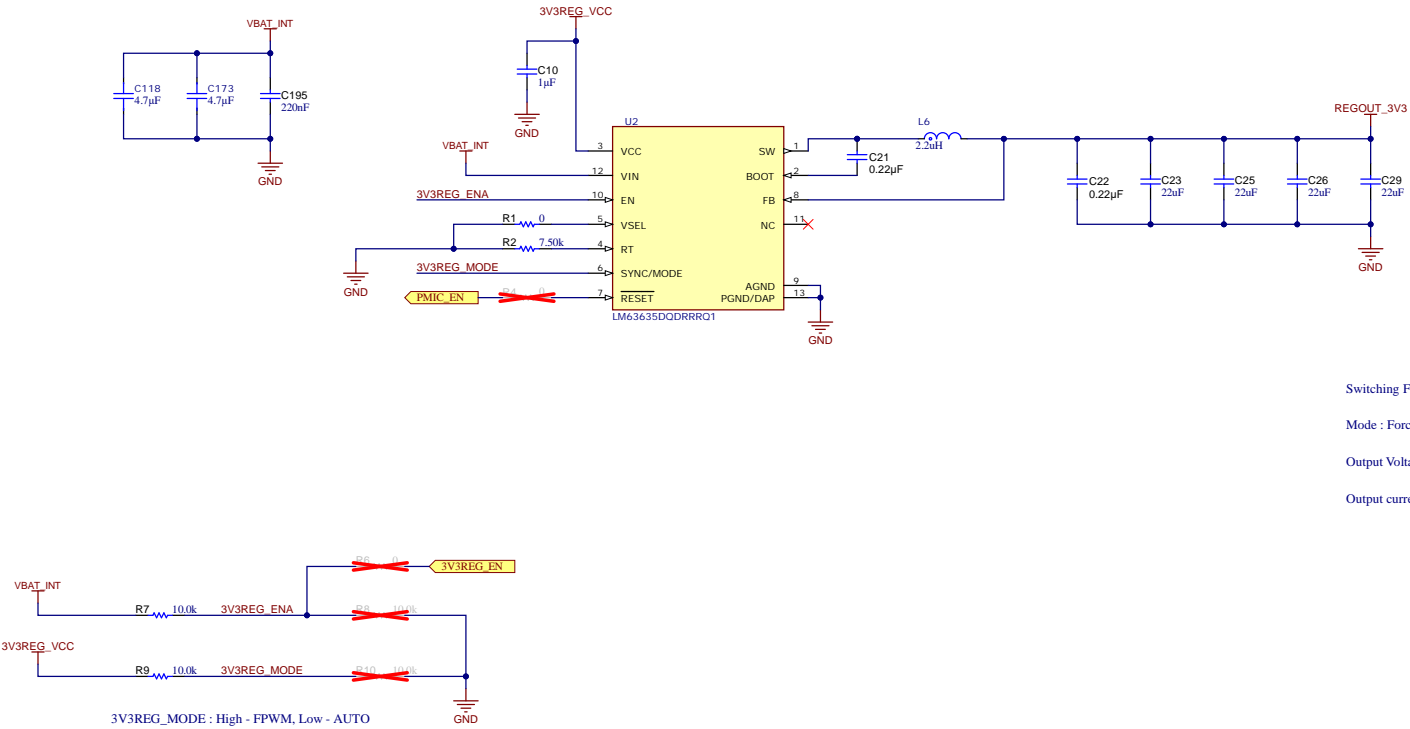


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Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

References

3V3 SUPPLY REFERENCE



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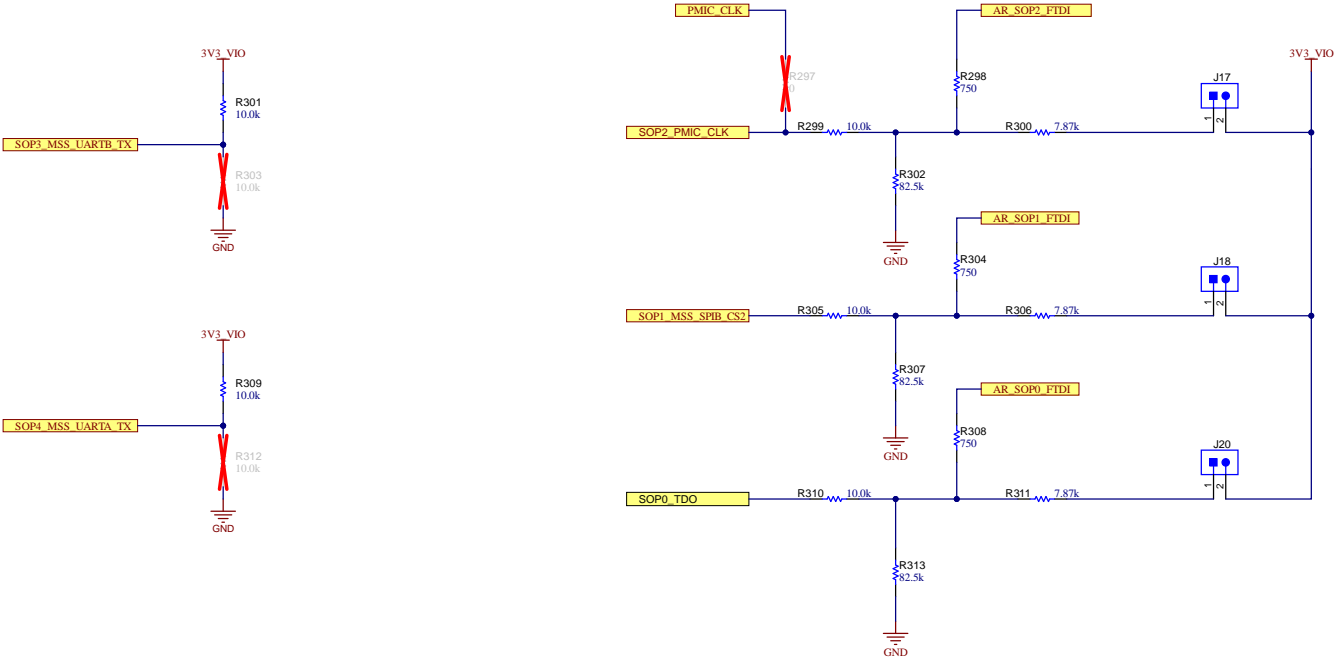
SOP REFERENCE

XTAL DETECT SOP CONFIG

SOP4, SOP3	
40 MHz	00
45.1584 MHz	01
49.152 MHz	10
50 MHz	11

SOP2, SOP1, SOP0

SOP_MODE1	SCAN/ATPG	010
SOP_MODE2	DEV/FLED/ORBIT	011
SOP_MODE3	THB	000
SOP_MODE4	FUNC	001
SOP_MODE5	DEV MANAGEMENT	101



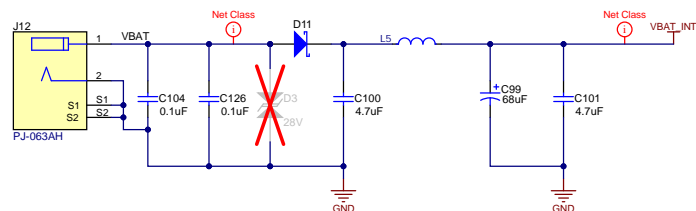
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Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

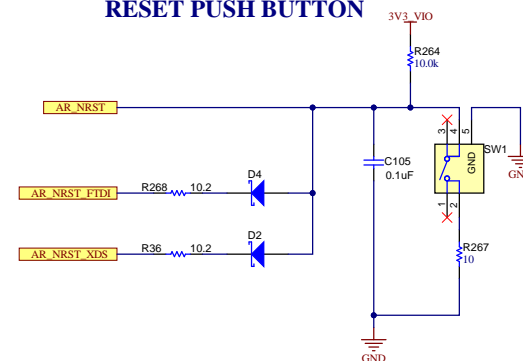


## POWER IN, RESETS, AND LEDS

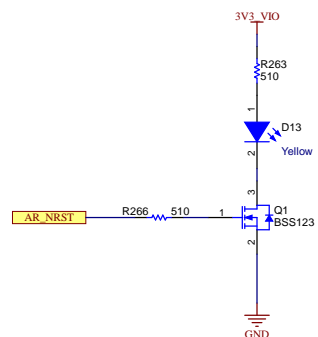
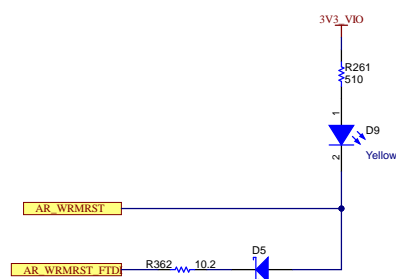
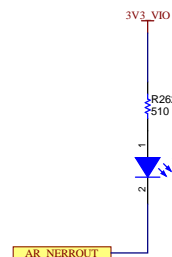
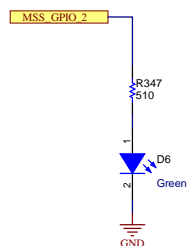
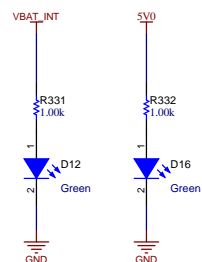
## POWER JACK



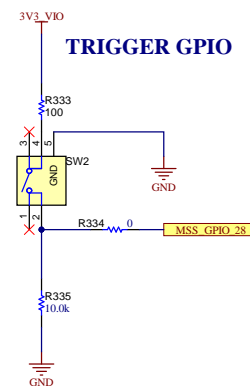
## RESET PUSH BUTTON




## INDICATION LEDS



## TRIGGER GPIO

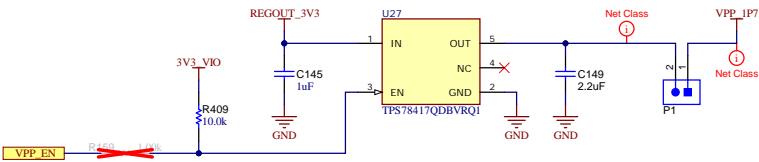


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Engineer: <b>James MURDOCK</b>	Contact: <b>http://www.ti.com/support</b>		© Texas Instruments 2024

References

VPP LDO



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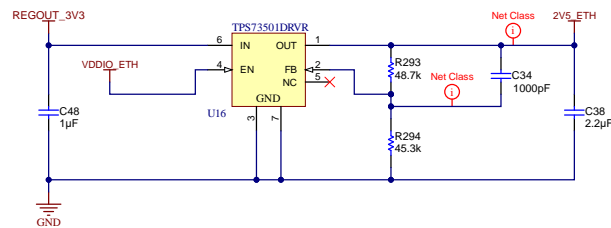
# ETHERNET POWER

## References

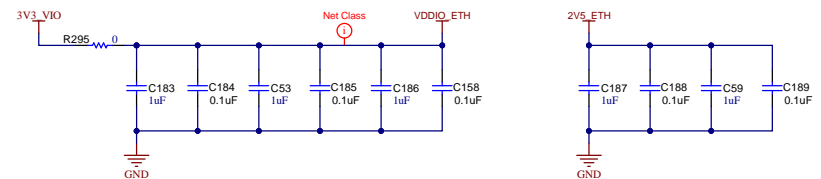
[TPS73501 Datasheet](#)

[TLV733P Datasheet](#)

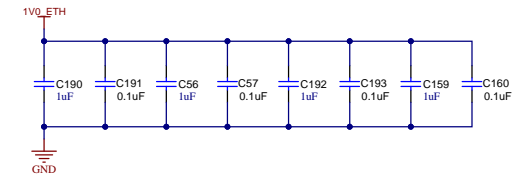
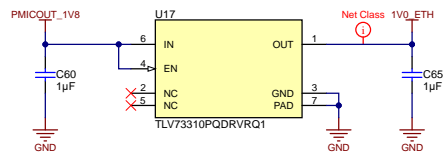
## 2.5V ANALOG SUPPLY



## DECOUPLING CAPS



## 1V ANALOG SUPPLY



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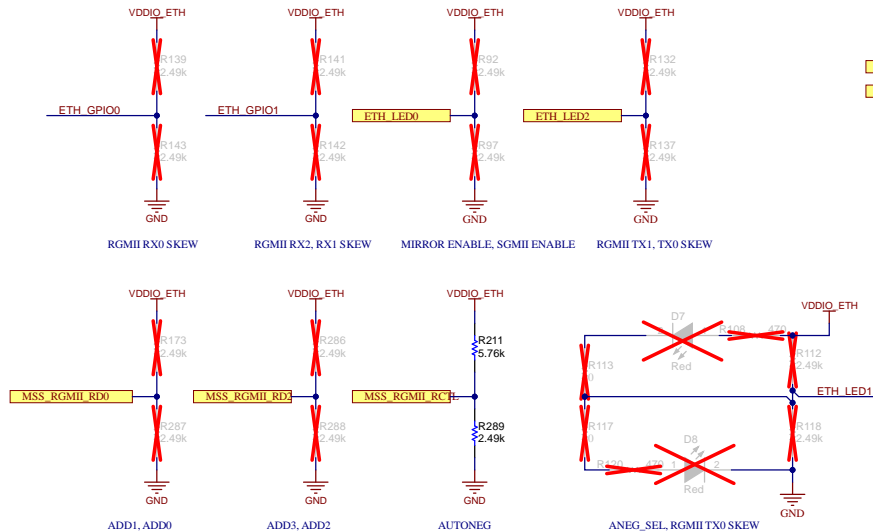
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Drawn By:	File: PROC194A_Ethernet_PWR_SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

## References

[DP83867E Datasheet](#)

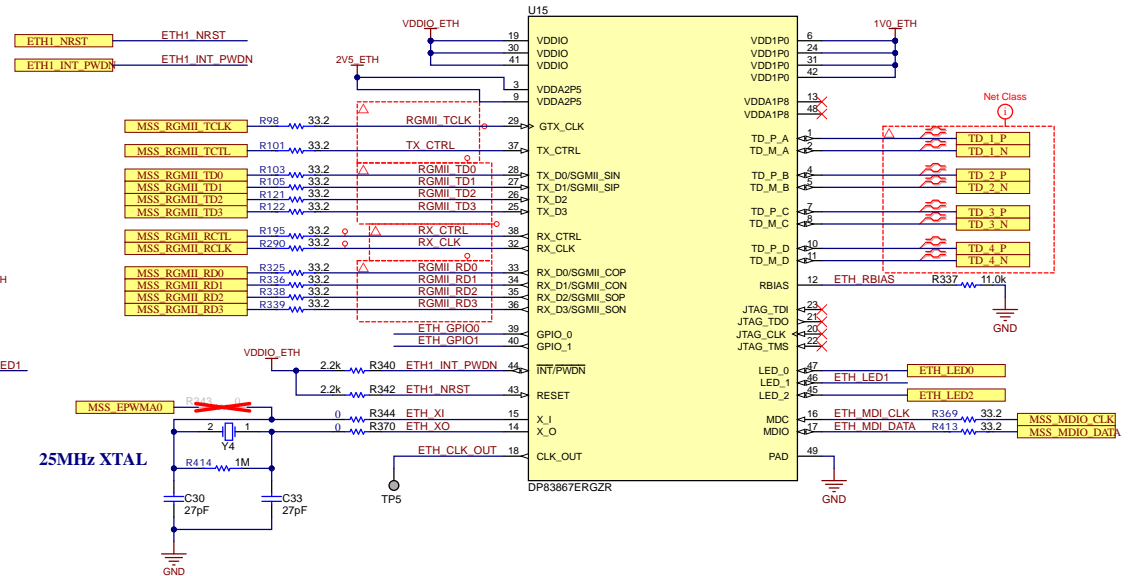
## BOOTSTRAP CONFIGURATION PINS

Resistor Values must be changed to change Modes, refer to datasheet for proper values



DEFAULT CONFIGURATION:  
ADD1, ADD0 = 0  
ADD3, ADD2 = 0  
AUTONEG = 1  
RGMII RX0 SKEW = 0  
RGMII RX2, RX1 SKEW = 0, 0  
RGMII TX1, TX0 SKEW = 0, 0  
ANEG\_SEL, RGMII TX0 SKEW = 0, 0  
MIRROR ENABLE, SGMII ENABLE = 0, 0

## ETHERNET PHY



Place R98, R101, R103, R105, R121 and R122 close to U29

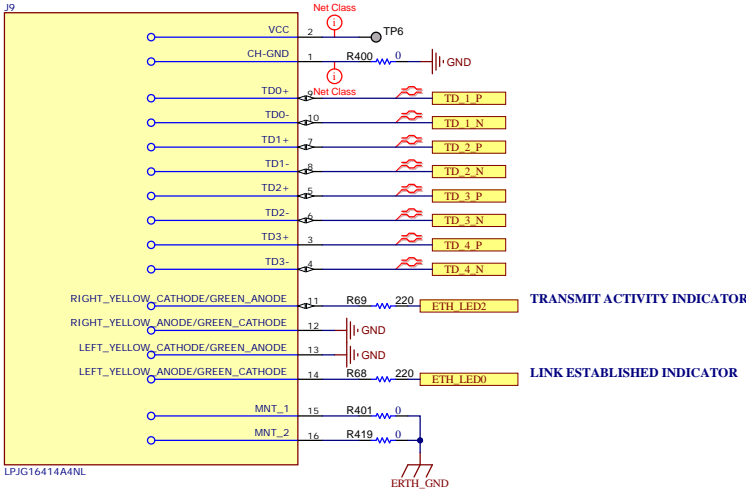
Place R195, R290, R325, R336, R338 and R339 close to U15

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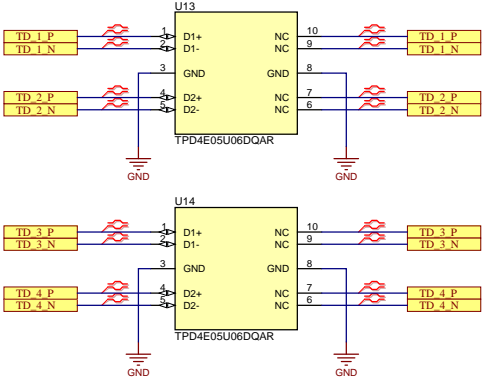
Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 12 of 25
Drawn By:	File: PROC194A_Ethernet_PHY.SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

# ETHERNET MAGNETICS

## RJ45 WITH MAGJACK



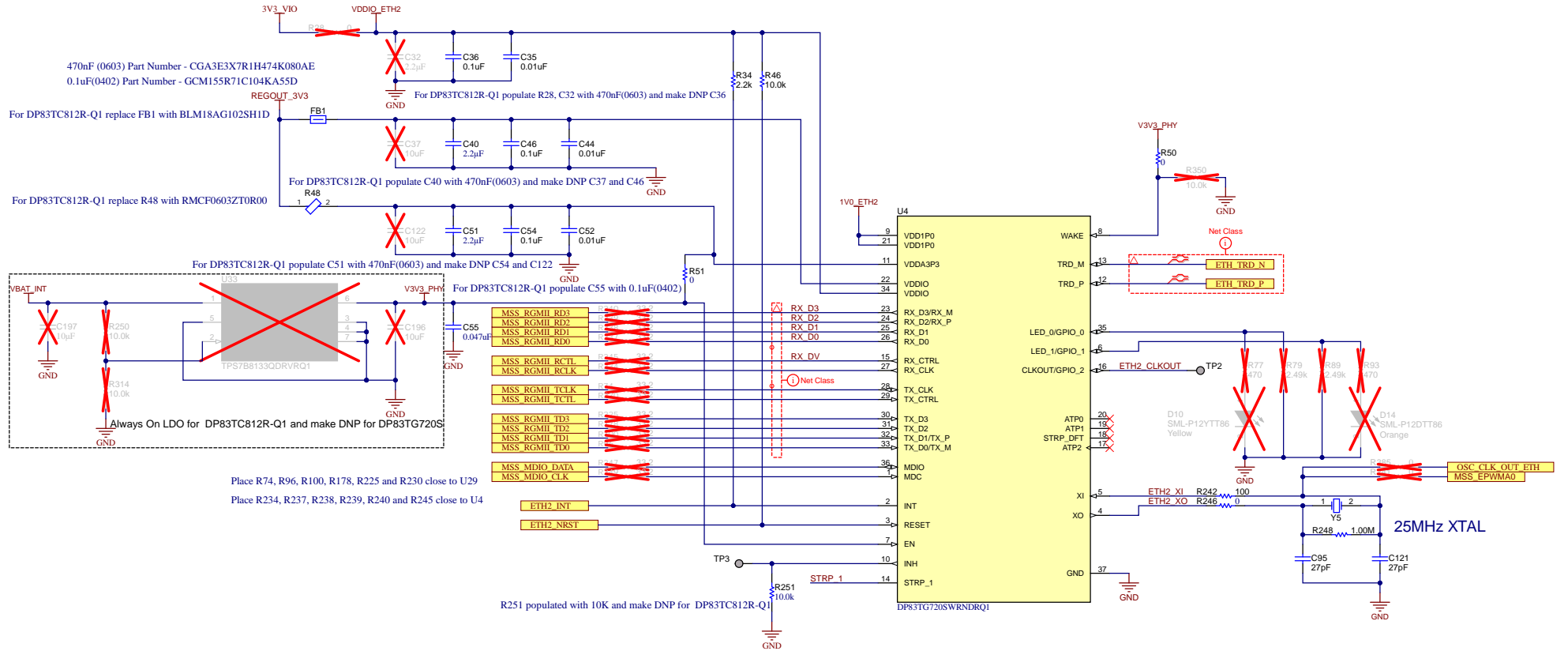
## ETHERNET ESD PROTECTION



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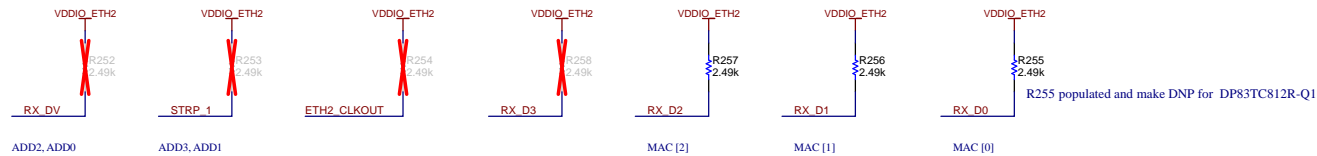
Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 13 of 25
Drawn By:	File: PROC194A_Ethernet_Magnetics.SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

# ETHERNET

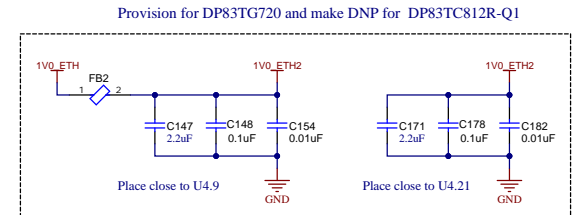


## BOOTSTRAP CONFIGURATION PINS

Resistor Values must be changed to change Modes, refer to datasheet for proper values



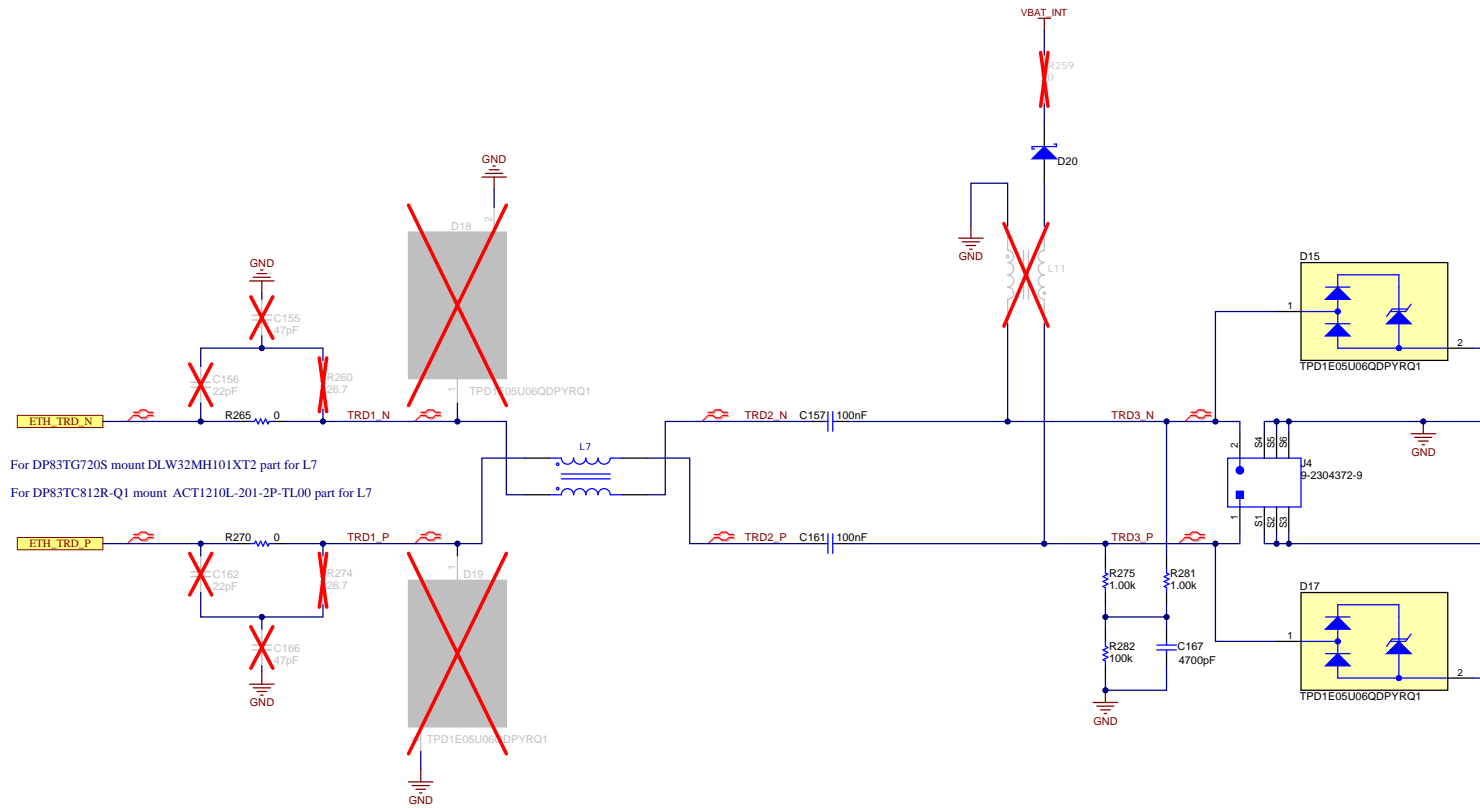
MAC Interface Selection Bootstraps  
MAC[2:0] - 1 1 1 RGMII (RX Shift Mode)



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Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 14 of 25
Drawn By:	File: PROC194A_Auto_Ethernet_PHY.SchDoc	Size: B
Engineer: James MURDOCK	Contact: http://www.ti.com/support	

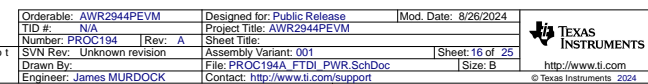
# ETHERNET CONNECTOR



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Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 15 of 25
Drawn By:	File: PROC194A_Auto_Ethernet_conn.SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

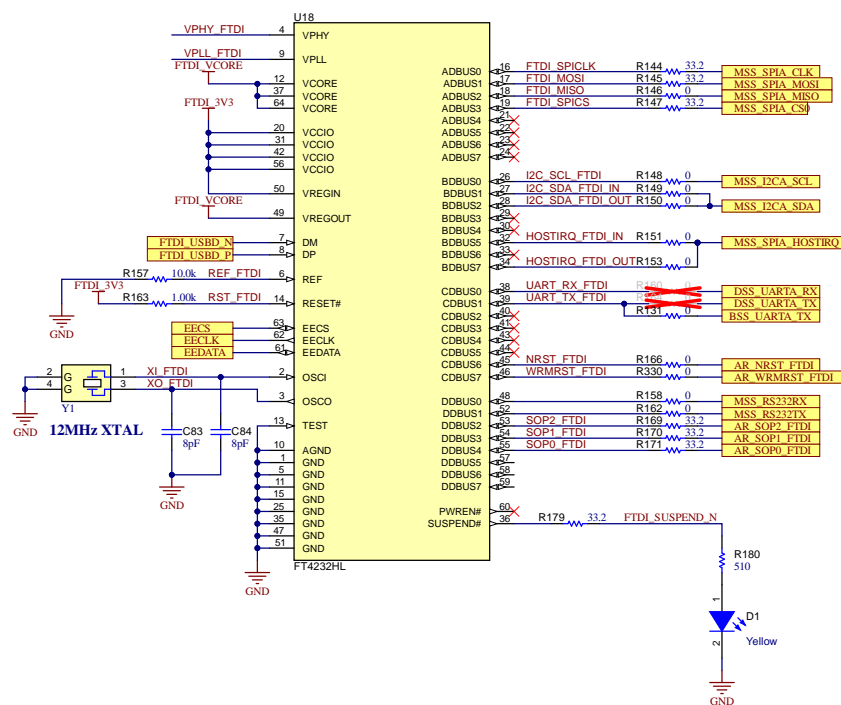
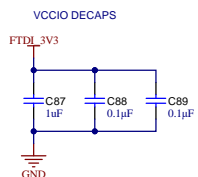
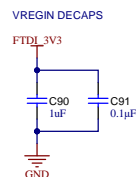
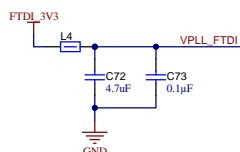
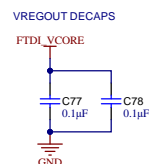
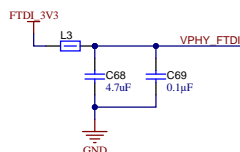
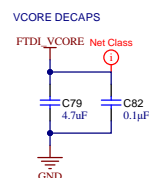
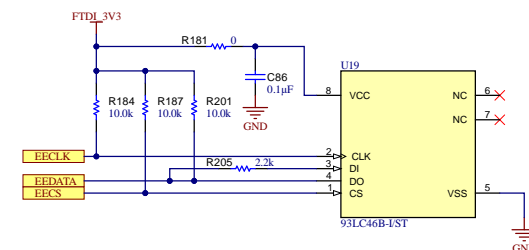
### 3.3V LDO FOR FTDI






## FT4232H Datasheet

## FTDI EEPROM



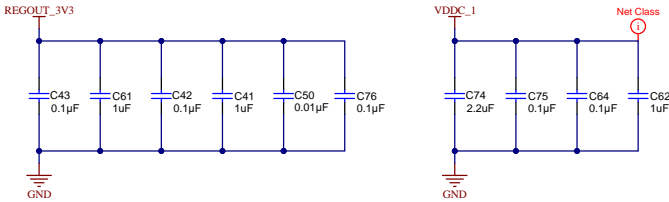
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TID:	Project TIVA: AWR2944PEVM		
Num: PROC194   Rev: A	Sheet Title:		
SVN Rev: Unknown revision	Assembly Variant: 001	[Sheet: 17 of 25]	
Drawn By:	File: PROC194A_FTDI.SchDoc	Size: B	
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

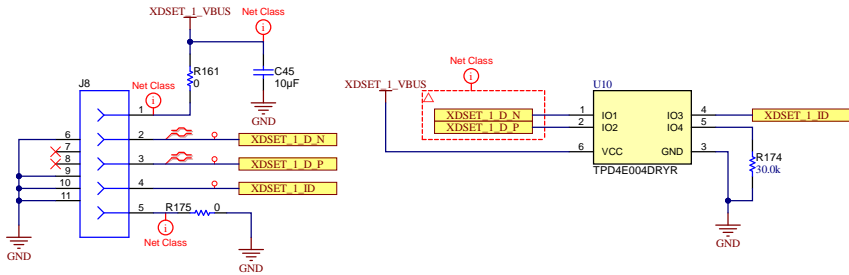


XDS110(1/2)

XDS110 DECOUPLING CAPS




XDS110 USB PORT



## XDS110(2/2)

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Order: <a href="#">AWR2944PEVM</a>		Designed for: <a href="#">Public Release</a>		Mod. Date: 8/26/2024		 <b>TEXAS</b> <b>INSTRUMENTS</b>
TID #: <a href="#">N/A</a>		Project Title: <a href="#">AWR2944PEVM</a>				
Part No: <a href="#">PROC194</a>		Rev: <a href="#">A</a>		Sheet Title:		
SYN Rev: Unknown revision		Assembly Variant: 001		Sheet: 19 of 25		
Drawn by:		File: <a href="#">PROC194_4_XDS110 Interface_1B_SchDoc</a>		Size: B		
Engineer: <a href="#">James MURDOCK</a>		Contact: <a href="#">http://www.ti.com/support</a>				<a href="#">http://www.ti.com</a> © Texas Instruments 2024

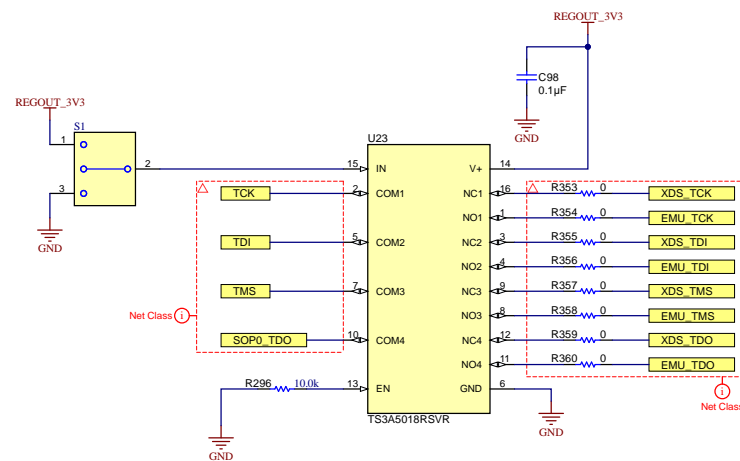
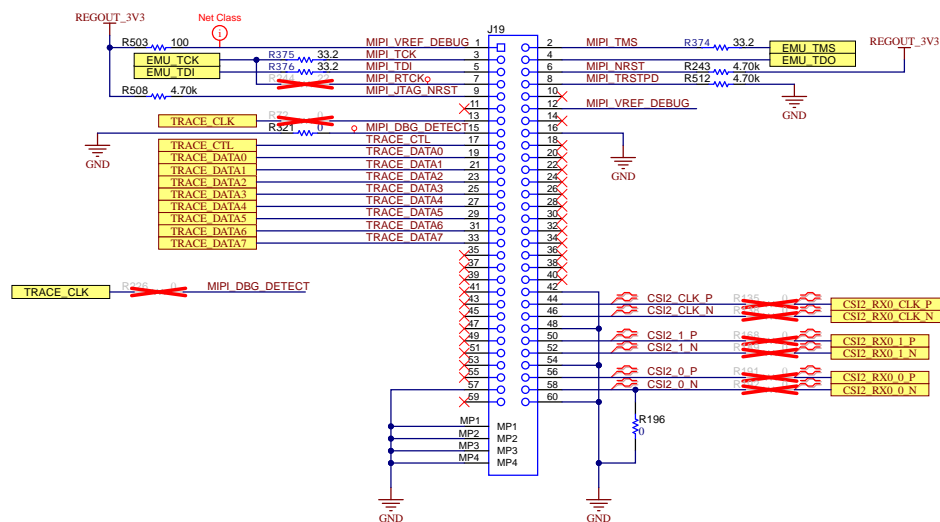
## References

[EMULATION AND TRACE HEADERS](#)  
[XDS560v2 EMULATOR](#)

## MIPI 60 PIN HEADER

NOTE: DEFAULT CONFIGURATION IS FOR MIPI 60 PIN EMULATOR

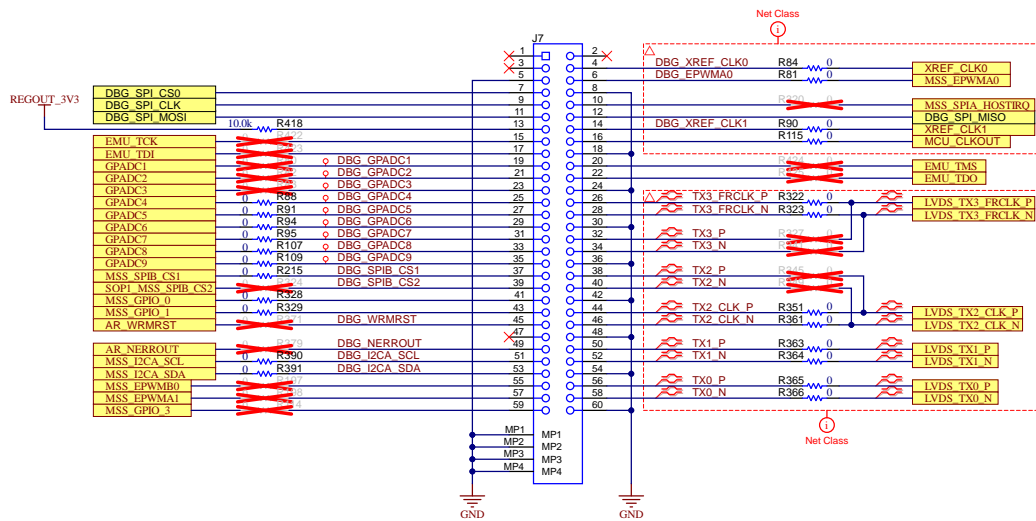
## JTAG MUX BETWEEN XDS110 AND MIPI 60 PIN



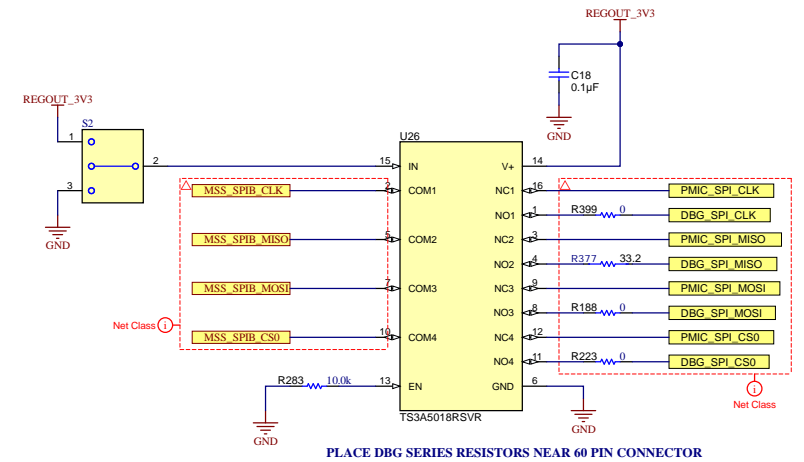
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Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 20 of 25
Drawn By:	File: PROC194A_JTAG_EMU_Connector.SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

# 60 PIN DEBUG CONNECTOR



## SPI MUX BETWEEN PMIC AND 60 PIN DEBUG CONNECTOR



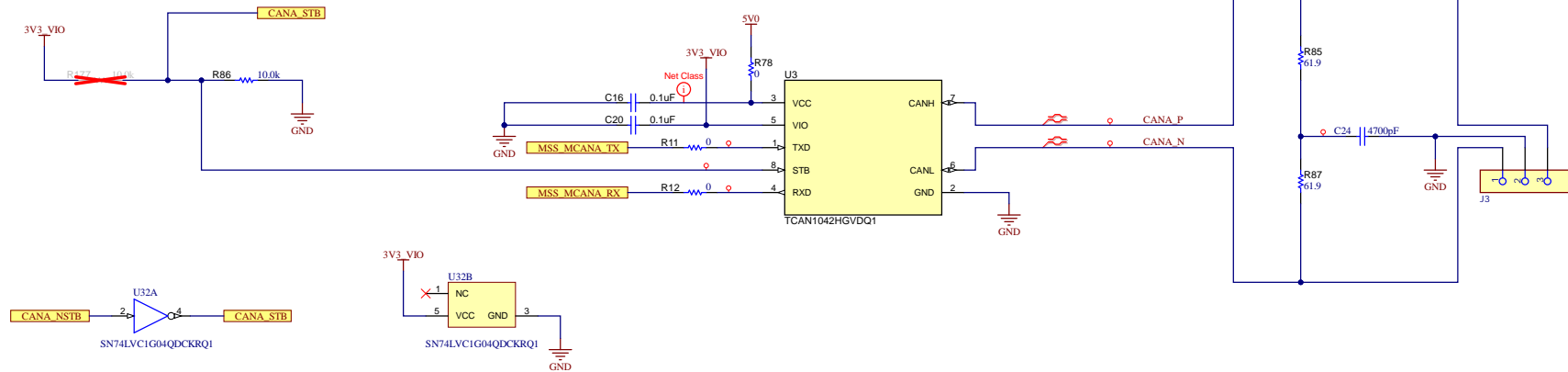
PLACE DBG SERIES RESISTORS NEAR 60 PIN CONNECTOR


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Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 21 of 25
Drawn By:	File: PROC194A_Debug_Connector_SchDoc	Size: B
Engineer: James MURDOCK	Contact: http://www.ti.com/support	http://www.ti.com

## References

CAN\_A PHY

[illegible]

Orderable: <b>AWR2944PEVM</b>	Designed for: <b>Public Release</b>	Mod. Date: <b>8/26/2024</b>	 <b>TEXAS INSTRUMENTS</b>  <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2024
TID #: <b>N/A</b>	Project Title: <b>AWR2944PEVM</b>		
Number: <b>PROC194</b>	Rev: <b>A</b>	Sheet Title:	
SVN Rev:	Assembly Variant: <b>001</b>	Sheet: <b>22</b> of <b>25</b>	
Drawn By:	File: <b>PROC194A_Can Interface.SchDoc</b>	Size: <b>B</b>	
Engineer: <b>James MURDOCK</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

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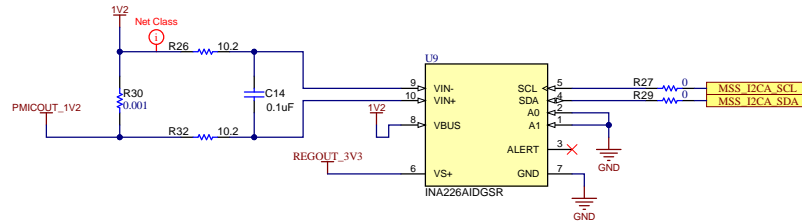
## CURRENT SENSORS

### References

[INA226 Datasheet](#)

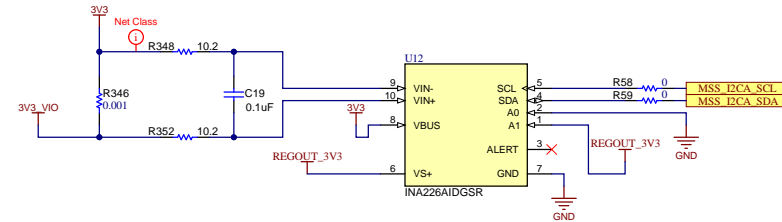
#### 1.2V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x40



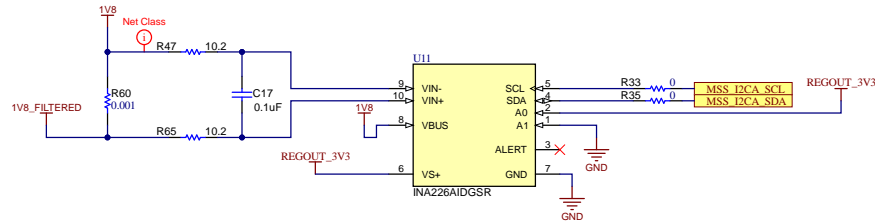
#### 3.3V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x44



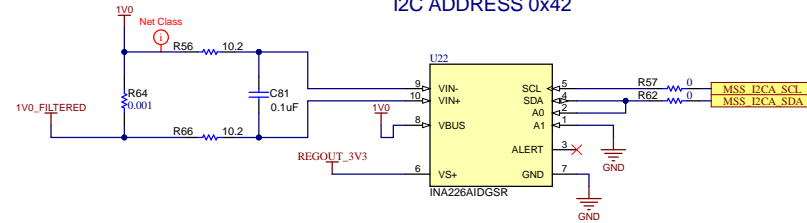
#### 1.8V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x41



#### 1.0V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x42



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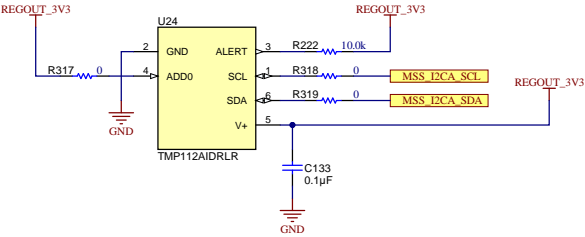
Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev:	Assembly Variant: 001	Sheet: 23 of 25
Drawn By:	File: PROC194A_Current_Sensors.SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

References

[TMP112 Datasheet](#)

TEMP SENSOR

I2C ADDRESS 0x49



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Orderable: AWR2944PEVM	Designed for: Public Release	Mod. Date: 8/26/2024
TID #: N/A	Project Title: AWR2944PEVM	
Number: PROC194	Rev: A	Sheet Title:
SVN Rev:	Assembly Variant: 001	Sheet: 24 of 25
Drawn By:	File: PROC194A_Temp_Sensor.SchDoc	Size: B
Engineer: James MURDOCK	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>





