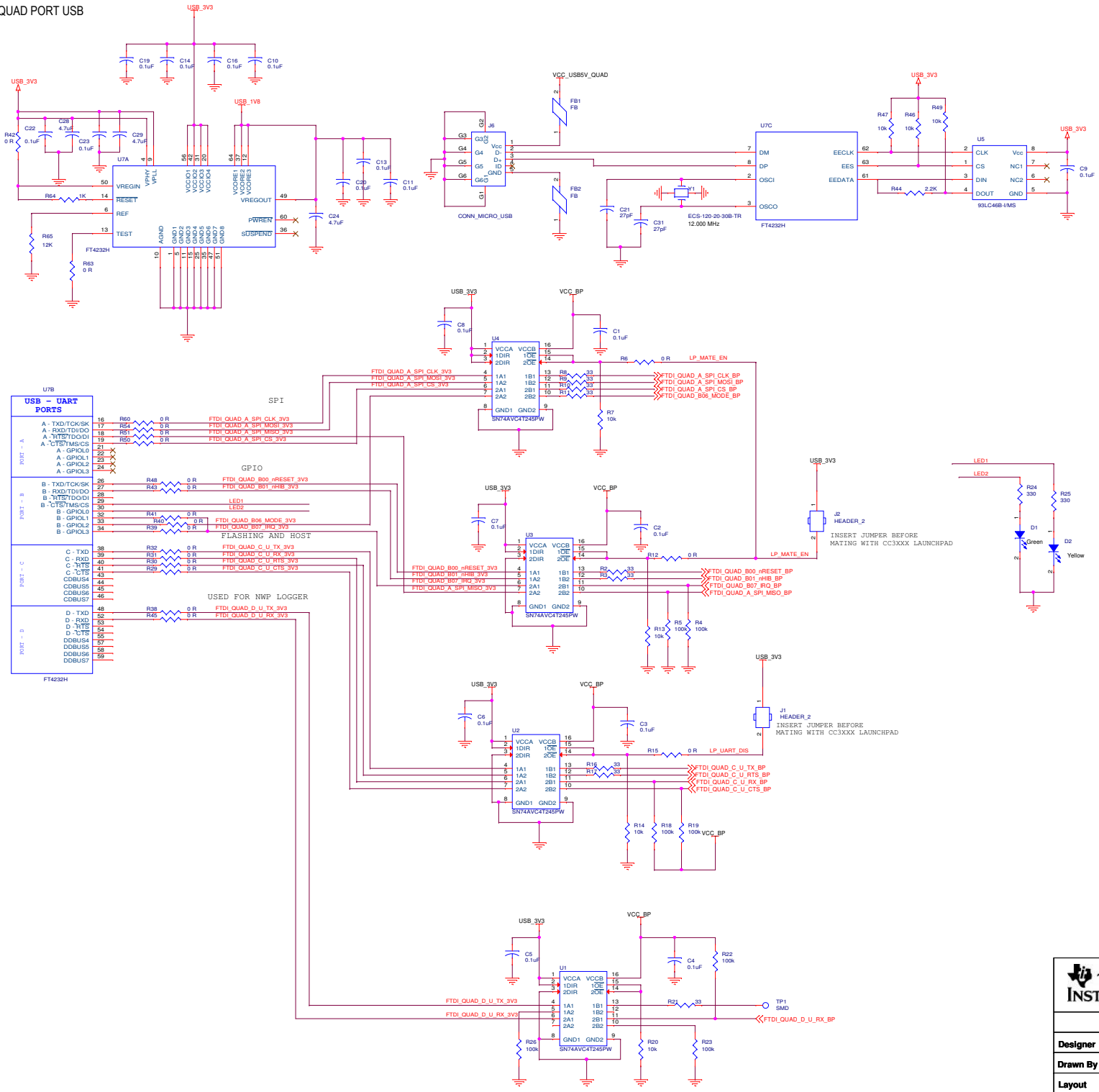


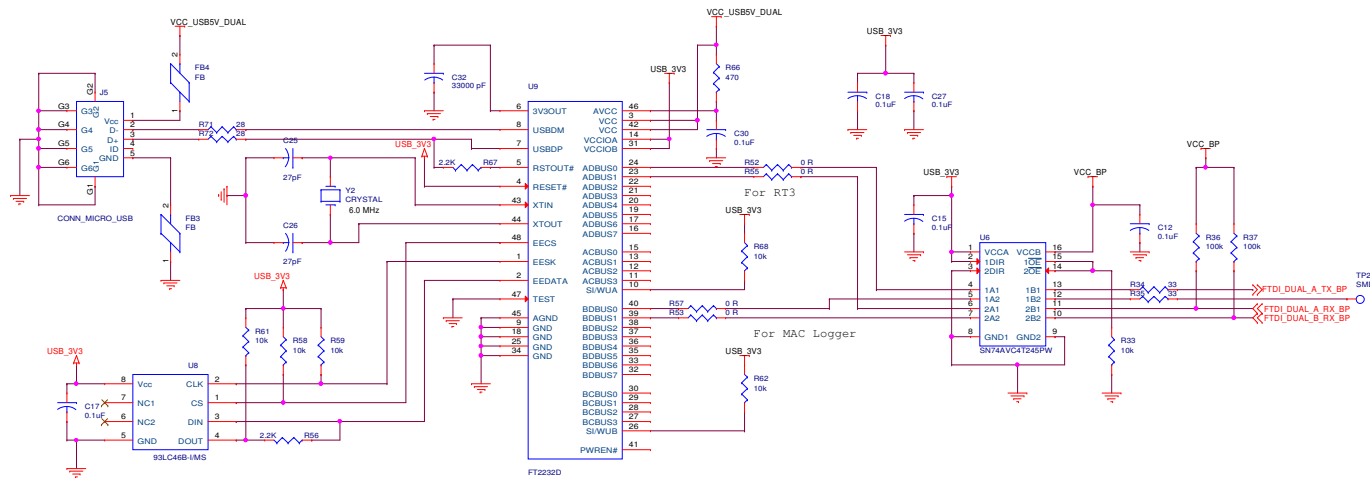
Rev conrol
=====
Rev : 3.0-A
Changes compared to Rev 2.0-A
1. Swapped the SCK/CS lines for the dual FTDI (bug in design).
2. Exchanged NWP port with the RS232. Now NWP port is avaiillable on Quad UART Port-4.
3. Added option to route the NWP logger to Dual UART on LP boards.
4. Added option to flash the CC3100BP while mated on MCU LPs.
5. Added jumper to disconnect 5V when connected with LP
6. Disconnected the UART for RT3 by default using DNP resistors

QUAD PORT USB

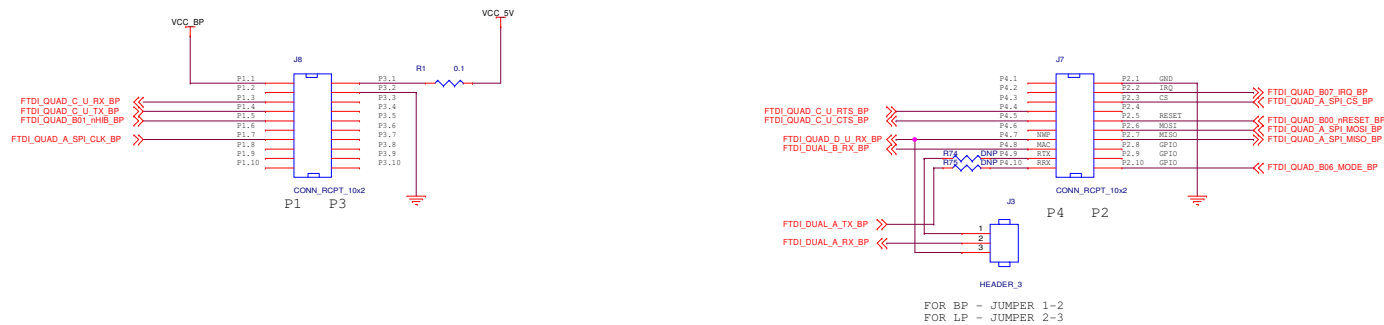


			Texas Instruments 66/3 Bagmane Tech Park CV Raman Nagar Bangalore		
	Approval	Date	Title CC31XX EMUBOOST		
Designer	TI	Date	Size A2	Project No <Project Number>	Rev 3.0-A
Drawn By	TI	Date	Scale	Date Wednesday, April 16, 2014	Sheet 1 of 3
Layout	TI	Date			

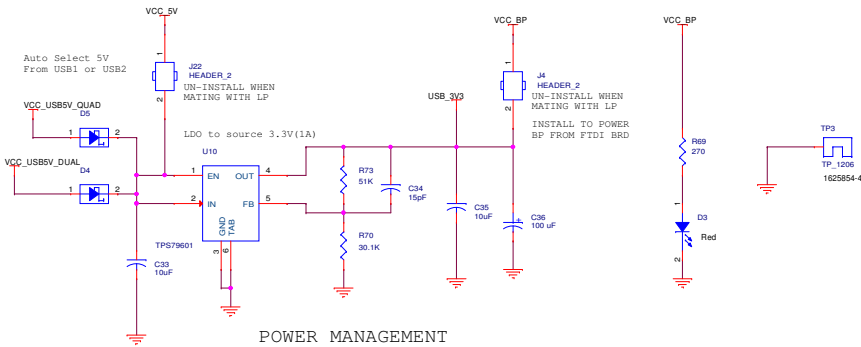
DUAL PORT USB



BOOSTERPACK INTERFACE



POWER SECTION



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	Approval	Date	Title CC31XX EMUBOOST		
Designer	TI	Date			
Drawn By	TI	Date	Size A2	Project No <Project Number>	Rev 3.0-A
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