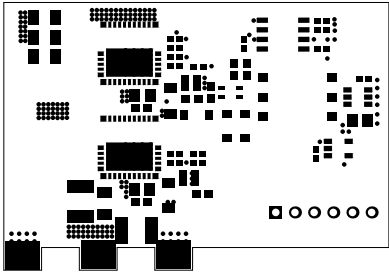
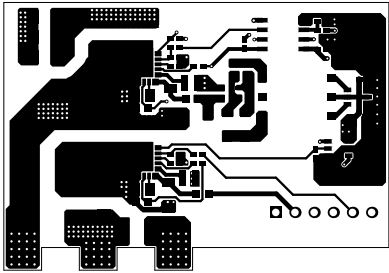


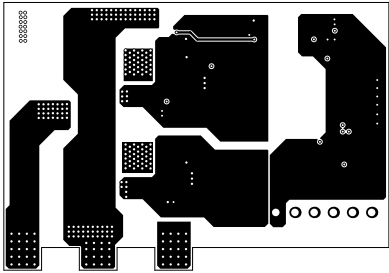
	BOARD #: PMP40690_6aN	REV: E1	SUN REV: Not In VersionControl
PLOT NAME = Top Overlay	GENERATED : 11/4/2020 4:49:51 PM		TEXAS INSTRUMENTS



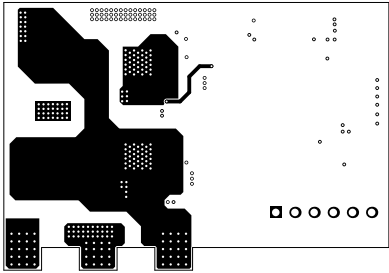
	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
Top Solder						
PLOT NAME = Top Solder Mask		GENERATED : 11/4/2020 4:49:51 PM			TEXAS INSTRUMENTS	



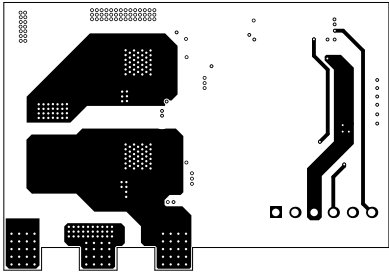
	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
PLOT NAME =	Top Layer	GENERATED	:	11/4/2020	4:49:51 PM	TEXAS INSTRUMENTS



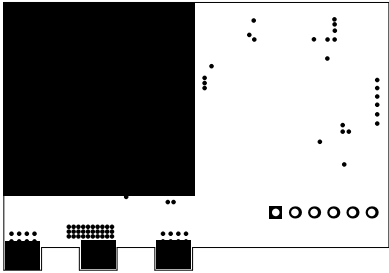
	BOARD #:	PMP40690_Ga	REV: E1	SUN REV: Not In VersionControl
PLOT NAME =	Signal Layer 1	GENERATED	: 11/4/2020 4:49:51 PM	TEXAS INSTRUMENTS



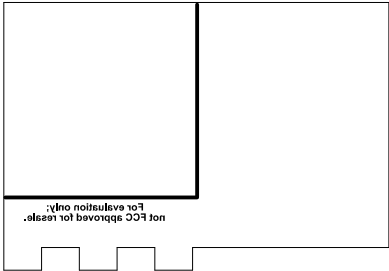
	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
PLOT NAME =	Signal Layer 2	GENERATED	:	11/4/2020	4:49:51 PM	TEXAS INSTRUMENTS



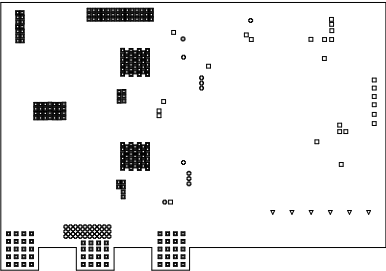
	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
PLOT NAME =	Bottom Layer	GENERATED	:	11/4/2020	4:49:51 PM	TEXAS INSTRUMENTS



	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
Bottom Solder						
PLOT NAME = Bottom Solder Mask		GENERATED : 11/4/2020 4:49:51 PM			TEXAS INSTRUMENTS	

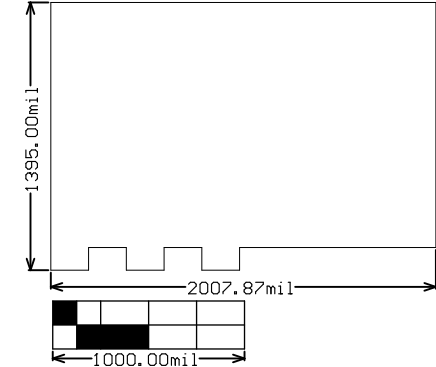


	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
PLOT NAME =	Bottom Overlay	GENERATED	:	11/4/2020	4:49:51 PM	TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Tolerance
□	26	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	41	12.00mil (0.305mm)	PTH	Round	Top Layer - Bottom Layer	
■	230	12.10mil (0.307mm)	PTH	Round	Top Layer - Bottom Layer	
▽	6	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
303 Total						

	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
Drill Drawing						
PLOT NAME = Drill Drawing		GENERATED	:	11/4/2020	4: 49: 51 PM	TEXAS INSTRUMENTS



	BOARD #:	PMP40690_Ga	REV:	E1	SUN REV:	Not In VersionControl
M2 Board Dimensions						
PLOT NAME =	Board Dimensions		GENERATED	: 11/4/2020	4:49:52 PM	TEXAS INSTRUMENTS