



Layer Stack Up Detail for: TIDA-00181.PcbDoc			
Layer	Material	Thickness	Notes
Top Solder Mask	Cu/Sr	Refines	Solder Resist
Top Layer	Cu/SL	1-mil	FS-4
Pre-Layer1	Cu/SL	1-mil	FS-4
Pre-Layer2	Cu/SL	1-mil	FS-4
Bottom Layer	Cu/SL	1-mil	FS-4
Bottom Solder Mask	Cu/SL	Refines	Solder Resist

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
100 MM X 145 MM

Number of Layers : 4
MIN. TRACK WIDTH: 10 MIL
MIN. CLEARANCE: 7.87 MIL
MIN. VIA PAD SIZE: 30 MIL

MINIMUM ANNULAR RING 0.177mm (7MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
☒ FR-408 ☐ FR-4 High Tg ☐ OTHER

THICKNESS: ☒ 62 MIL (1.6mm) +/-10% ☐ OTHER

TOLERANCE: ☒ ANSI IPC-6012 TYPE 3 CLASS 2
☐ OTHER +/-

BOW & TWIST: ☒ ANSI IPC-6012 TYPE 3 CLASS 2
☐ OTHER +/-

COPPER THICKNESS (FINISHED):
OUTER: ☒ 1.4MIL (1oz) ☐ 2MIL (1.4oz) ☐ 2.8MIL (2oz)
INNER SIGNAL: ☒ 1.4MIL (1oz) ☐ 2.8MIL (2oz) ☐ N/A

DRILLING:
REFERENCE: ☒ AS SHOWN ☒ NC_DRILL FILES
PTH MIN COPPER THICKNESS: ☒ 1MIL ☐ OTHER

BOARD FINISH:
SILKSCREEN: ☒ TOP ☒ BOTTOM
SILKSCREEN COLOR: ☒ WHITE ☐ OTHER

SOLDER RESIST COLOR:
☒ GREEN ☐ BLUE ☐ OTHER

SURFACE FINISH: ☒ IMMERSION GOLD (ENIG) ☐ ENEPIG
☐ MM. TN/SILVER OR EQUIV ☐ OTHER

ARRAY/PANEL: ☐ CUT AND TRIM PER MECH LAYER 1
☐ N.C. ROUTE ☒ V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:
☒ ANSI IPC-A-600F CLASS -> ☐ 1 ☒ 2 ☐ 3
☒ UL 94V-0 ☒ RoHS ☐ OTHER PER ORDER

ADDITIONAL REQUIREMENTS:
MICROSECTION: ☐ YES

BARE BOARD ELEC. TEST: ☐ NONE ☒ REQUIRED ☐ PER ORDER

MANUFACTURER'S UL: ☐ RAL ☐ METAL ☒ SLK

Symbol	Hit Count	Tool Size	Plated	Hole Type
B	23	16mil (0.406mm)	PTH	Round
A	240	20mil (0.508mm)	PTH	Round
C	12	38mil (0.965mm)	PTH	Round
F	26	44mil (1.118mm)	PTH	Round
D	30	51.968mil (1.32mm)	PTH	Round
E	2	55.118mil (1.4mm)	PTH	Round
G	4	125.984mil (3.2mm)	PTH	Round
337 Total				

Drill Table
TOLERANCE FOR 16MIL DRILL +0/-16MIL



PROJECT TITLE:
2 Output Push-Pull Gate Drive Power Supply

DESIGNED FOR:
Public Release

FILE NAME:
TIDA-00181.PcbDoc

ENGINEER:
Sanjay Pithadia

LAYOUT BY:
Sharanu

SCALE: 0.67

ALTIM DESIGNER VERSION:
10.0.0.27009

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00181	REV: E2	SUN REV: Not In VersionControl
LAYER NAME = Drill Dimensions			
PLOT NAME = Fabrication Drawing	GENERATED : 5/16/2014 6:30:13 PM	TEXAS INSTRUMENTS	

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