

	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

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Number: TIDA-00440	Rev: E1	Designed for: Public Release	Mod. Date: 2/13/2015
SVN Rev.: Not in version control		Project Title: Insulation Monitoring Circuit	
Sheet Title:			
Assembly Variant: Variant name not interpreted	Sheet 1 of 5		
Drawn By: Sanjay Pithadia		File: Pst1-Cover.SchDoc	Size: B
Engineer: Sanjay Pithadia		Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

100 - 850Vdc

500Vdc

# Daughter Card (500V Output)

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Number: TIDA-00440 | Rev: E1  
 SVN Rev: Not in version control  
 Drawn By: Sanjay Pithadia  
 Engineer: Sanjay Pithadia

Designed for: Public Release | Mod. Date: 2/18/2015  
 Project Title: Insulation Monitoring Circuit  
 Sheet Title: Daughter Board  
 Assembly Variant: Variant name not interpreted  
 File: Ptd\_SchDoc | Size: B  
 Contact: <http://www.ti.com/support>

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	1	2	3	4	5	6
A						
B						
C						
D						

PCB Number: TIDA-00440  
PCB Rev: E1

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.