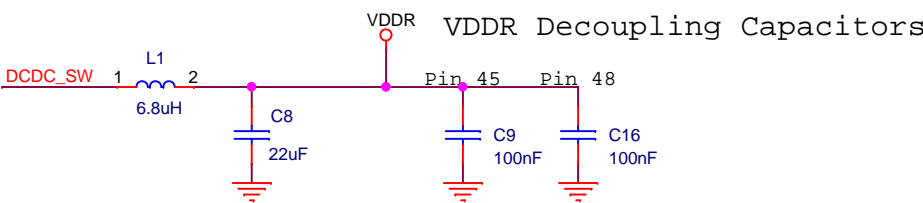
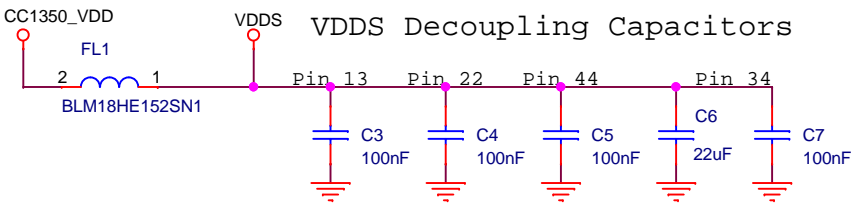


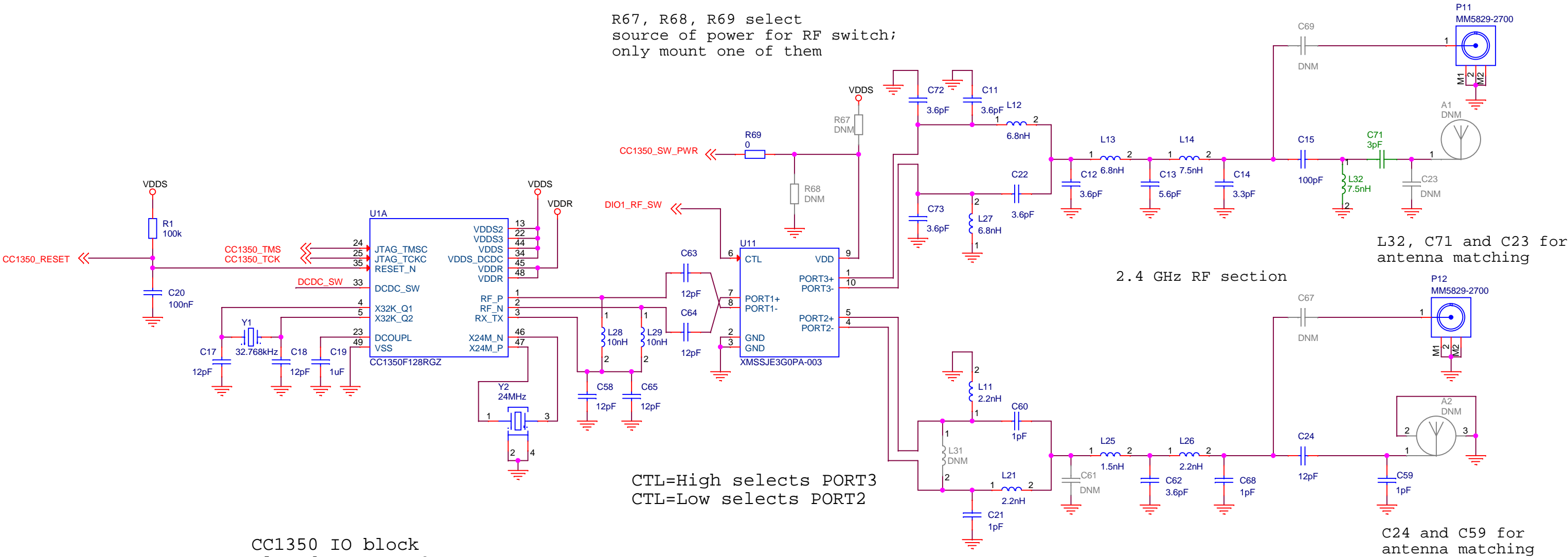
CC1350 RF



Place L1 and C8 close to pin 33

Sub-1 GHz RF section

R67, R68, R69 select source of power for RF switch; only mount one of them



# BoosterPack Headers and Peripherals

The diagram illustrates the internal connections of the CC1350 IO block (U1B) to various headers and peripherals. The CC1350 IO block (U1B) is connected to the CC1350\_VDD and CC1350\_GND pins. The external flash (U4) is connected to the CC1350\_VDD and CC1350\_GND pins. The 5V and 3V headers (P1, P2) are connected to the CC1350\_VDD and CC1350\_GND pins. The diagram also shows the connections for the external flash (U4) and the 5V and 3V headers (P1, P2).

**CC1350 IO block (U1B) Pin Connections:**

Pin	Signal
1	DIO1_RF_SW
2	DIO2_RXD
3	DIO3_TXD
4	DIO4_SCL
5	DIO5_SDA
6	DIO6_RLED
7	DIO7_GLED
8	DIO8_MISO
9	DIO9_MOSI
10	DIO10_SCLK
11	DIO11_CS
12	DIO12
13	DIO13_BTN1
14	DIO14_BTN2
15	DIO15
16	DIO16_TDO
17	DIO17_TDI
18	DIO18_RTS/SWO
19	DIO19_CTS
20	DIO20_Flash_CS
21	DIO21
22	DIO22
23	DIO23_Analog
24	DIO24_Analog
25	DIO25_Analog
26	DIO26_Analog
27	DIO27_Analog
28	DIO28_Analog
29	DIO29_Analog
30	DIO30_Analog

**External flash (U4) Pin Connections:**

Pin	Signal
1	SI/SIO0
2	SCLK
3	nCS
4	nWP/SIO2
5	nRST/SIO3
6	SO/SIO1
7	GND
8	EGP

**5V and 3V header (P1, P2) Pin Connections:**

Pin	Signal
1	CC1350_VDD
2	5V
3	3V

**Jumpers M13-M14 to be mounted on header P6**

**External flash (U4) Pin Connections:**

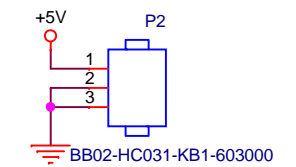
Pin	Signal
1	SI/SIO0
2	SCLK
3	nCS
4	nWP/SIO2
5	nRST/SIO3
6	SO/SIO1
7	GND
8	EGP

**External flash (U4) Pin Connections:**

Pin	Signal
1	SI/SIO0
2	SCLK
3	nCS
4	nWP/SIO2
5	nRST/SIO3
6	SO/SIO1
7	GND
8	EGP

**External flash (U4) Pin Connections:**

Pin	Signal
1	SI/SIO0
2	SCLK
3	nCS
4	nWP/SIO2
5	nRST/SIO3
6	SO/SIO1
7	GND
8	EGP

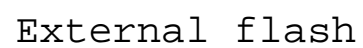



M13M14

P6

2 1  
4 3

BB02-HJ041-KB1-603000



Title: <b>LAUNCHXL-CC1350US</b>		 <b>TEXAS INSTRUMENTS</b>	
Drawn: <b>TER</b>			
Checked: <b>NN</b>			
Size: <b>A3</b>	Rev: <b>1.3.0</b>	Sheet: <b>2 of 5</b>	
Date: <b>Tuesday, November 22, 2016</b>			

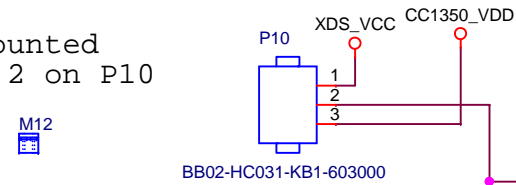
# XDS110 Debugger Interface

P10 selects the voltage source for the level shifters

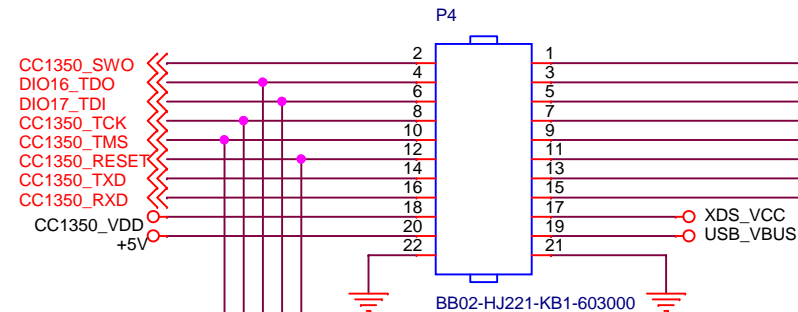
When powering CC1350 from the XDS suppl, connect jumper between pins 1 and 2.

When powering CC1350 from the external supply, connect jumper between pins 2 and 3.

Jumper M12 to be mounted  
between pins 1 and 2 on P10

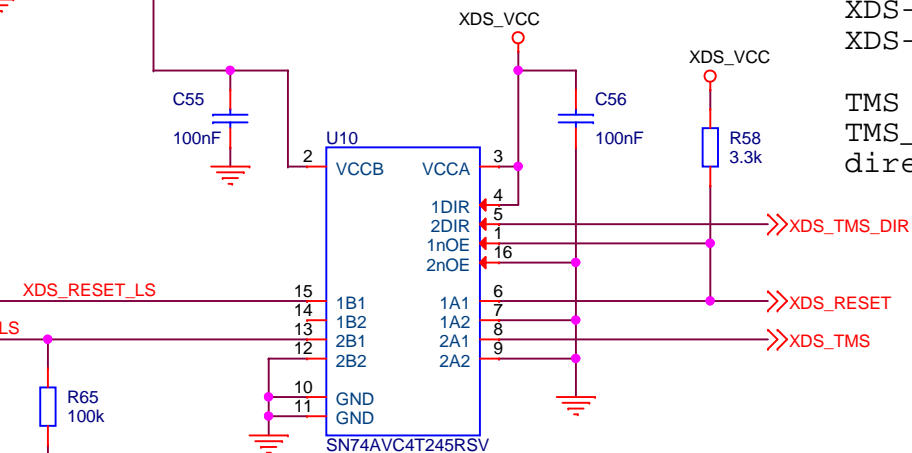
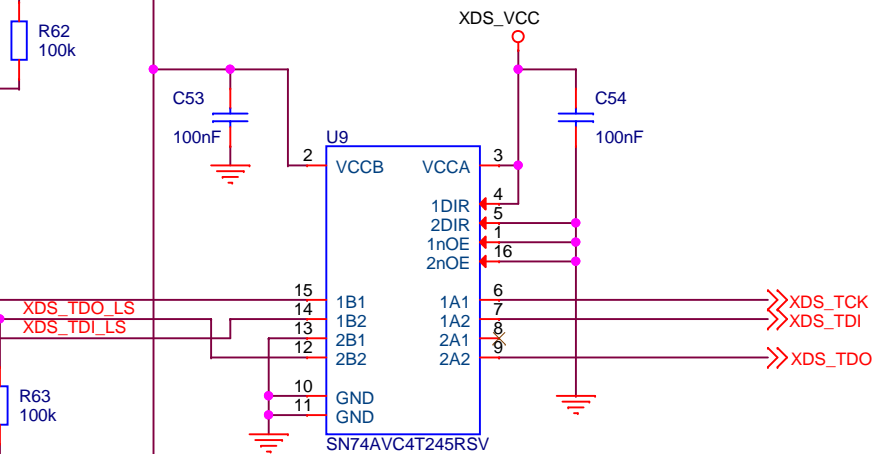
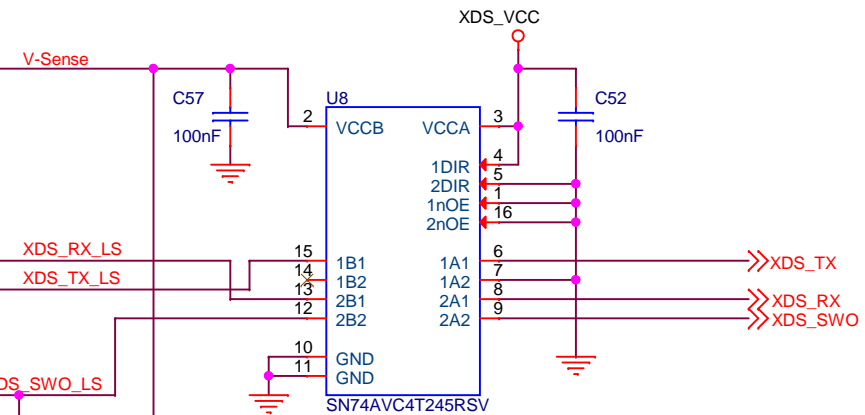
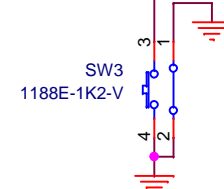
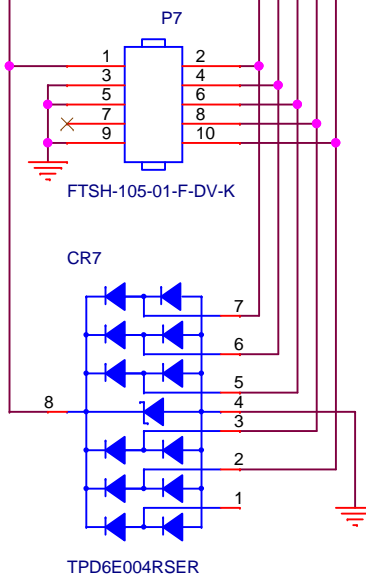
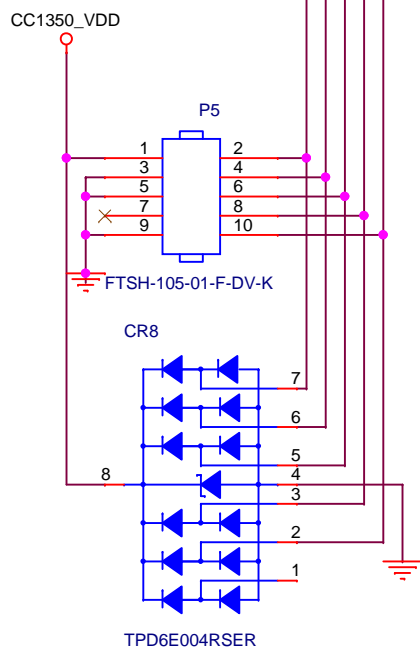


Jumpers M1-M11 to be mounted  
on header P4



Use P5 for debugging  
CC1350 with an  
external debugger  
(requires that all  
jumpers on P4 be  
removed)

Use P7 for debugging  
external targets  
(requires that all  
jumpers on P4 be  
removed)



XDS-RST = 0 -> output = 0  
XDS-RST = 1 -> output = Hi-Z

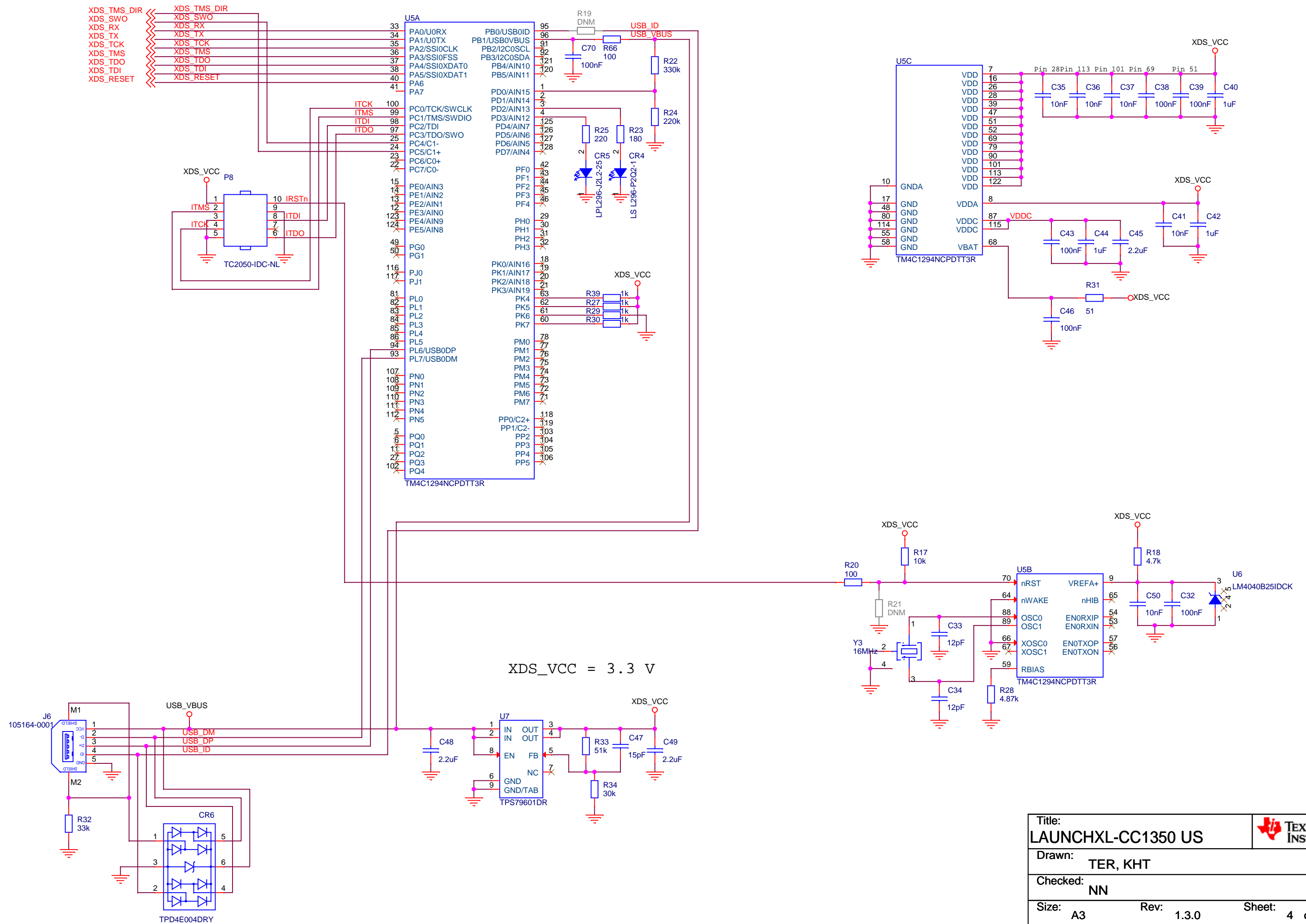
TMS signal is bidirectional.  
TMS\_DIR used to control  
direction of level shifter

DIR = H: A -> B  
DIR = L: B -> A

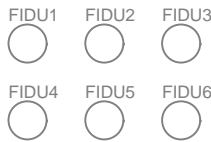
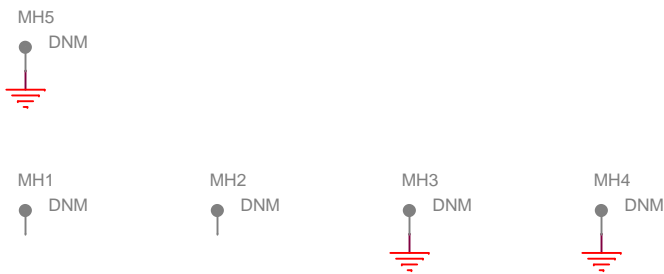
OE = H: output = Hi-Z


Title: <b>LAUNCHXL-CC1350US</b>			
Drawn: <b>TER, KHT</b>			
Checked: <b>NN</b>			
Size: <b>A3</b>	Rev: <b>1.3.0</b>	Sheet: <b>3 of 5</b>	
Date: <b>Tuesday, November 22, 2016</b>			

## XDS110 Debugger



Mechanical



Title: LAUNCHXL-CC1350US		 TEXAS INSTRUMENTS	
Drawn: TER, KHT			
Checked: NN			
Size: A3	Rev: 1.3.0	Sheet: 5 of 5	
Date: Tuesday, November 22, 2016			