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COMPUTER GENERATED DRAWING - DO NOT REVISE MANUALLY			
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	Production Release	02/24/2021	
B	Addition of DLP472NE/TE DMDs	03/01/2024	
C	Updated L502 from 22uH to 4.7uH	05/29/2025	

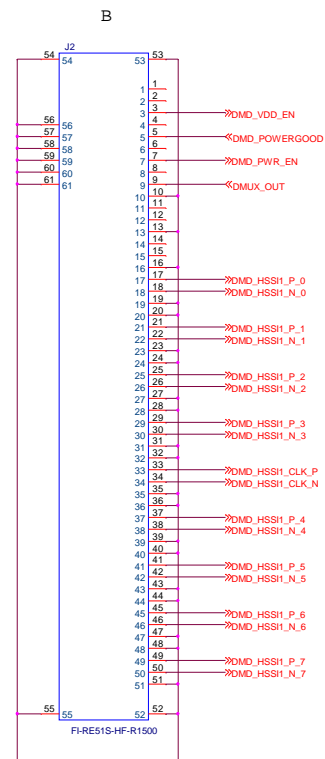
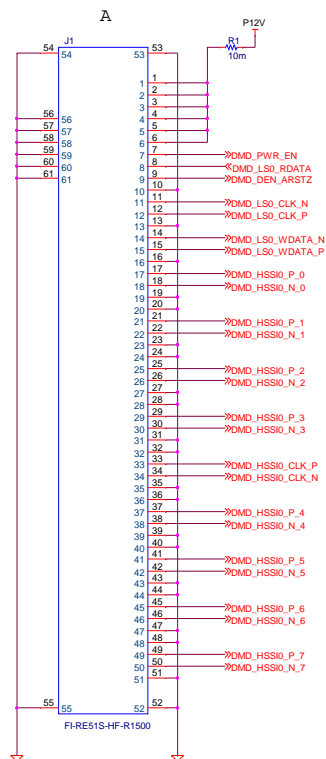
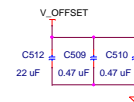
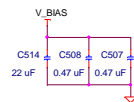
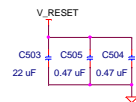
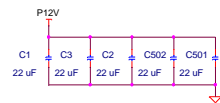
Revisions

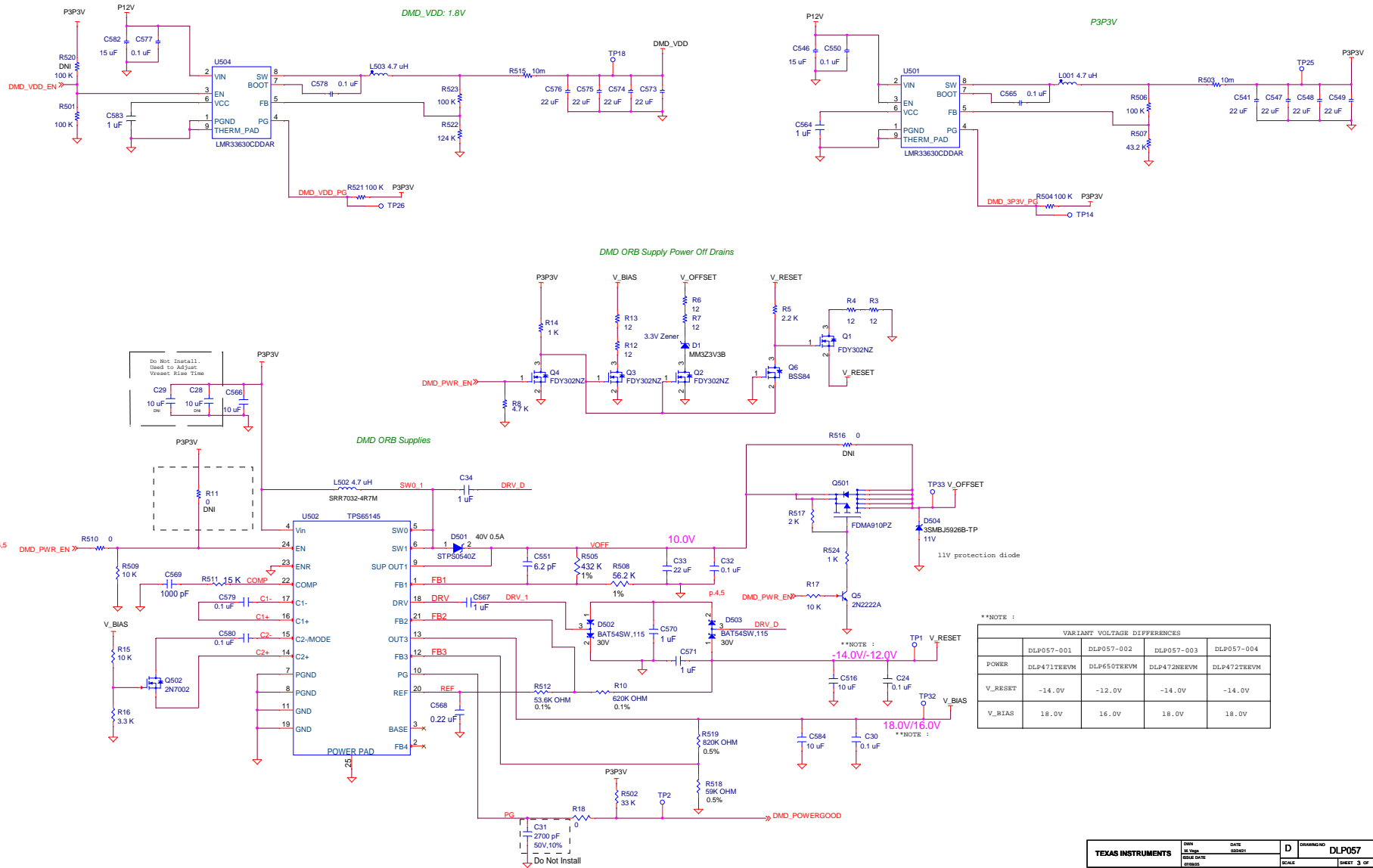
Rev A
Production Release

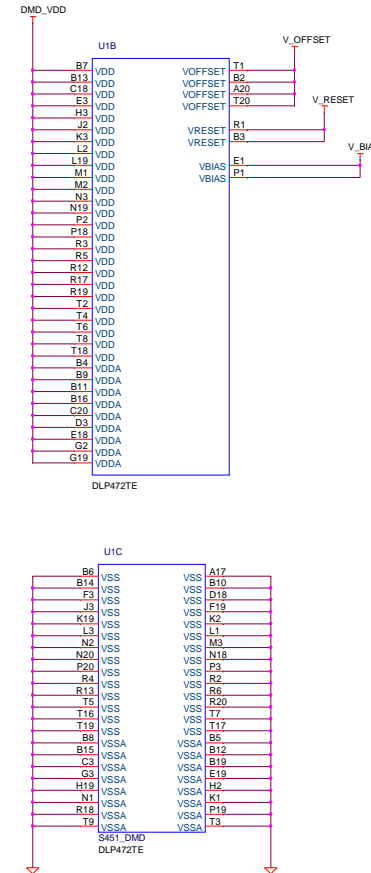
Rev B
Addition of DLP472NE/TE DMDs

Rev C
Updated L502 from 22uH to 4.7uH

		DWN	M. Vega	DATE	02/24/2021	TEXAS INSTRUMENTS (C) COPYRIGHT 2024 TEXAS INSTRUMENTS ALL RIGHTS RESERVED	
		ENGR	L. Wang		07/09/2025		
		SYST	R. Matcovschi		07/09/2025		
		PRJ					
		QA	---		---	TITLE DLP471TE, DLP472NE/TE and DLP650TE EVMs	
NEXT ASSY	USED ON					A3	DRAWING NO DLP057
APPLICATION		SW	Cadence Capture V.17.2			SCALE	SHEET 1 of 6
							REV C







*** NOTE :	
VARIANT PIN NAME DIFFERENCES FOR U1	
DLP057-001, -003, -004	DLP057-002
DLP471TEEVM, DLP472[NE/TE]EVM	DLP650TEEVM
Pin A3 - D_AP<4> Pin A4 - D_AN<2> Pin F2 - D_AP<2> Pin E2 - D_AN<2>	Pin A3 - D_AP<2> Pin A4 - D_AN<2> Pin F2 - D_AP<4> Pin E2 - D_AN<4>

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TEXAS INSTRUMENTS	DWN M. Vega	DATE 02/24/21	A3	DRAWING NO DLP057	REV C
	ISSUE DATE 07/09/25				
			SCALE	SHEET 5 OF 6	

A --- Production Release

PCB LOGO:
Texas Instruments



PCB LOGO:
WEEE and CE



PCB LOGO:
FCC disclaimer

*For evaluation only; not
FCC approved for resale*

LBL1
PCB LABEL
THT-1-423-10
LABEL 0.25"HX0.75"W WH

Z_BOARD1
Printed Circuit Board, DLP057

Z_MECH2
CABLE ASSY HI SPEED 51POS 20CM
JF08R0R051020UA

Z_MECH3
CABLE ASSY HI SPEED 51POS 20CM
JF08R0R051020UA

VARIANT LABEL - LBL1	
DLP057-001	DLP471TEEVM
DLP057-002	DLP650TEEVM
DLP057-003	DLP472NEEVM
DLP057-004	DLP472TEEVM

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TEXAS INSTRUMENTS	DWN M. Vega	DATE 02/24/21	A3	DRAWING NO DLP057	REV C
	ISSUE DATE 07/09/25				
			SCALE	SHEET 6 OF 6	