

NOTES, UNLESS OTHERWISE SPECIFIED:

1. A "Z" suffix on a signal name indicates an active low signal.
2. All components with designators "U", "Q", and "D" are electrostatic discharge sensitive.
3. All components with designators above 500 are mounted solder side of the board.
4. All resistor values are in ohms, 1/16 W and 5% unless otherwise specified.
5. All capacitor values in microfarads unless otherwise specified.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	Initial Release	5/14/2023	TBD

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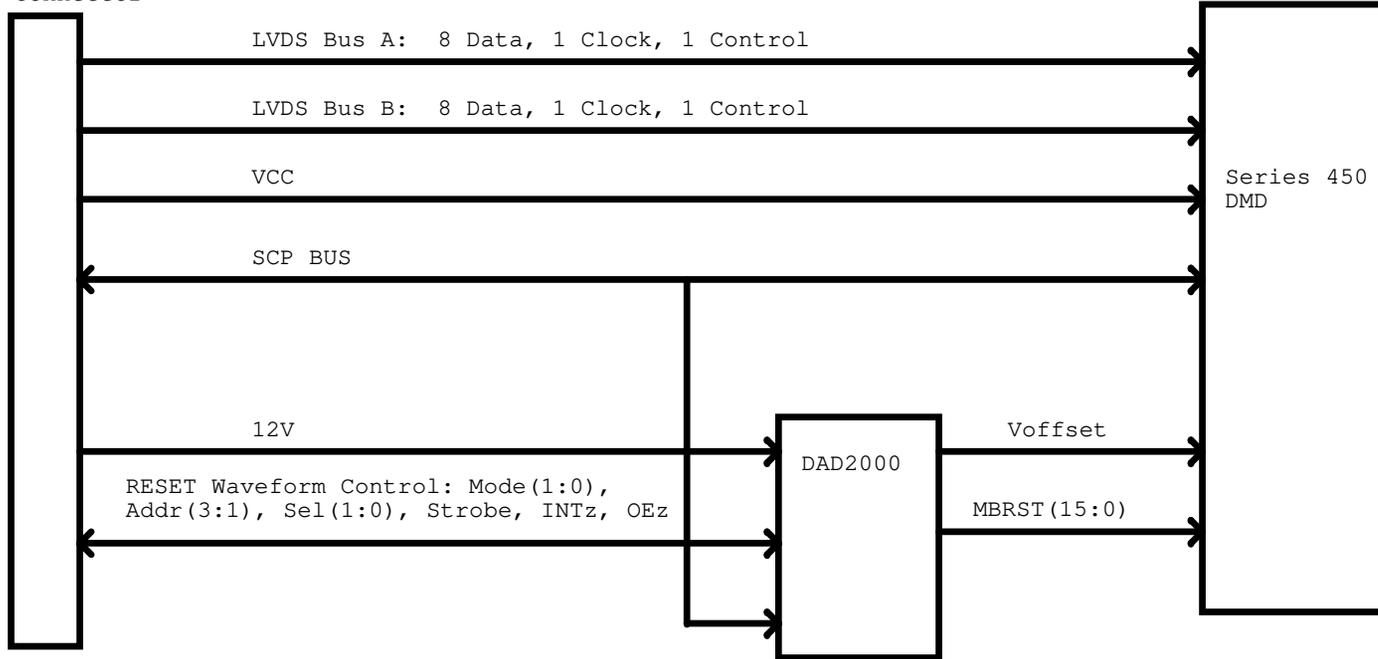
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		APVD	Michael McCormick		3/14/2023	
PWB: 2518898		MFG				TITLE
CCA: 2518899		QA				Schematic, S450 DMD, DLP093
NEXT ASSY	USED ON					A3 DRAWING NO 2518897
APPLICATION		SW	Allegro CIS 17.2			REV A
				SCALE		SHEET 1 of 6

Flex Connector



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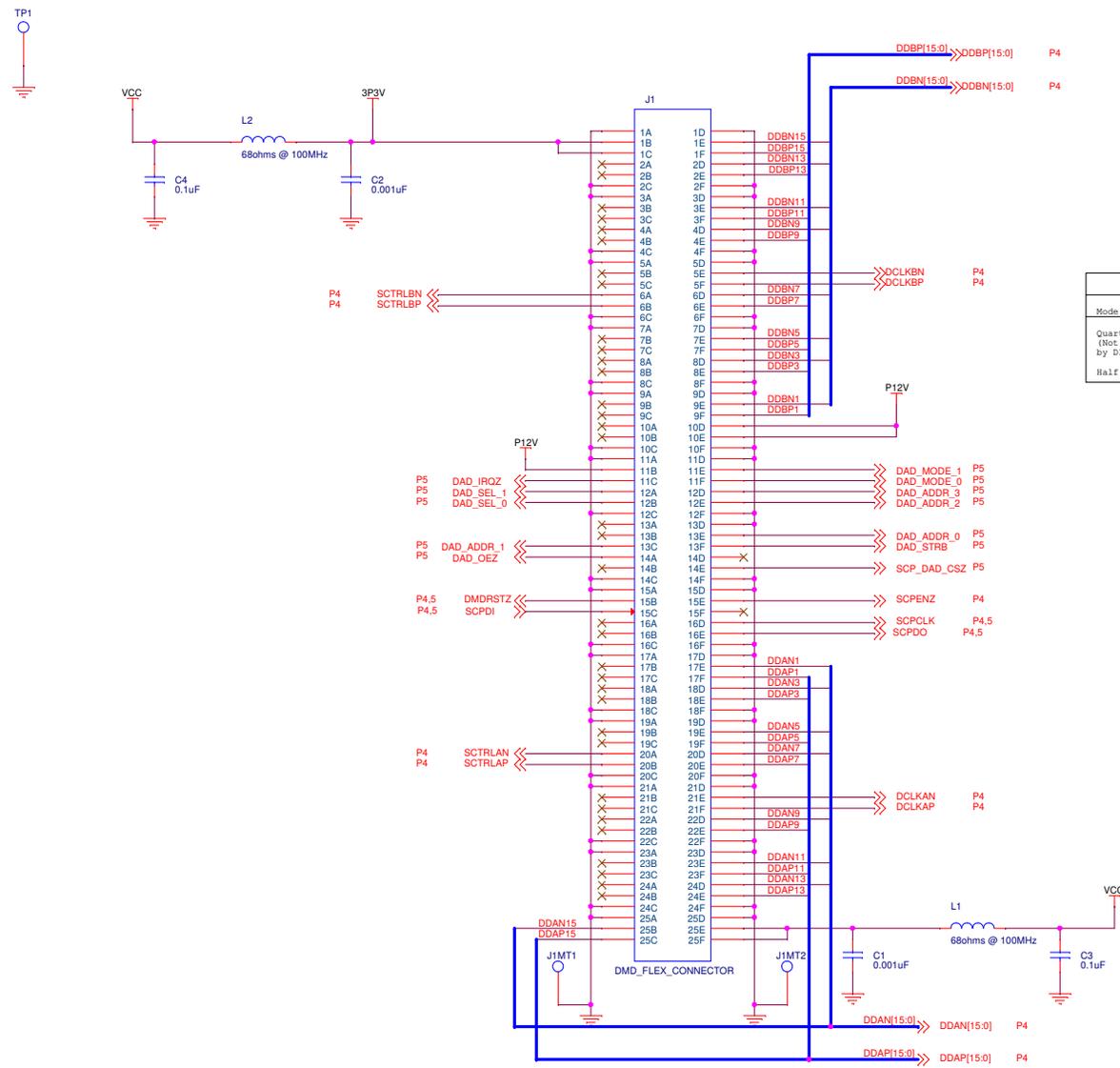
A3

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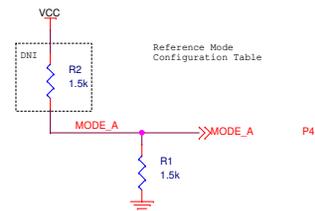
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SHEET 2 OF 6

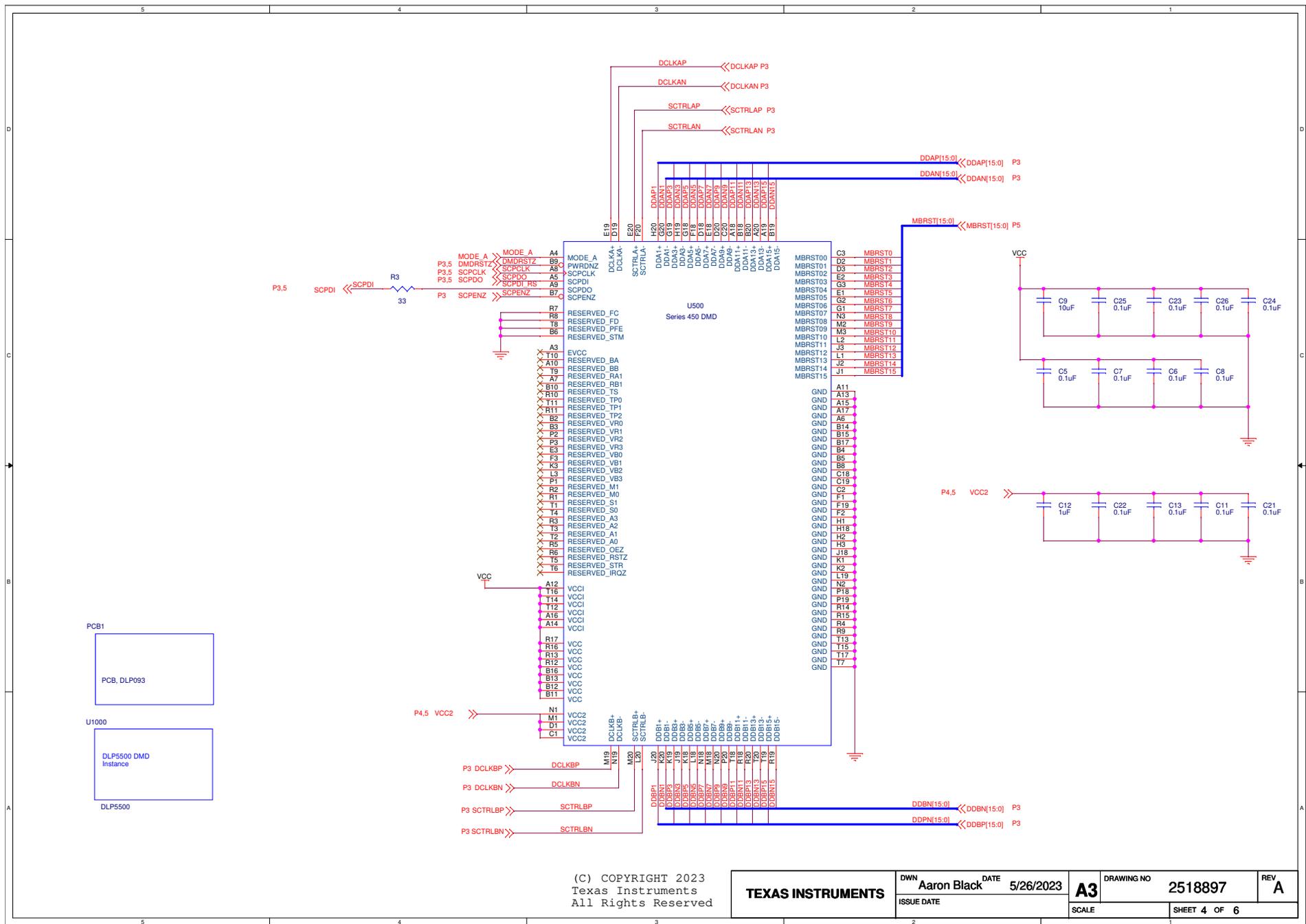


Mode Configuration Table		
Mode	R2 (pull-up)	R1 (pull-down)
Quarter Bus Mode (Not Supported by DLP5500)	Installed	Not Installed
Half Bus Mode	Not Installed	Installed



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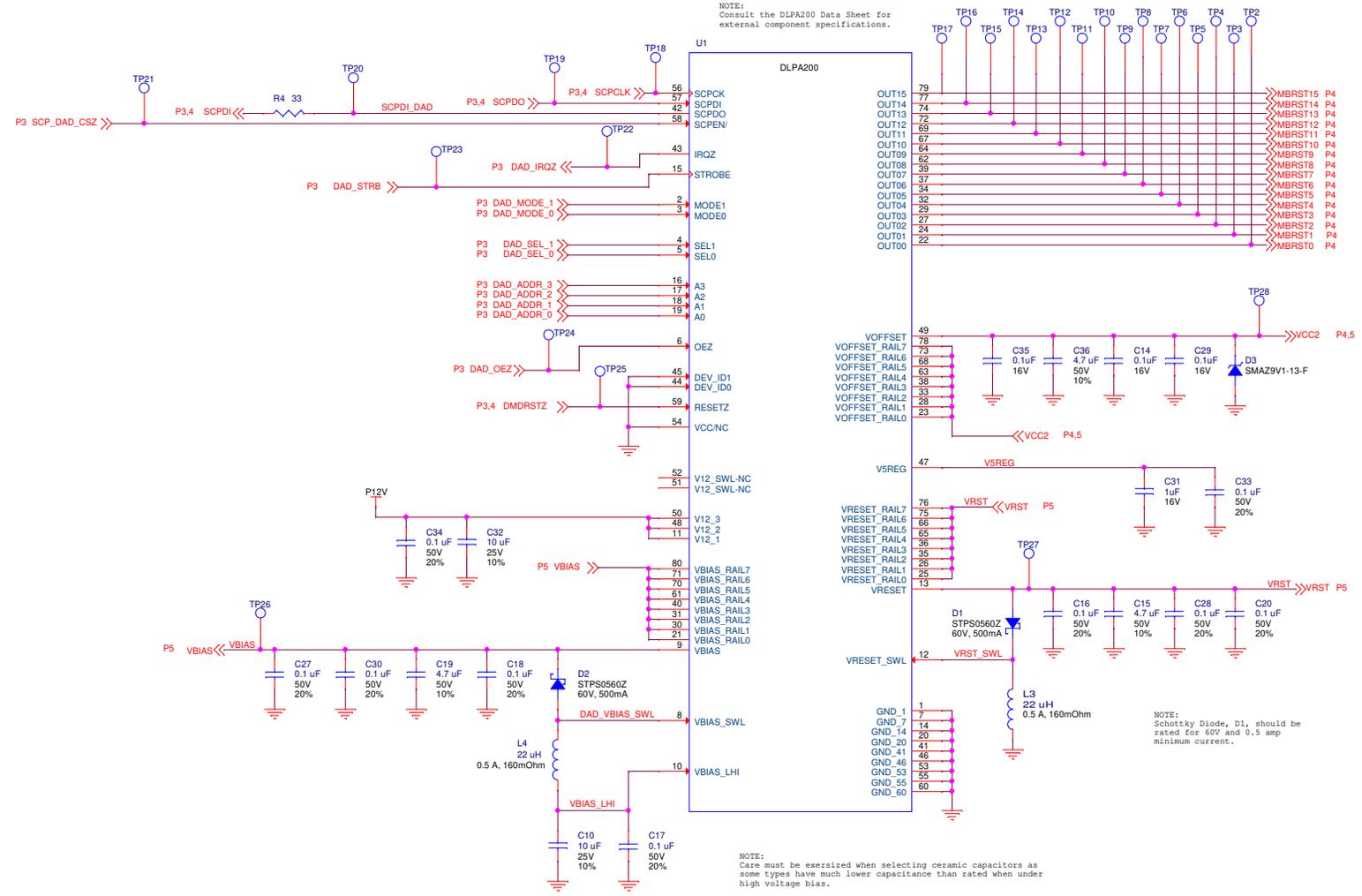
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NOTE:
Consult the DLPA200 Data Sheet for
external component specifications.



NOTE:
Care must be exercised when selecting ceramic capacitors as
some types have much lower capacitance than rated when under
high voltage bias.

NOTE:
Schottky Diode, D1, should be
rated for 60V and 0.5 amp
minimum current.

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DLPA200 Reset Driver

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