


NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "DMD_P3P3V" represents connection to the +3.3V digital power plane.
2. The symbol  represents connection to the digital ground plane.
3. A "Z" suffix on a signal name indicates an active low signal.
4. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
5. All resistor values are in ohms, 1/16W and 5% unless otherwise specified.



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		SVS				ALL RIGHTS RESERVED	
		PRJ				TITLE	
		QA				DLP660TE DMD Board	
NEXT ASSY	USED ON					D	DRAWING NO
		SW					DLP080
APPLICATION				SCALE		SHEET	1 of 8
						REV	B

J500

HPC FMC CONNECTOR

From
Secondary
Controller

From
Primary
Controller

PAGE 3

A/B DMD DATA (DD_AP[15:0], DD_AN[15:0], DD_BP[15:0], DD_BN[15:0])

A/B DMD CLK (DCLK_AP, DCLK_AN, DCLK_BP, DCLK_BN)

A/B SCTRL (SCTRL_AN, SCTRL_AP, SCTRL_BN, SCTRL_BP)

C/D DMD DATA (DD_CP[15:0], DD_CN[15:0], DD_DP[15:0], DD_DN[15:0])

C/D DMD CLK (DCLK_CP, DCLK_CN, DCLK_DP, DCLK_DN)

C/D SCTRL (SCTRL_CN, SCTRL_CP, SCTRL_DN, SCTRL_DP)

DAD CONTROL (STROBE, MODE[1:0], SEL[1:0], ADDR[3:0], OEZ)

SCP CONTROL (CLK, DO, DI, ENZ, IRQZ)

DMD_RSTZ

U1
VREG
3.3V->1.8V

3.3V

U2

DMD Power Supplies

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U5, U6, U7
Level Translators
3.3V->1.8V

VOFFSET

VBIAS

VRESET

PG_OFFSET

EN_OFFSET

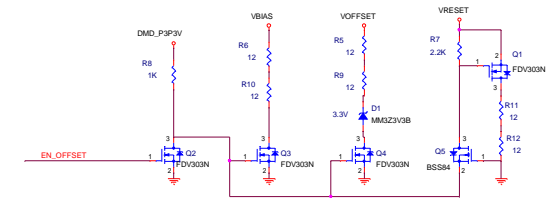
U3

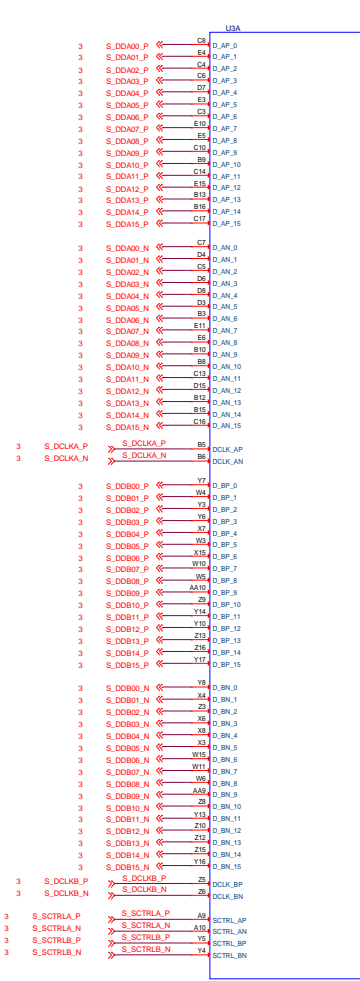
DLP660TE

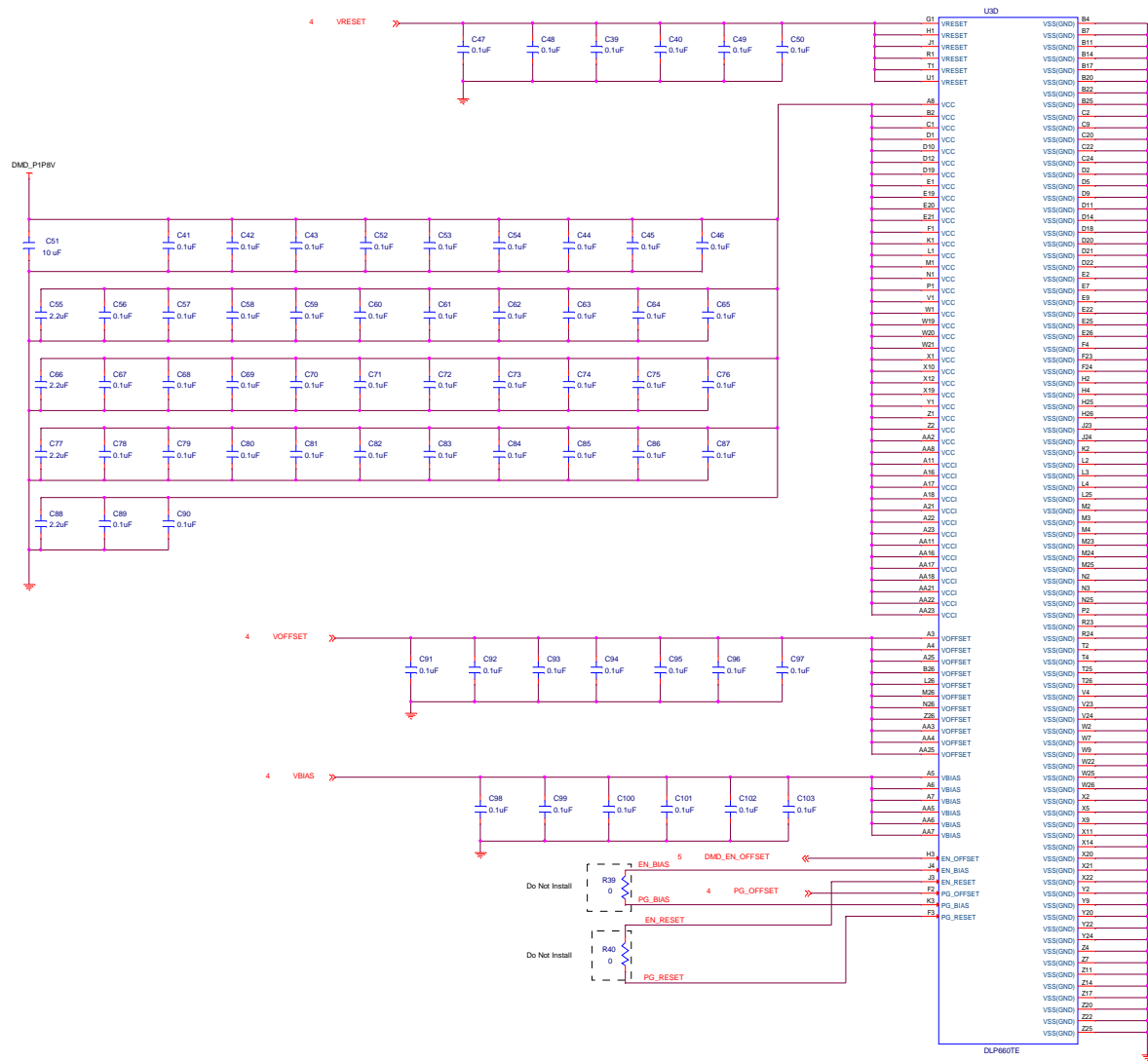
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DMD Power Supplies







DMD Power/Gnd

Rev A:
Initial Release

Rev B:
Removed J1 & J2 Mustang Flex Interface
Added J500 FMC connector

TEXAS INSTRUMENTS

DWN Oscar Guevara	DATE 6/17/2022
ISSUE DATE	

B	DRAWING NO DLP080
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REV
B

SCALE	SHEET 7 OF 8
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	ISSUE DATE			SCALE	SHEET 8 OF 8