

# DISCLAIMER

\* SN75DP126

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## File Description

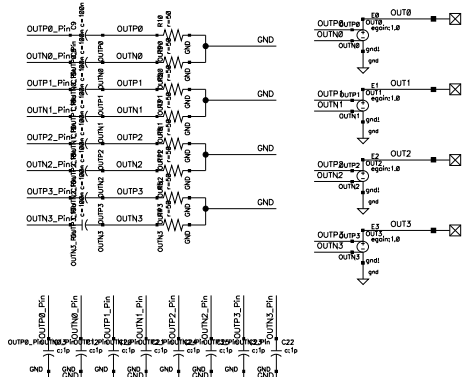
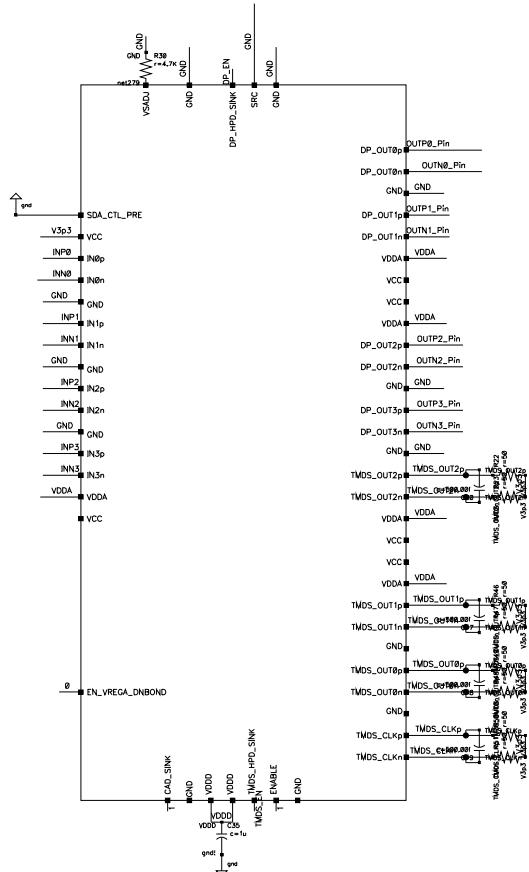
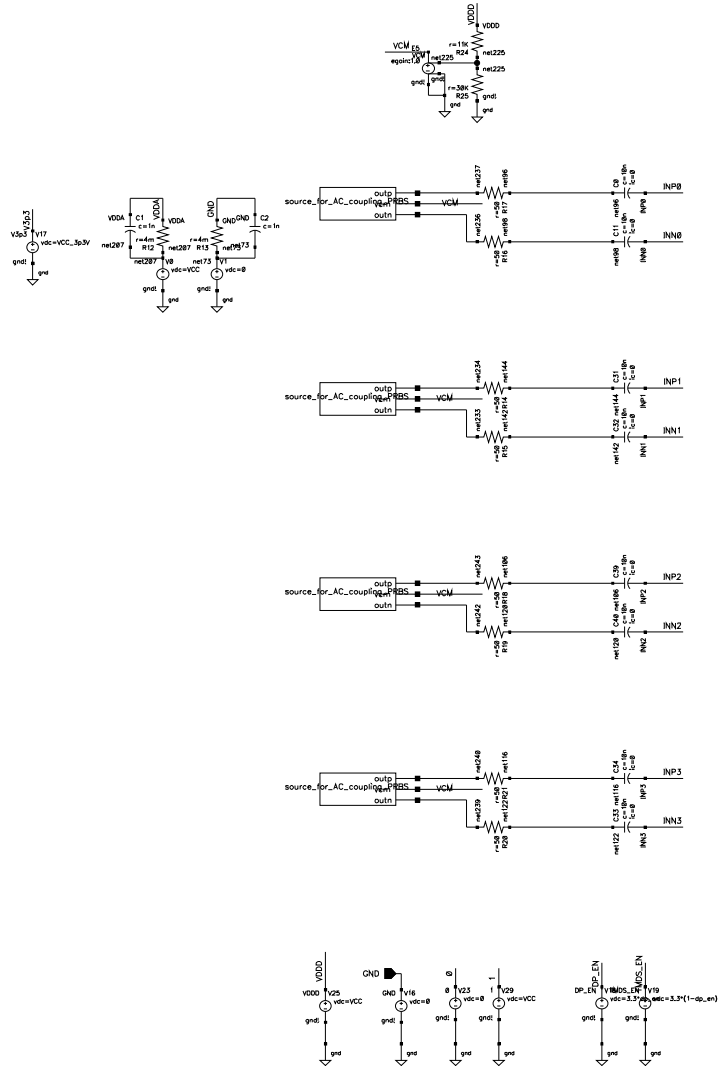
‘DP126\_test\_bench.sp’ -which is the test bench- invokes other 2 files:

‘DP126\_nom\_process\_enc.lib’ –nominal process library, and ‘DP126\_subckts\_enc.sp’ –  
subcircuits’ definition.

## **Instructions**

- 1) Open your simulator HSPICE\_A-2008.03-SP1 on Windows
- 2) On the file menu, open and run file 'DP126\_test\_bench.sp'.

Plots of the test-setup schematic and simulation results are shown below:



# DP126: HSPICE Transient Simulation

