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* Model Usage Notes:
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* A. Features have been modelled
* 1. Output Voltage Setting with VID controller
* 2. Programmable soft-start time and output voltage transition time
* 3. Fixed frequency
* 4. Low-side FET Zero-Crossing
* 5. Low-side FET Valley Current Limit(OCP)
* 6. Low-side FET Negative Current Limit
* 7. Power Good
* 8. Over Voltage Protection(OVP)
* 9. Under Voltage Protection(UVP)
* 10. Out-Of-Bounds(OOB) Operation
* 11. Built-in output discharge
* 12. UVLO Protection
* 13. BOOT functionality
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* B. Features have not been modelled
* 1. Operating Quiescent Current
* 2. Shutdown Current
* 3. Temperature dependent characteristics
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* C. Application Notes
* 1. Ground Pins have been tied to OV internally and hence model does not support Inverting
*    topologies.
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