



AM62D SKEVM

REV E1

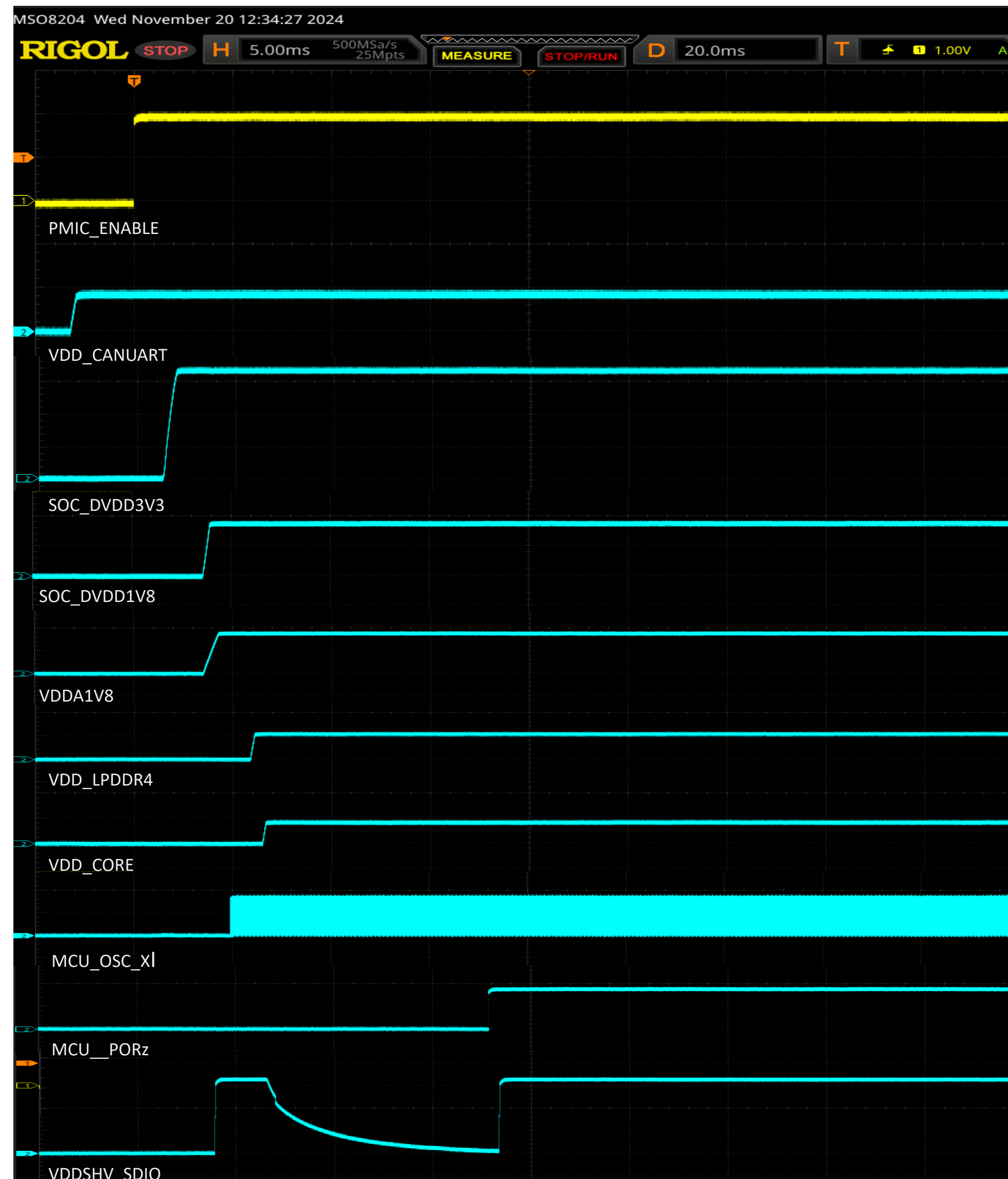
Power up Sequence

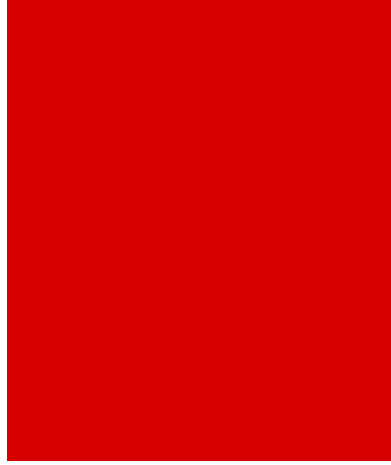
20-11-2024

Power Up sequence timing measurements



Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC ENABLE	3.3	0
SoC_DVDD3V3	3.3	1.6
VDD_CANUART	0.75	-3.25
VDDA1V8	1.8	3.75
SoC_DVDD1V8	1.8	3.7
VDD_LPDDR4	1.1	5.95
VDD_CORE	0.85	6.65
VDDSHV_SDIO	3.3	4
MCU_OSC0_XI	1.8	5
MCU_PORz (Reset)	1.8	18.25





AM62D SKEVM

REV E1

Power Down Sequence

20-11-2024

Power Down sequence timing measurements



Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC ENABLE	3.3	0
SoC_DVDD3V3	3.3	4.4
VDD_CANUART	0.75	99.15
VDDA_1V8	1.8	2.65
SoC_DVDD1V8	1.8	2.91
VDD_LPDDR4	1.1	2.15
VDD_CORE	0.75	1.55
VDDSHV_SDIO	3.3	16.68
MCU_OSC0_XI	1.8	2.55
MCU_PORz (Reset)	1.8	450uS



