**IWR6843ISK**  Via Fill & Cap Requirements

There are many via-s on the PCB which are either on the pads or close to pads.

These need to filled with non-conductive epoxy and capped with copper plating to ensure flat surface.

Near U1 (top assembly):

All the via-s (blind as well as PTH) near the BGA area need to be filled and capped



U1

U2 thermal pad (top assembly):



U4 (top assembly):



U5 (top assembly):

