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MUX SETTINGS

Switch Refdes	Default position	Position for STAND ALONE mode*	Position for DCA1000 mode	Position for 40Pin LP/BP
S1	OFF (SPI-1)	OFF	OFF	OFF
S2	ON (40pin)	Default position	Default position	ON
S3	ON (FTDI)	ON	Default position	OFF
S4	ON (FTDI/40pin/J16)	ON	OFF	ON
S5 <sup>1</sup>	ON (XDS110)	Default position	Default position	Default position
S6	ON (FTDI)	ON	OFF	OFF
S7	ON (FTDI)	ON	OFF	OFF
S8	OFF (60pin)	Default position	OFF	ON
S9	OFF (60pin)	Default position	OFF	ON
S10	ON (FTDI)	ON	Default position	OFF
S11	ON (FTDI)	ON	Default position	OFF
S12	ON (XDS110)	Default position	Default position	Default position

Table 1 : Switch settings for different sources

\*Standalone mode means the combination of Starter kit and MMWAVEICBOOST

(1)S5 has RS232 connections from 40pin/FTDI/60pin/XDS110. Default position will be XDS110.

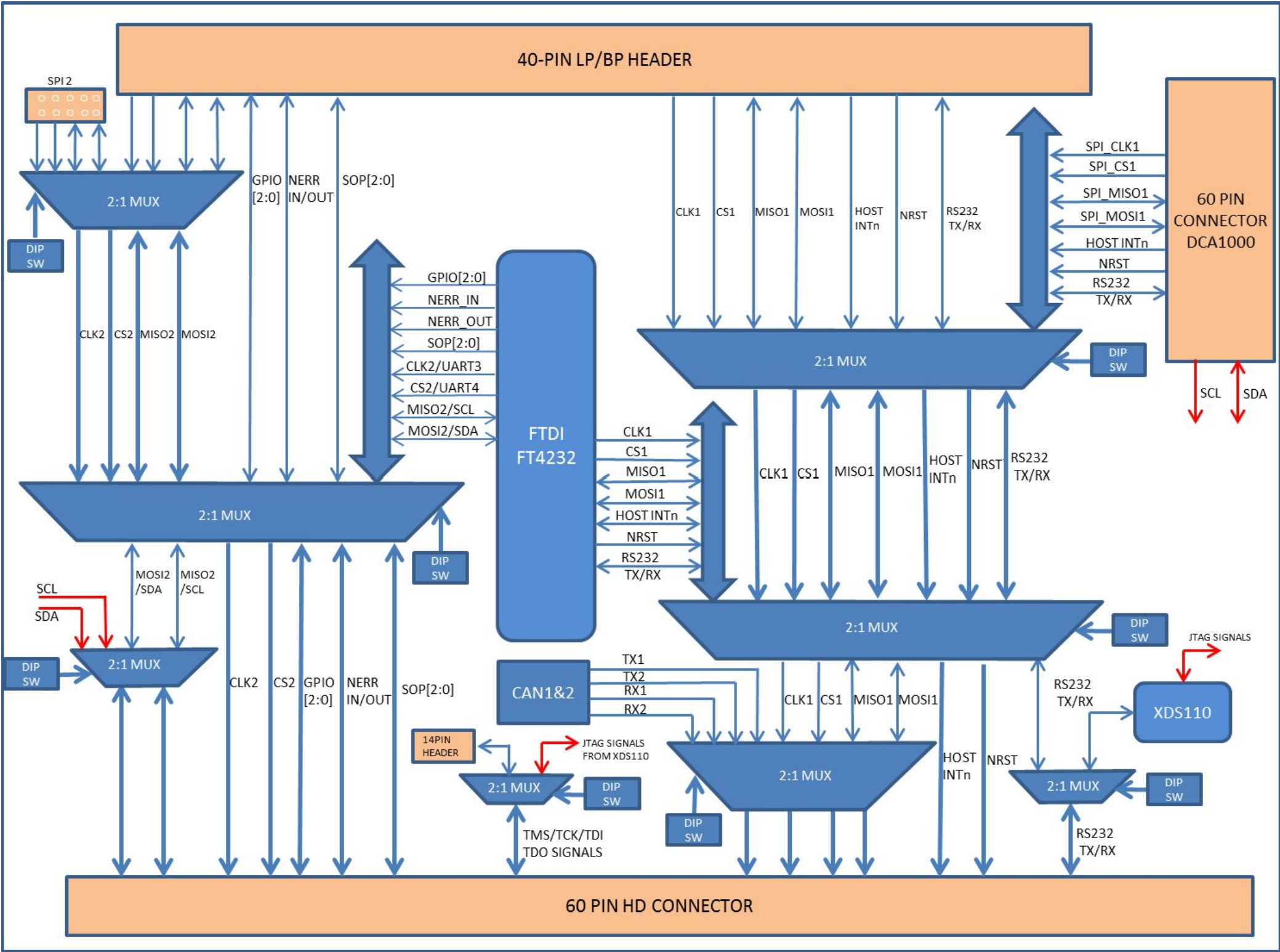
I2C DEVICES

S.No	DESCRIPTION	I2C ADDRESS
1	PMIC	110 0000
2	CURRENT SENSOR 3.3V	100 0010
3	CURRENT SENSOR 3.3V (PMIC)	100 0011
4	CURRENT SENSOR 1.8V	100 0110
5	CURRENT SENSOR 1.2V	100 0111
6	CURRENT SENSOR 1.0V	100 1100
7	TEMPERATURE SENSOR1	100 1001
8	TEMPERATURE SENSOR2	100 1000

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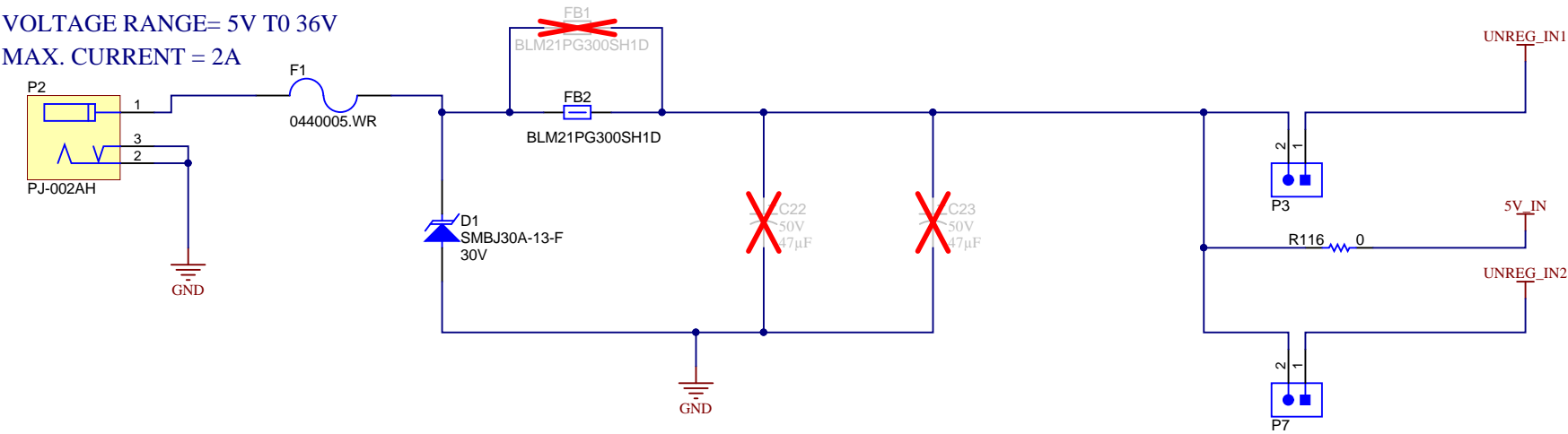


MUXING BLOCK DIAGRAM



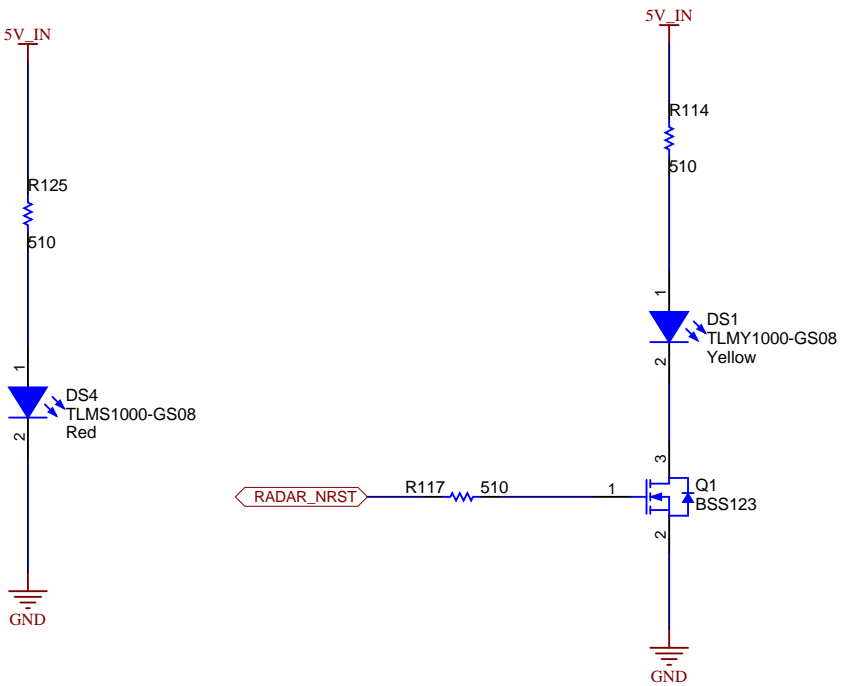
POWER SUPPLY CONNECTOR

VOLTAGE RANGE= 5V TO 36V  
MAX. CURRENT = 2A

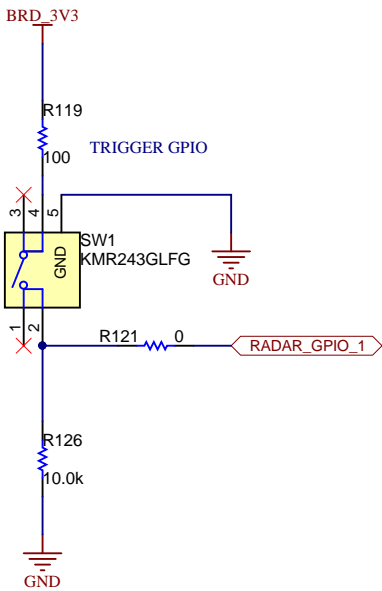
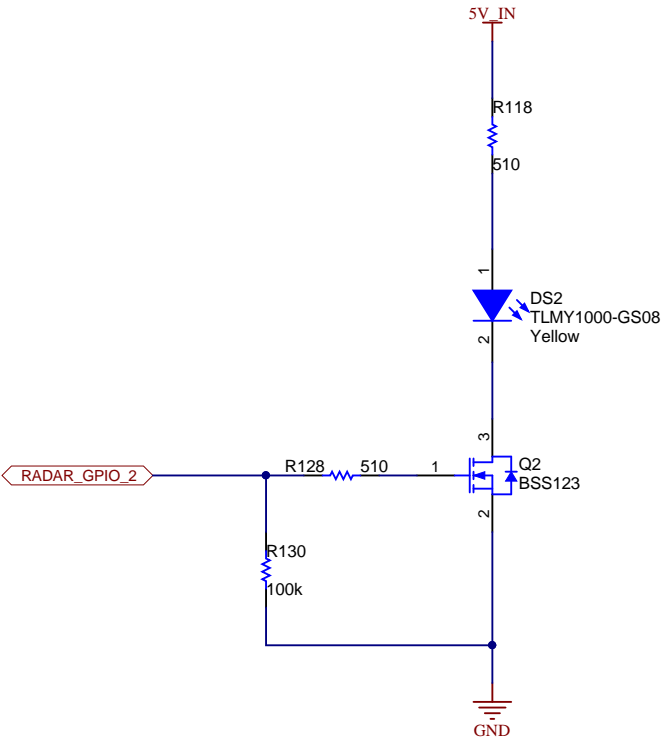
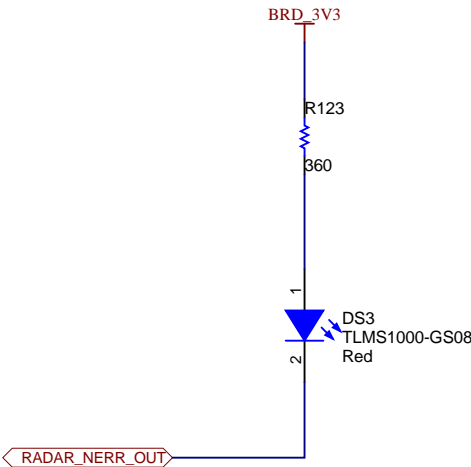
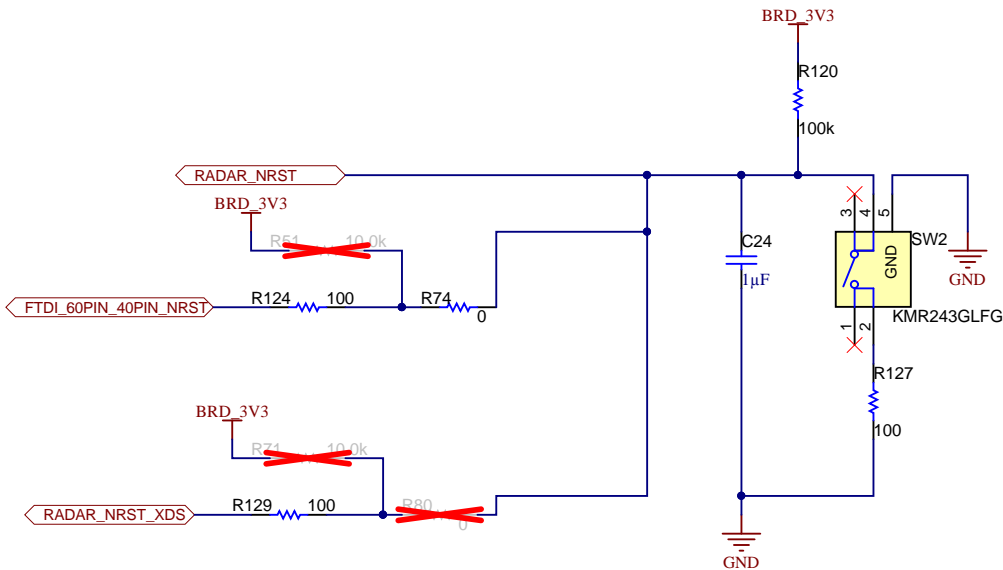


**Note :** Replace the capacitor(PN:597D476X9050Z2T)in C22 and C23  
if the input voltage exceeds 5V

VIN= 5V : Short P3, Mount R116 and do not short P7  
VIN= >5V : Short P3,P7 and Remove R116

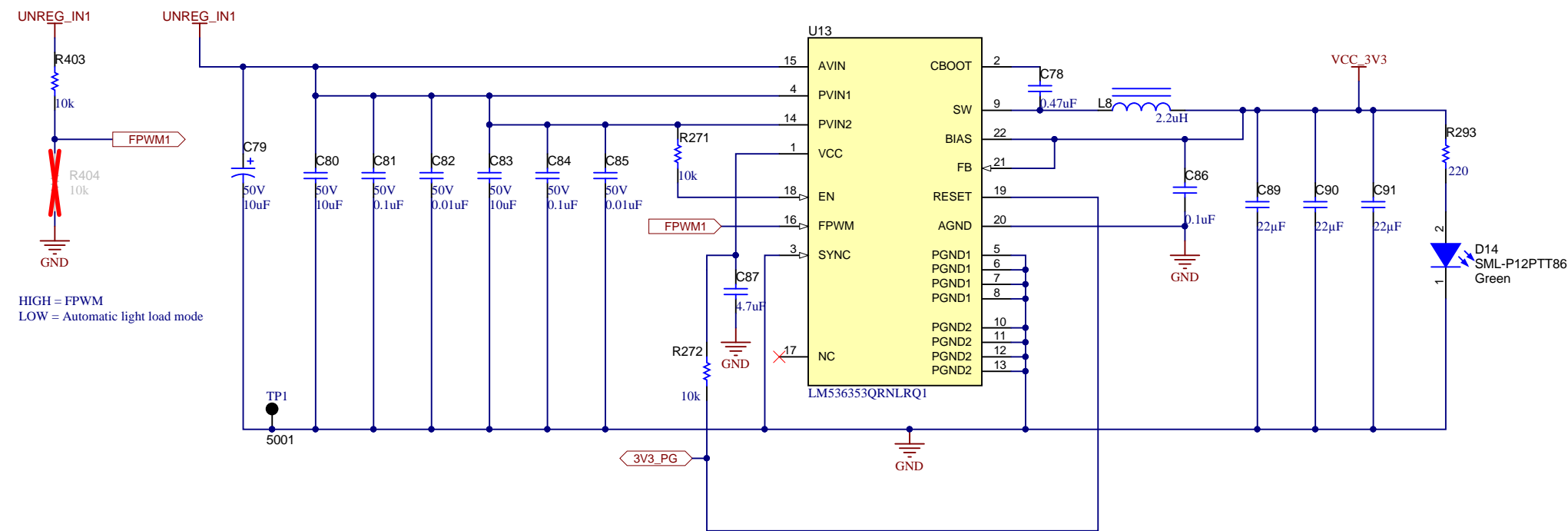


RESET AND LEDS  
INDICATION LEDS

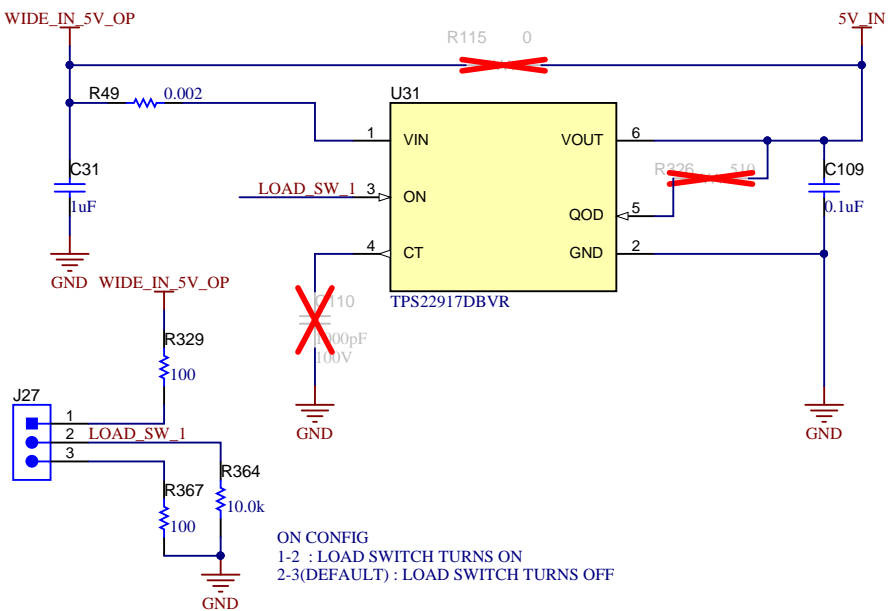


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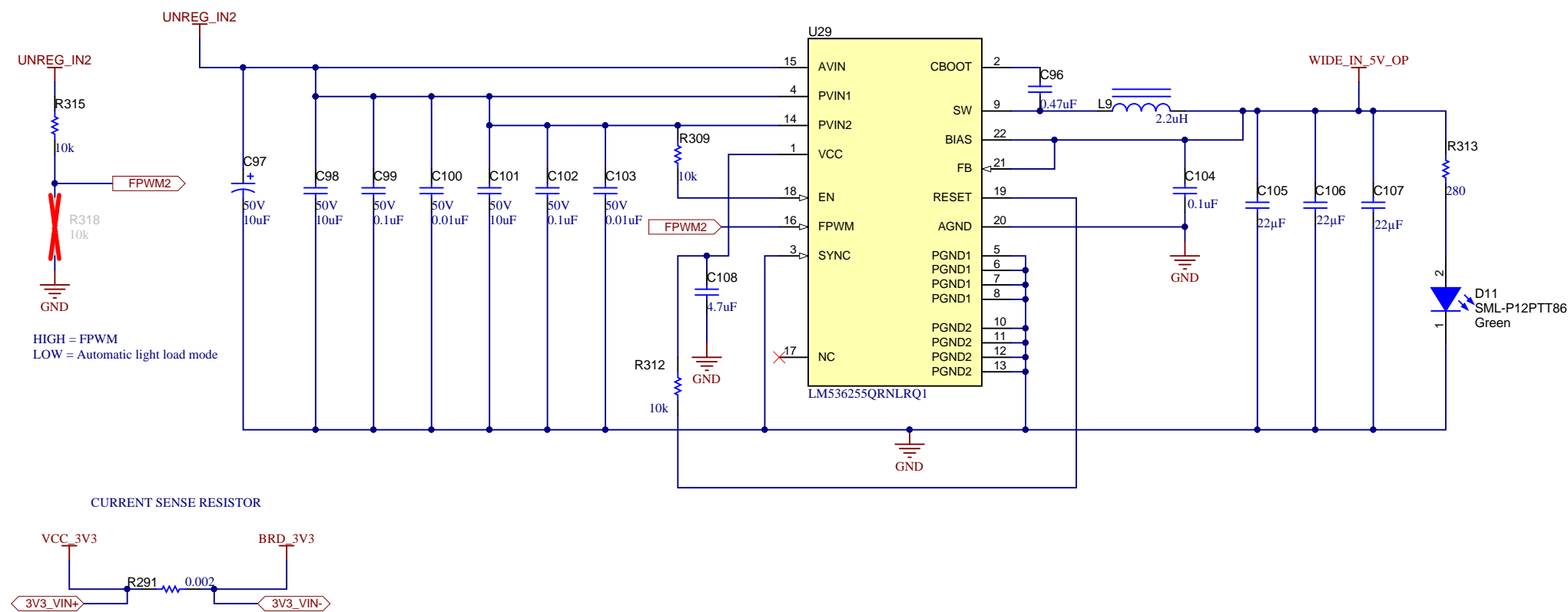
POWER SUPPLY INPUT 5V TO 3.3V OUTPUT



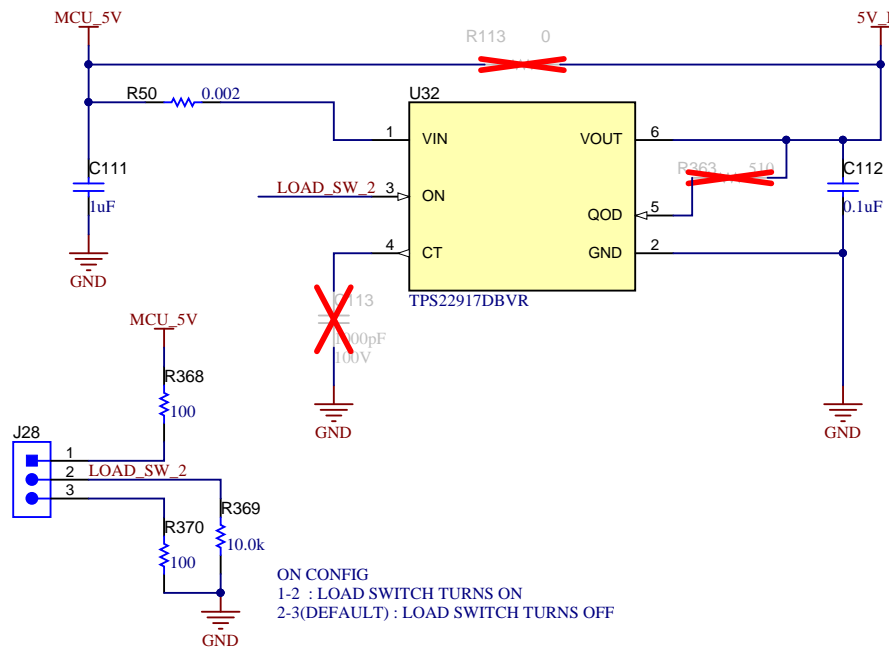
5V SUPPLY FROM JACK



POWER SUPPLY INPUT(6V-36V) TO 5V OUTPUT

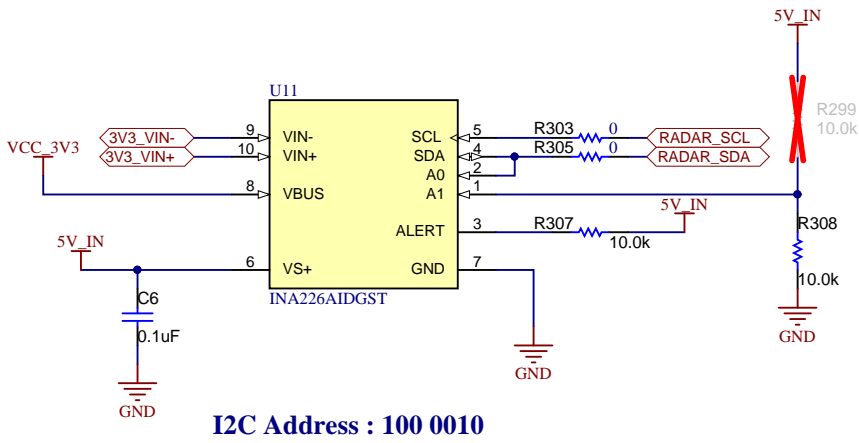


5V SUPPLY FROM THE 40PIN LP/BP CONNECTOR

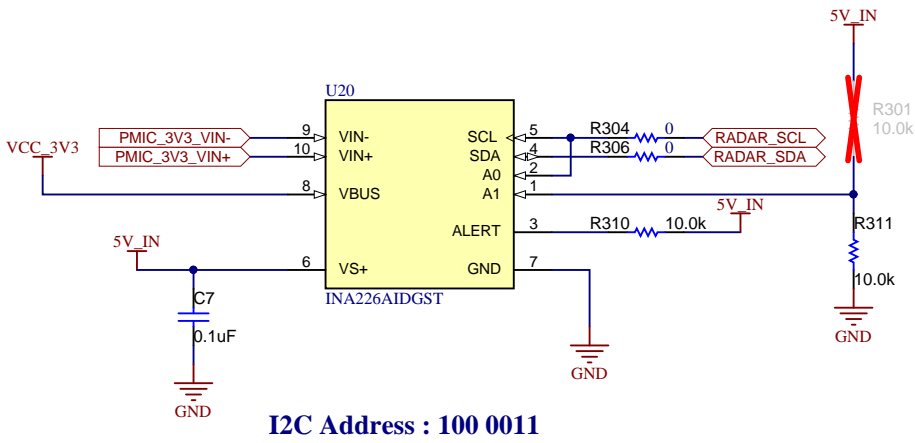


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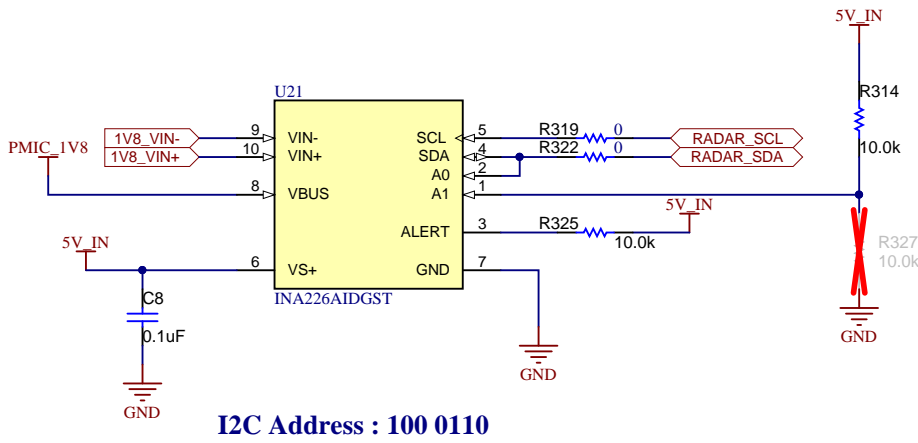
Current Sensors



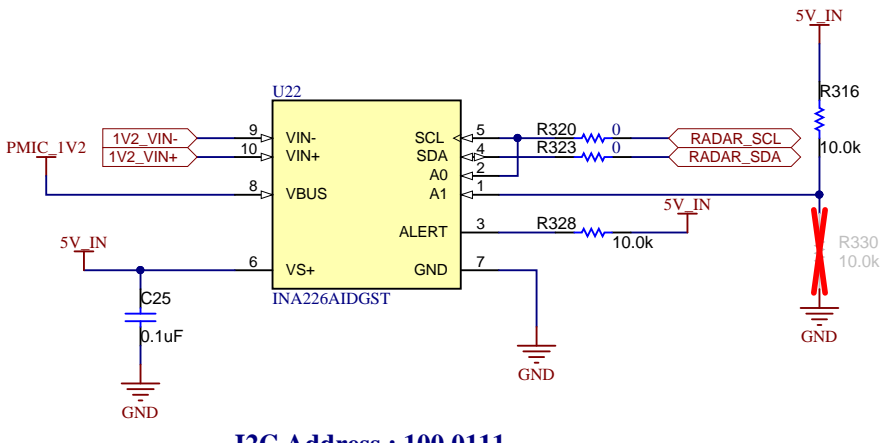
I2C Address : 100 0010



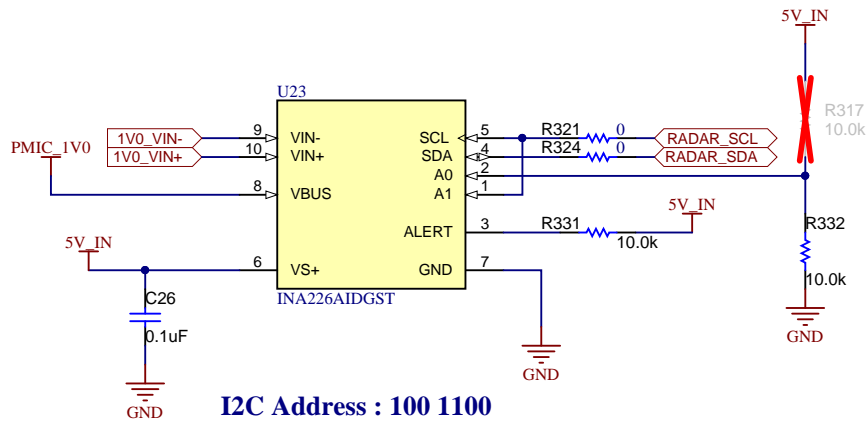
I2C Address : 100 0011



I2C Address : 100 0110



I2C Address : 100 0111

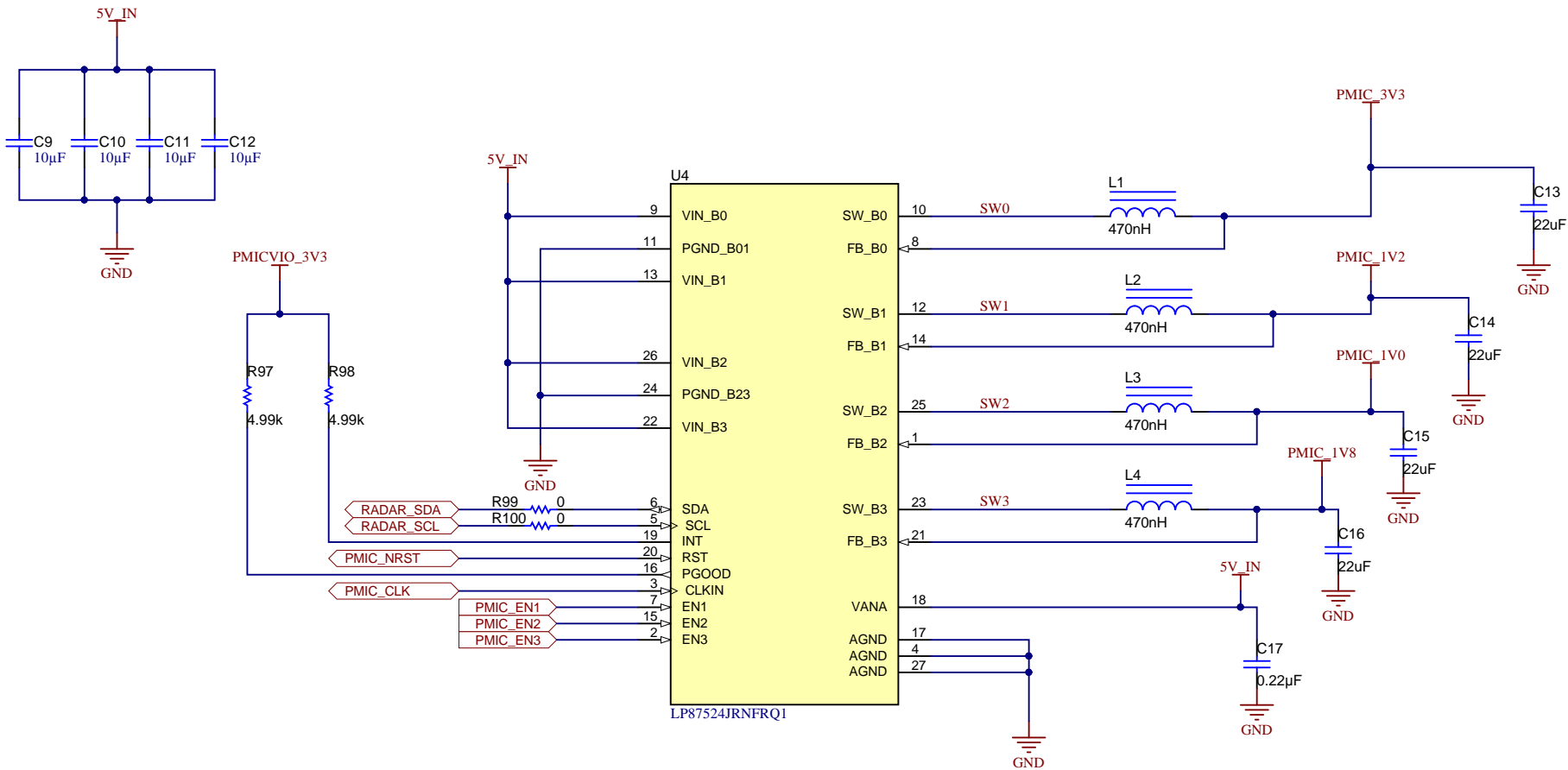


I2C Address : 100 1100

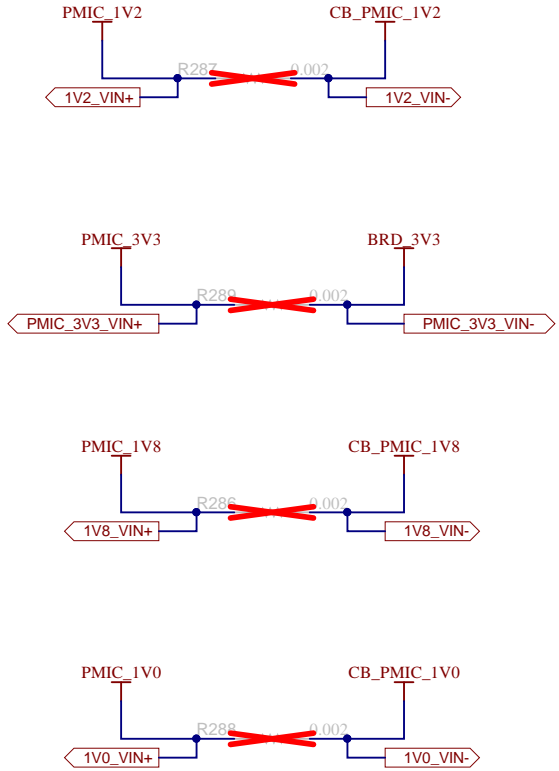
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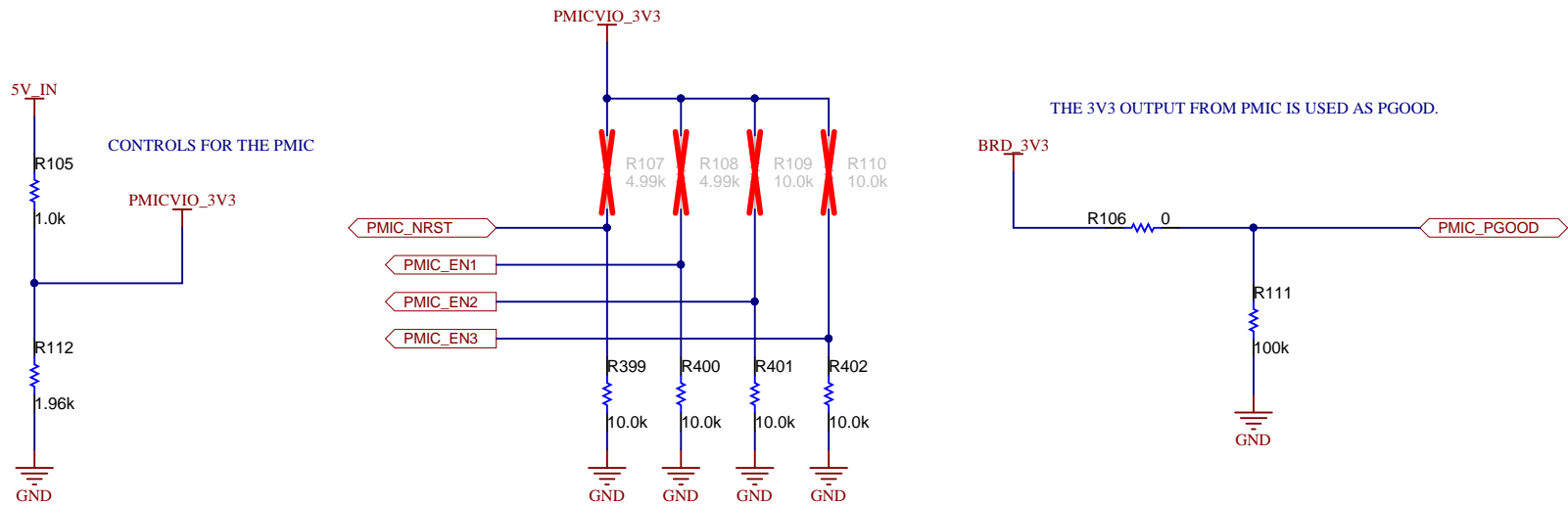
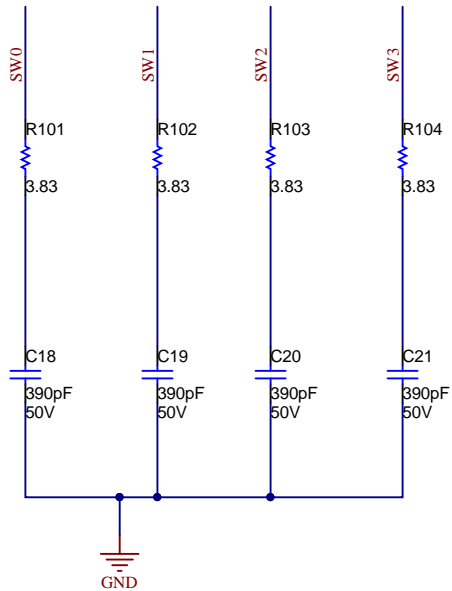
PMIC (3.3V, 1.2V, 1.8V,1.0V OUTPUTS)



CURRENT SENSE RESISTORS

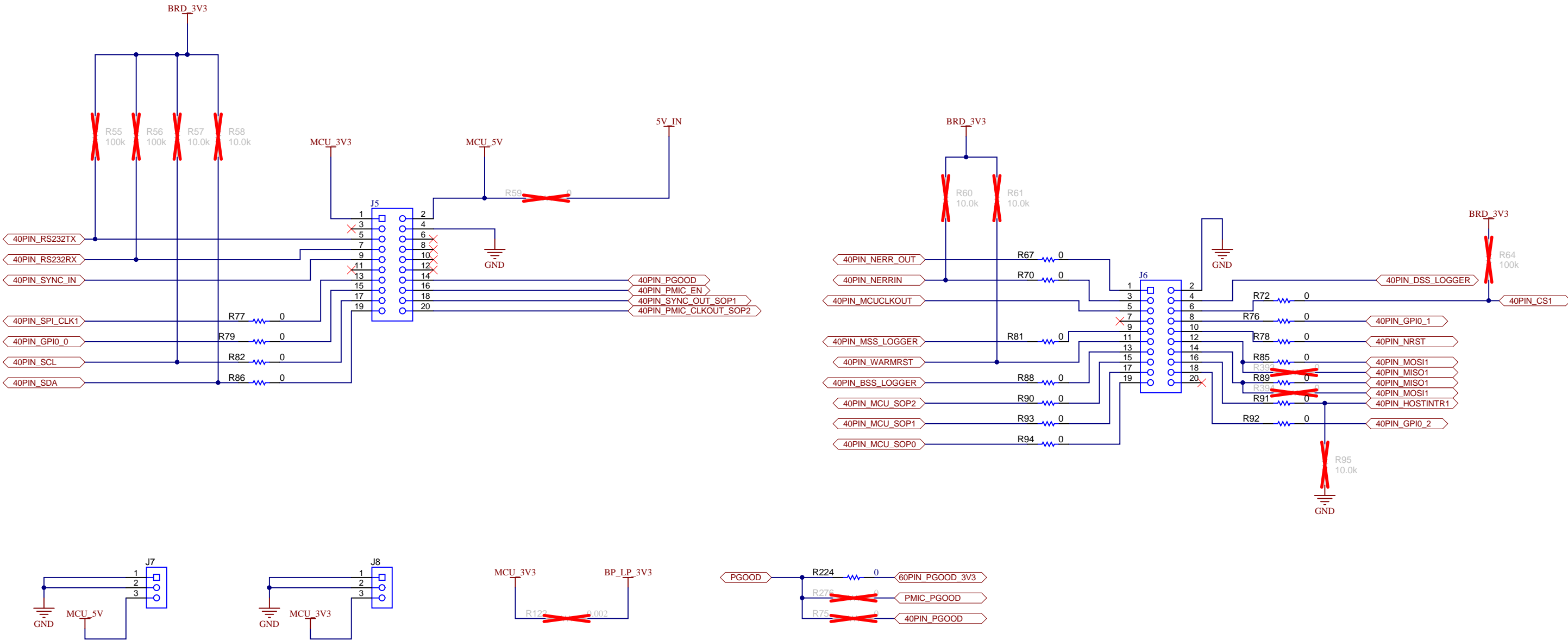


SNUBBER ON SWITCHING NODES



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BP/LP CONNECTOR



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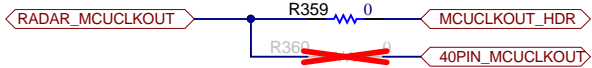
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Number: <a href="#">PROC074</a>	Rev: <a href="#">B</a>	Sheet Title:
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">8</a> of <a href="#">22</a>
Drawn By: <a href="#">Chethan Kumar Y.B</a>	File: <a href="#">PROC074B_LP_Connector.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Chethan Kumar Y.B</a>	Contact: <a href="#">http://www.ti.com/support</a>	<a href="#">http://www.ti.com</a>
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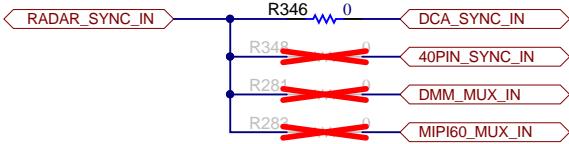


BP/LP RNR OPTIONS

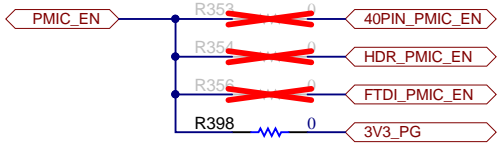
RNR FOR MCUCLKOUT



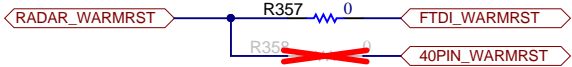
RNR FOR SYNC IN



RNR FOR PMIC ENABLE



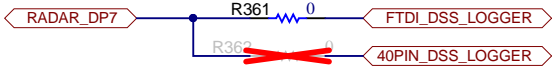
RNR FOR WARMRST



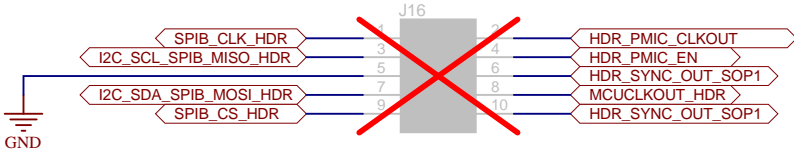
RNR FOR PMIC CLKOUT



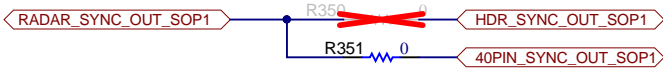
RNR FOR DSS LOGGER



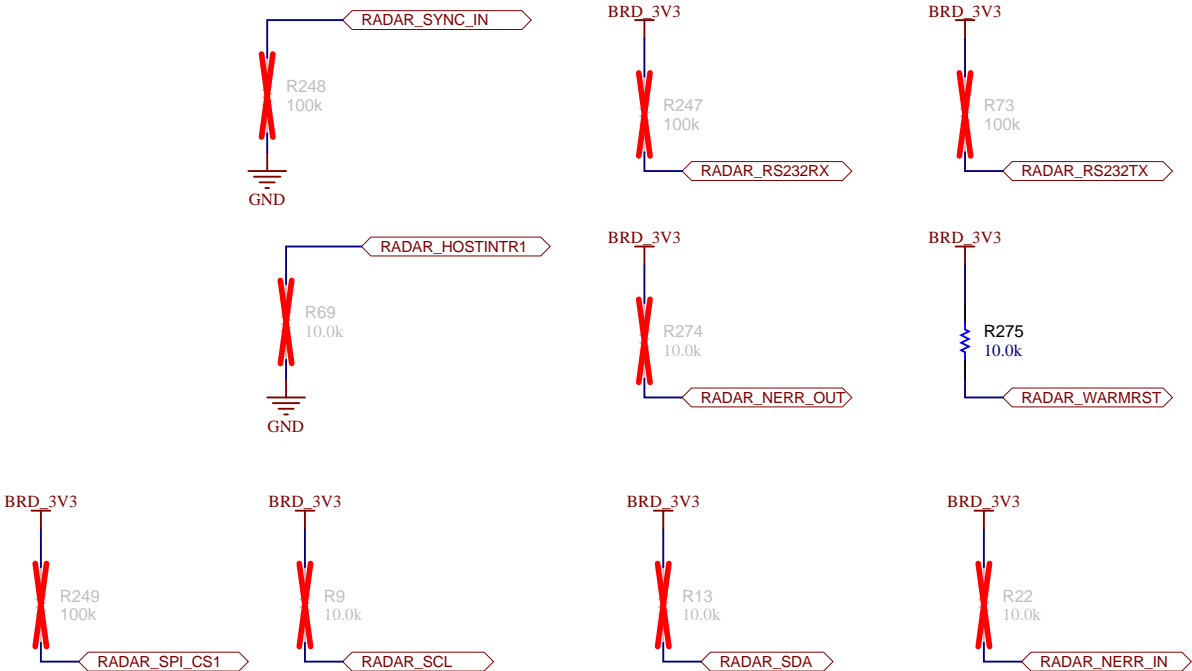
LP/BP SPARE PINS HEADER 1



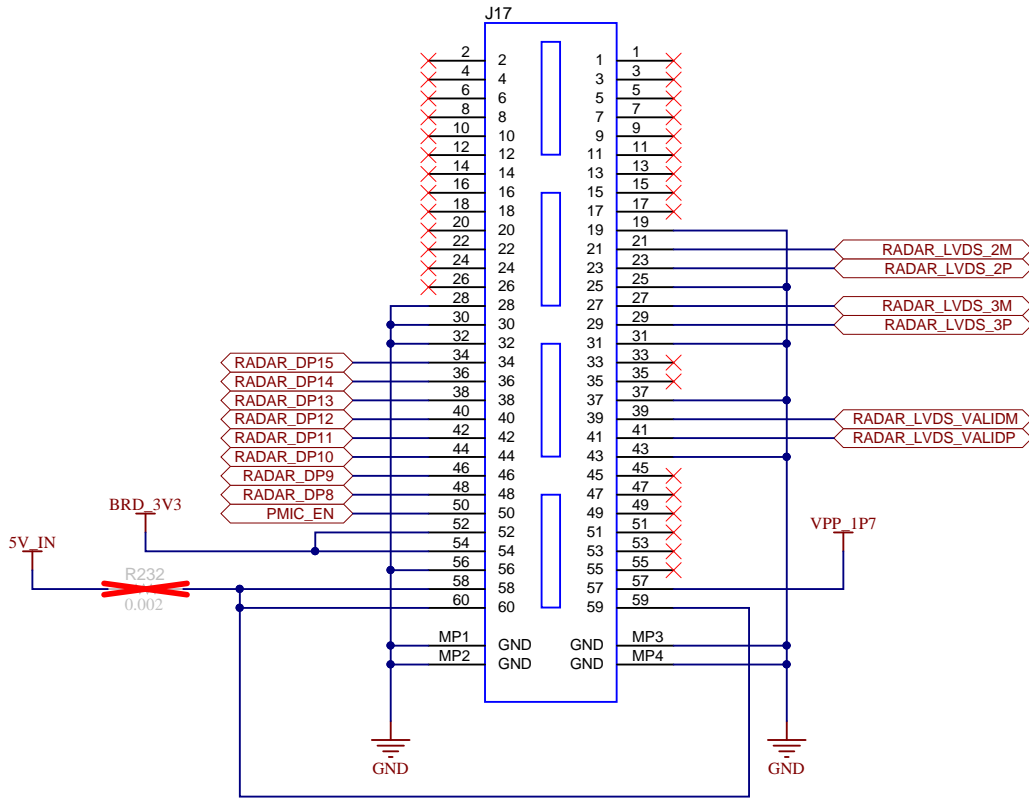
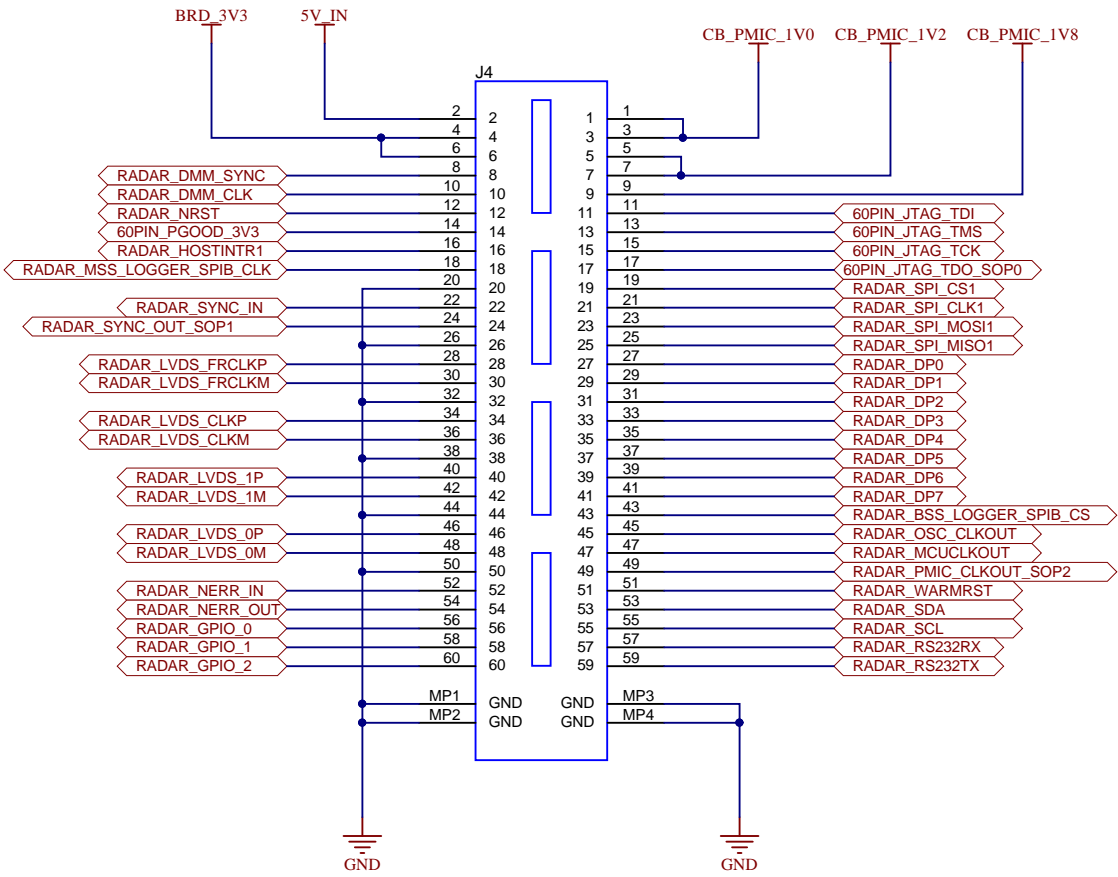
RNR FOR SYNC OUT



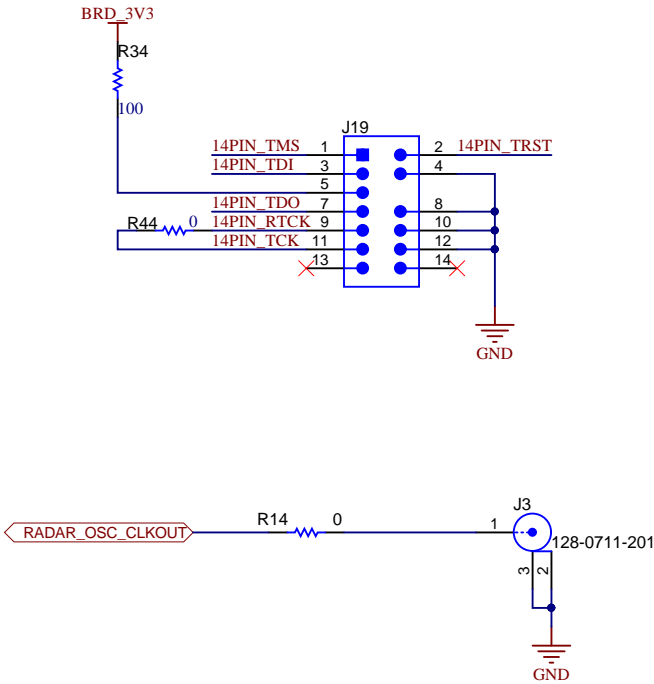
PULL UPs/DOWNs RESISTORS FOR I2C,WARMRST,NERROUT, NERRIN, RS232, SYNC\_IN & HOST\_IN



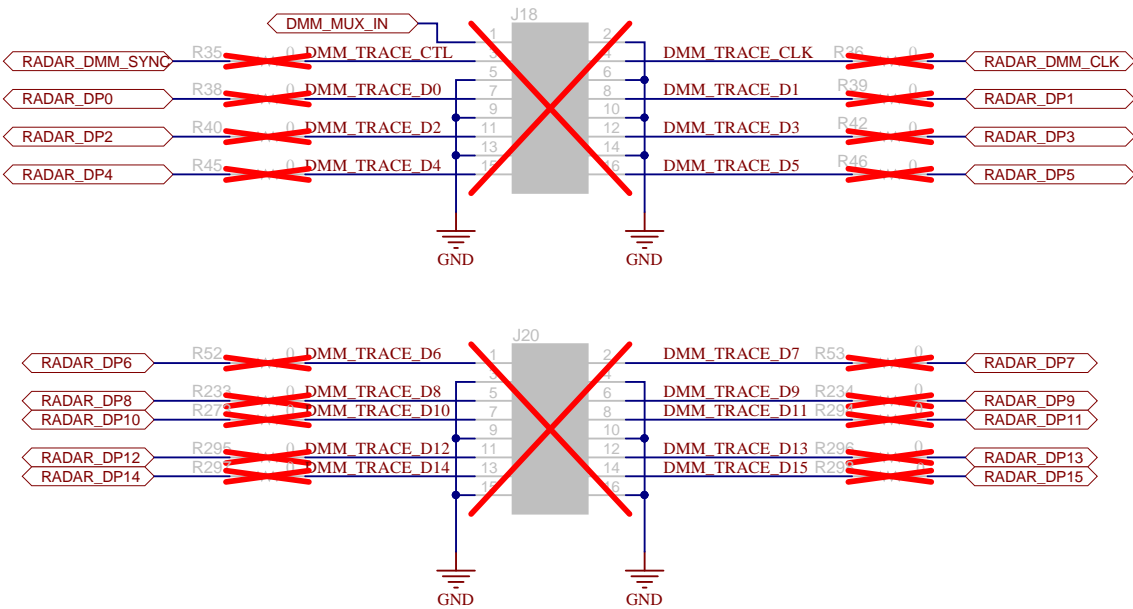
HD CONNECTOR FOR LVDS AND JTAG



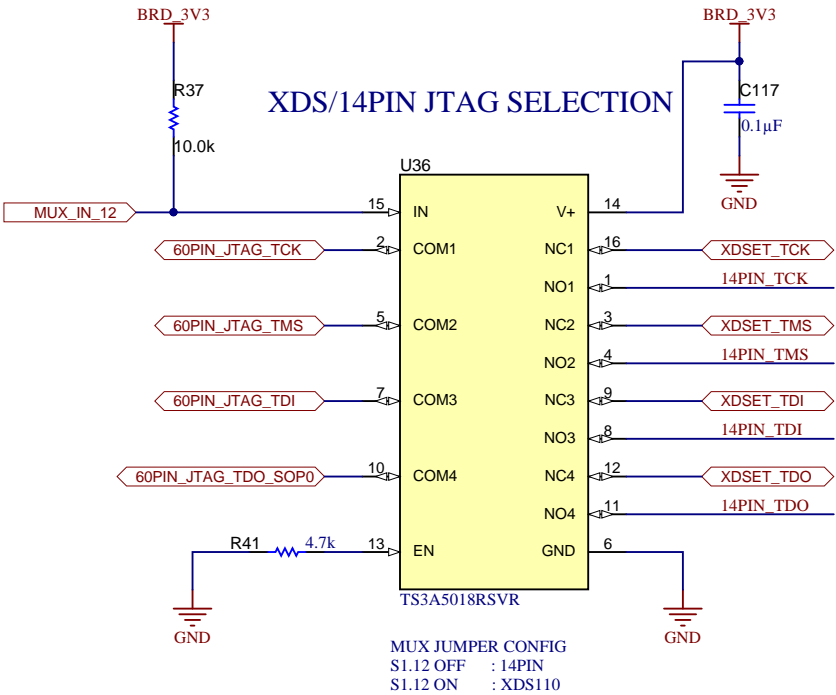
JTAG DEBUG CONNECTOR



JTAG TRACE DMM INTERFACE HEADER

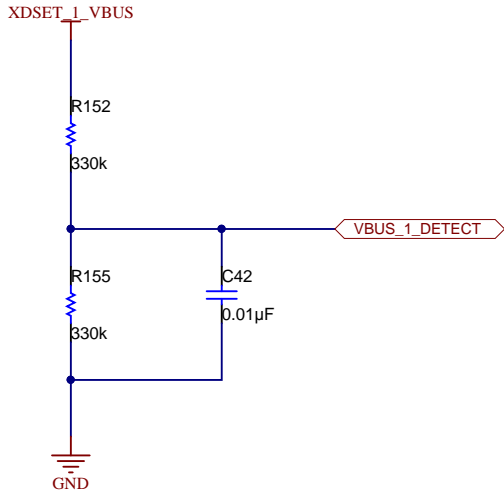
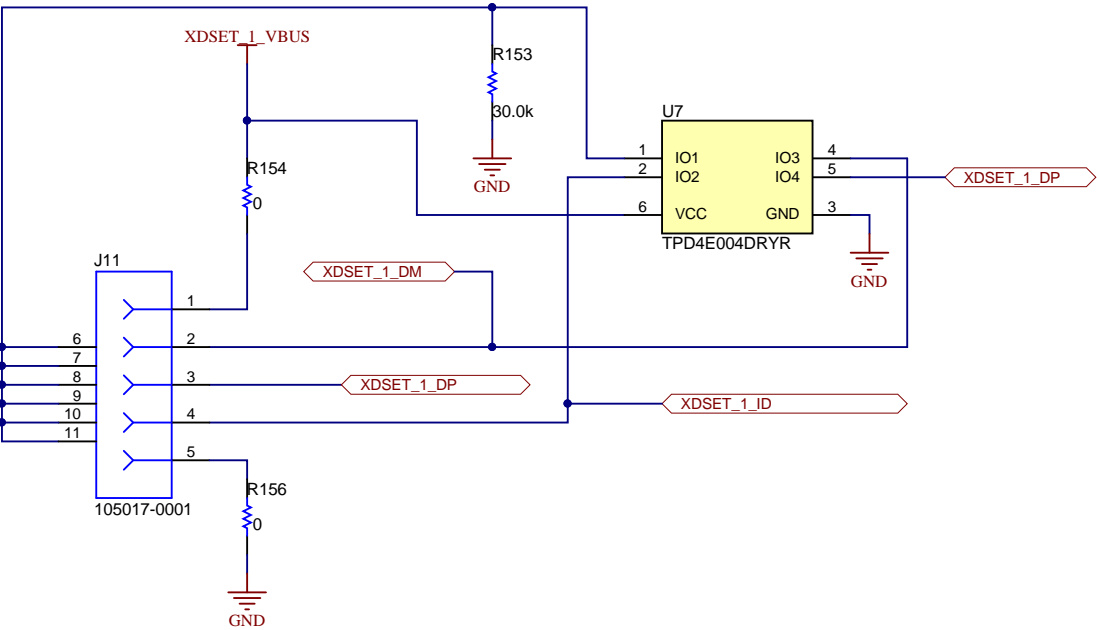
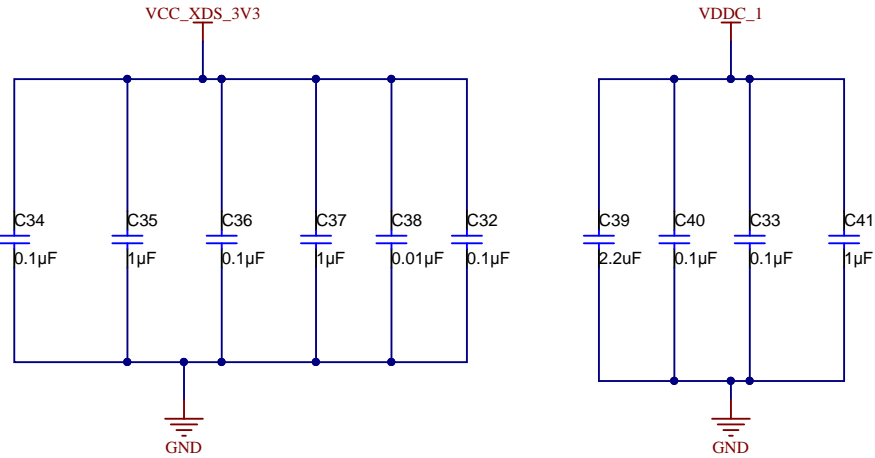
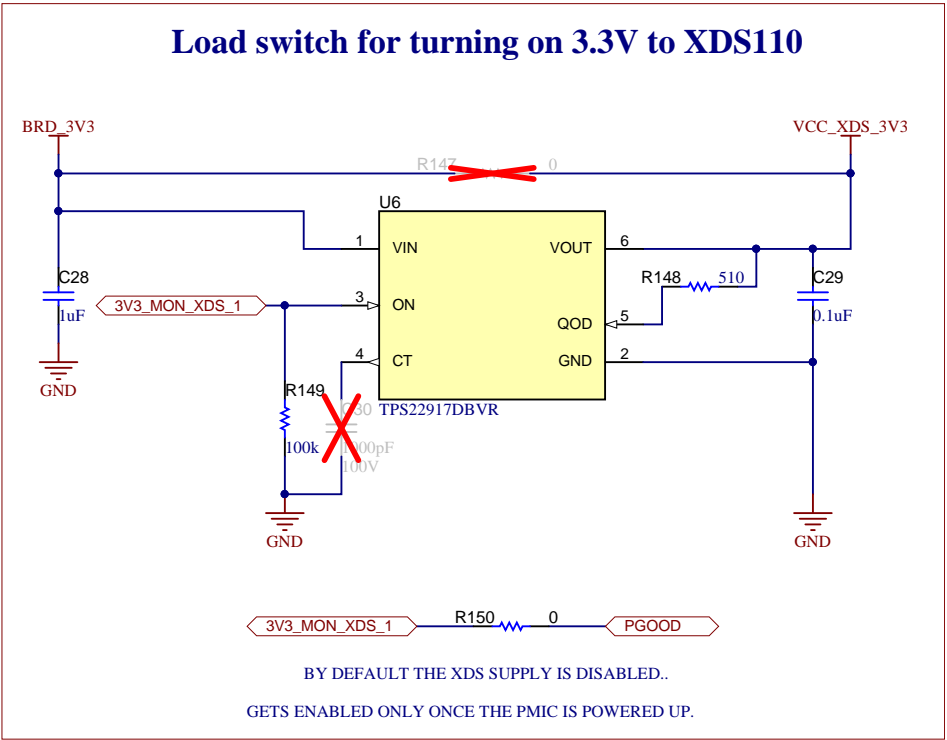


XDS/14PIN JTAG SELECTION

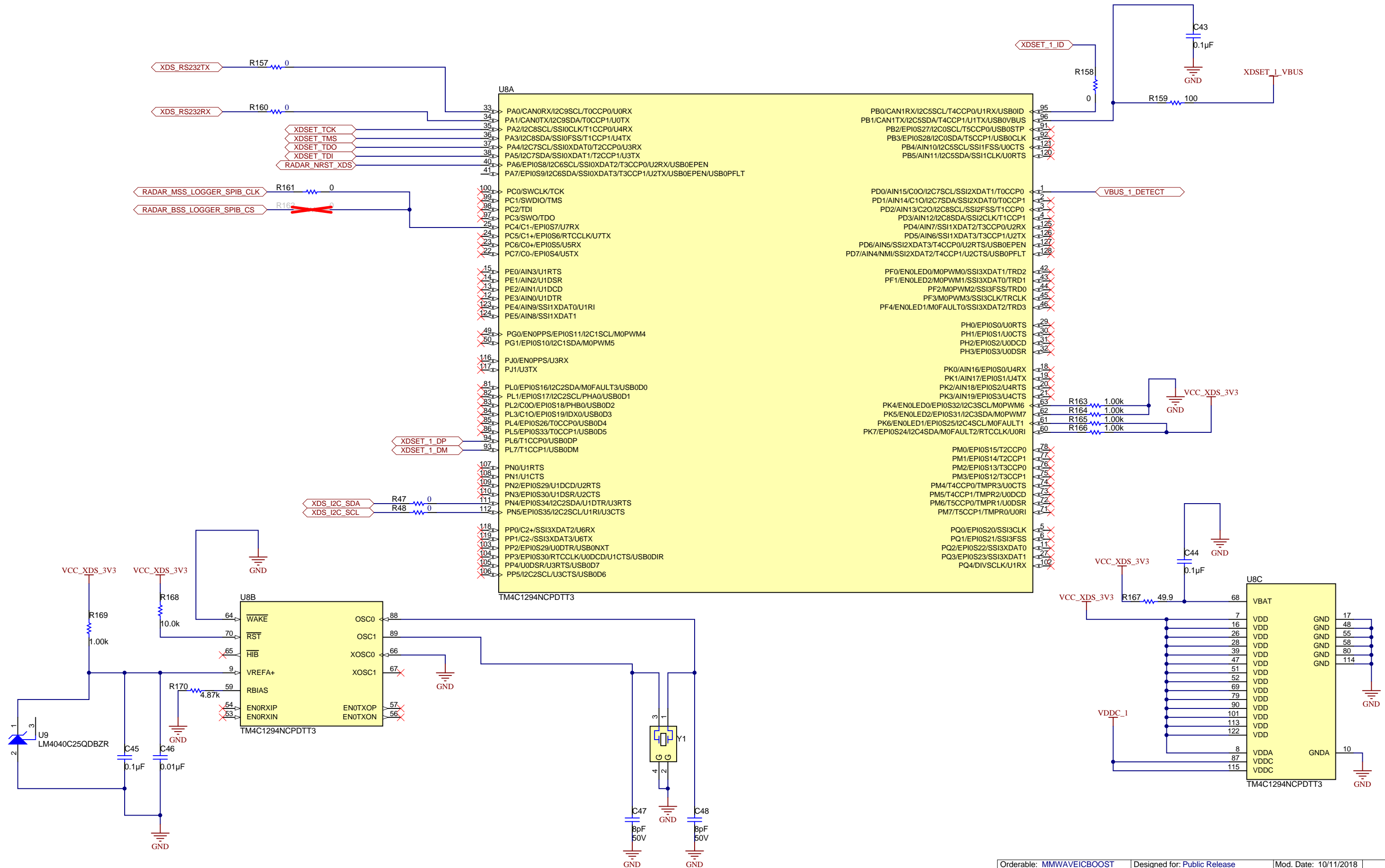


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XDS110(1/2)



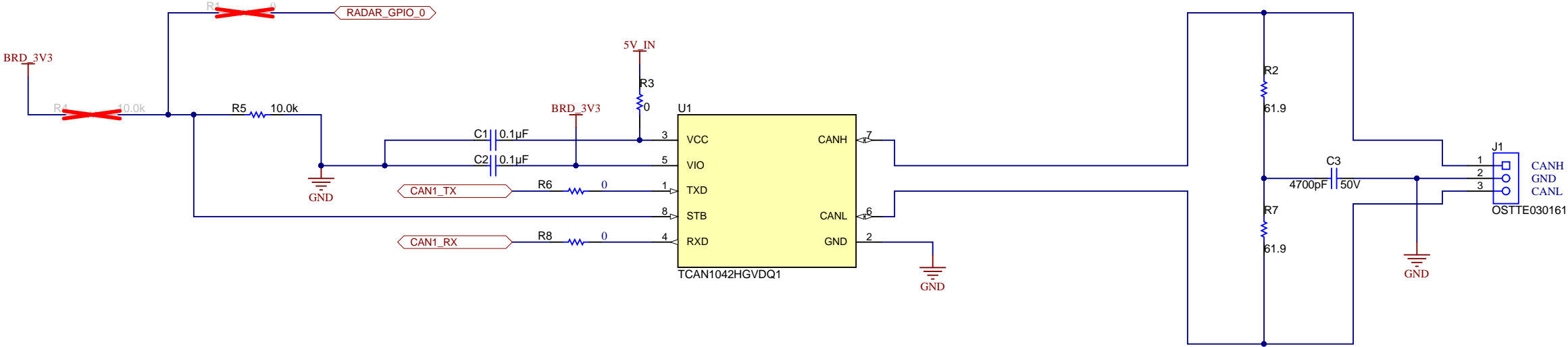
# XDS110(2/2)



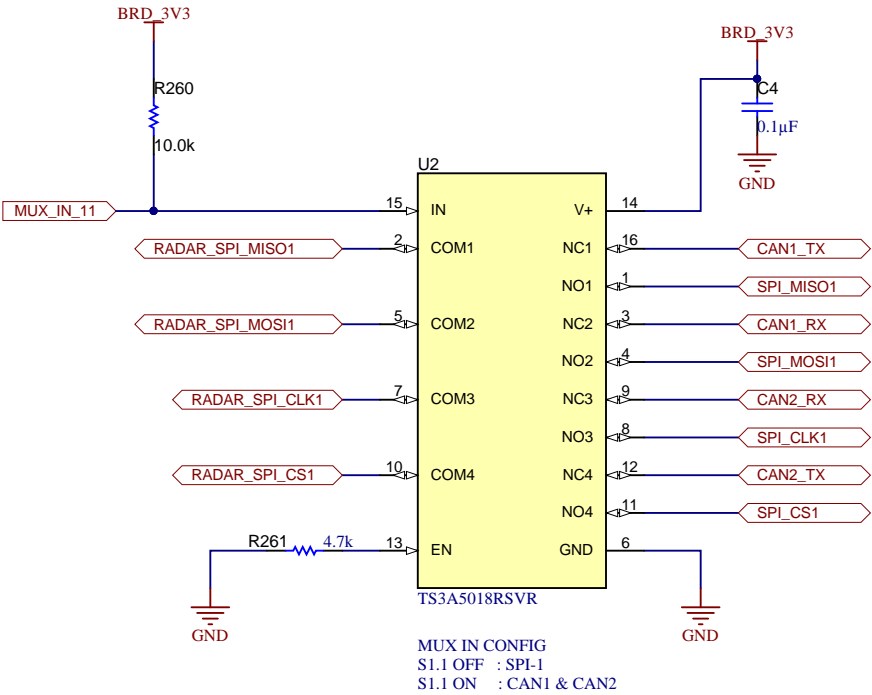
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SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">12</a> of <a href="#">22</a>	
Drawn By:	File: <a href="#">PROC074B_XDS110_Interface_1B.SchDoc</a>	Size: B	
Engineer: <a href="#">Chethan Kumar Y.B</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2018	



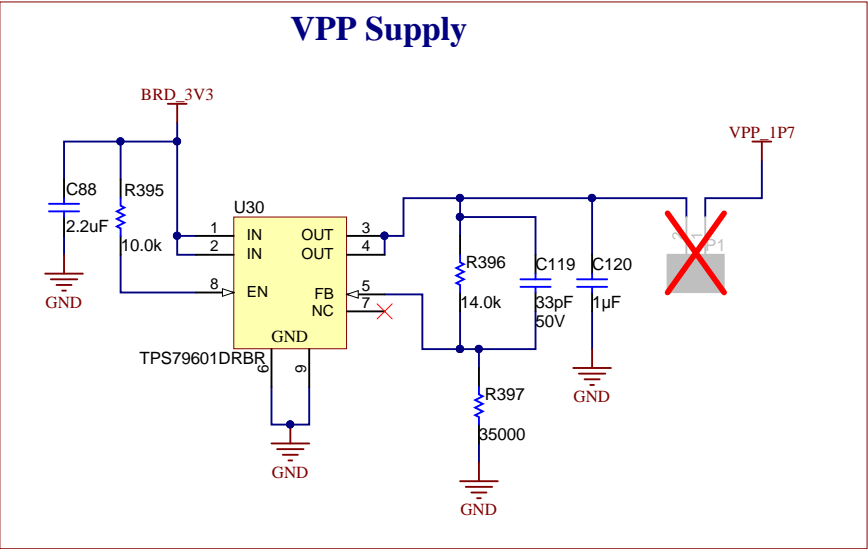
CAN\_FD TRANSCEIVER



CAN/SPI SELECTION

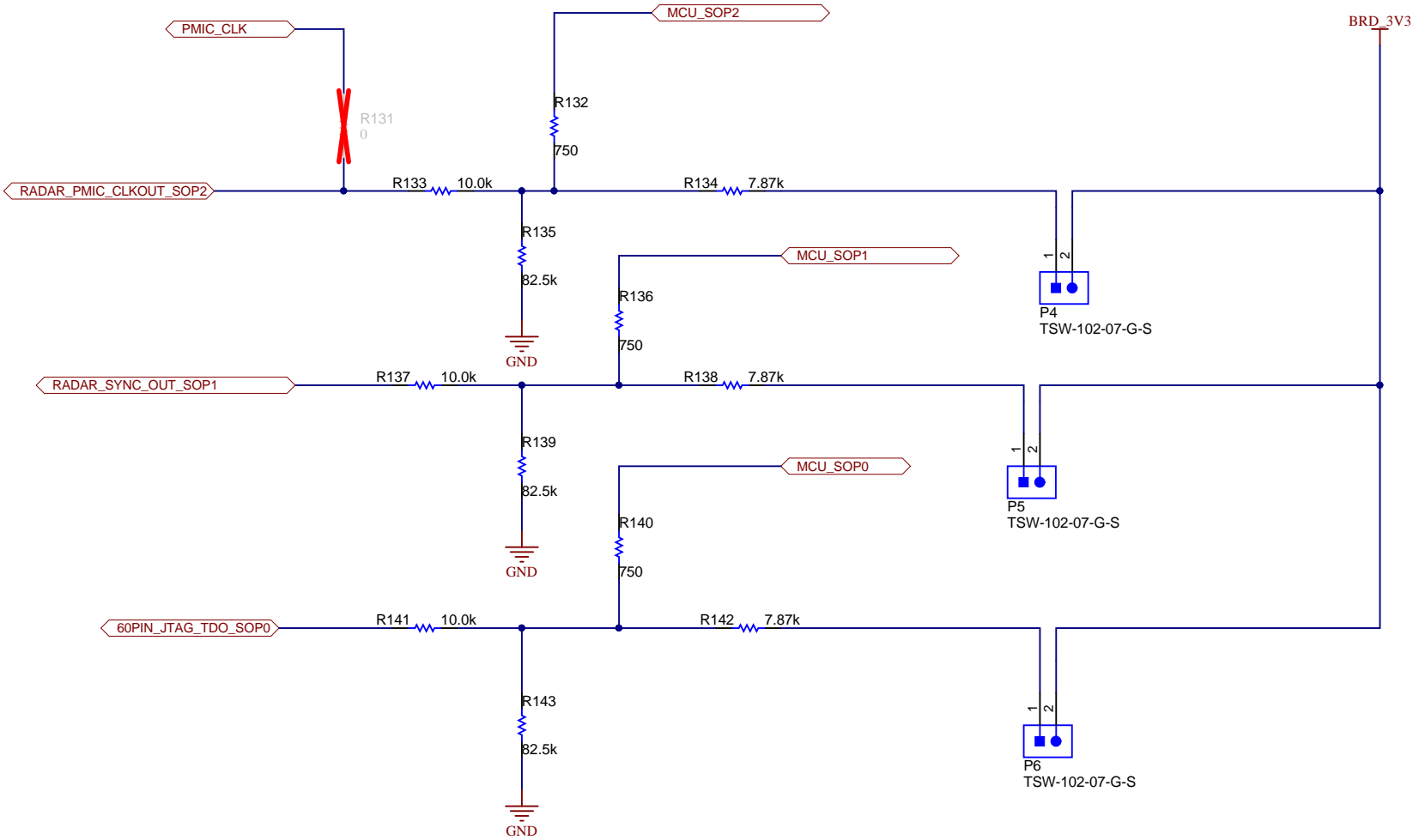


VPP Supply



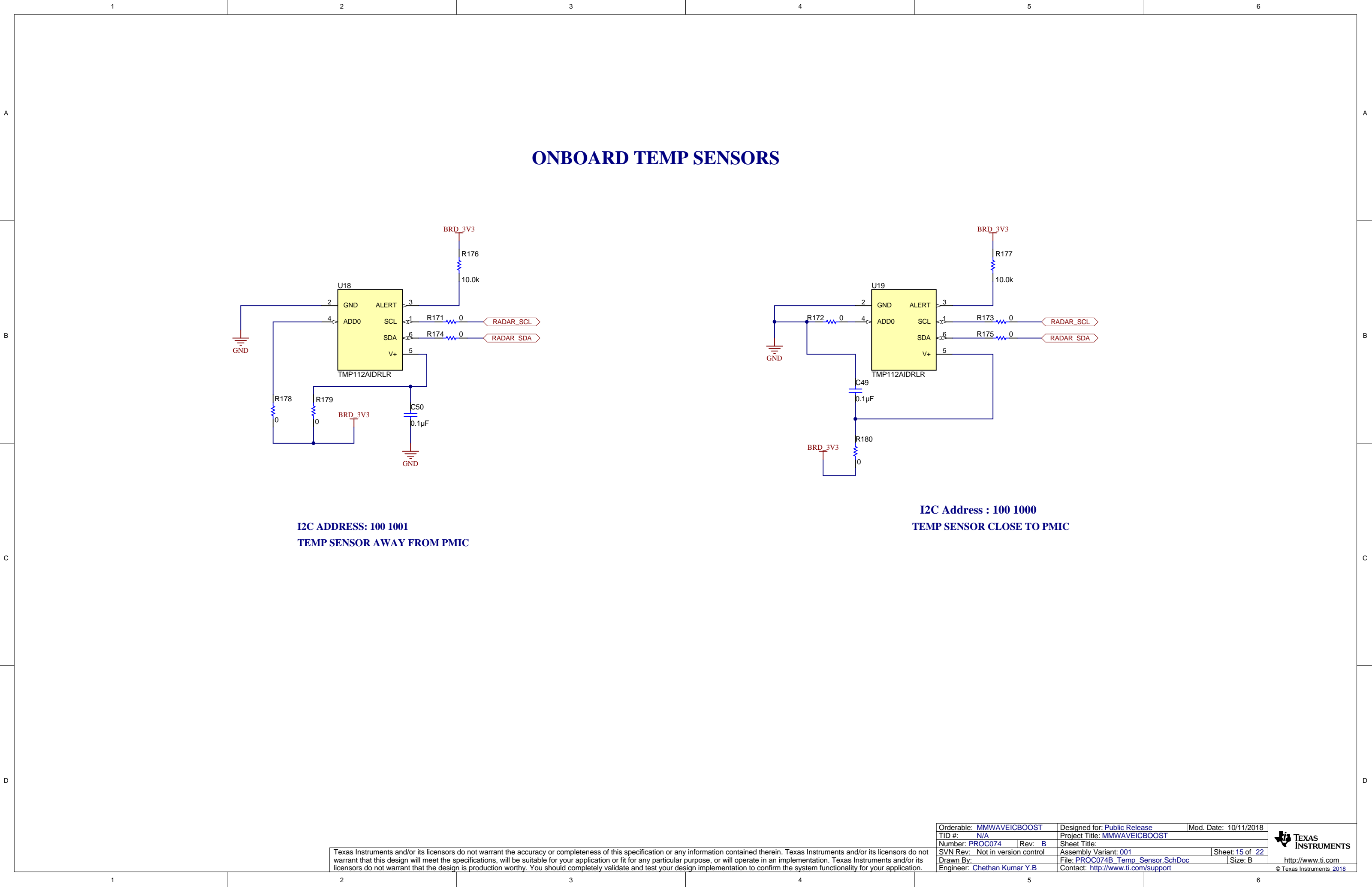
SOP HEADERS

SOP_MODE2	"011"	DEV/FLED
SOP_MODE4	"001"	FUNC -> DEFAULT VALUE FOR OUTPUTS
SOP_MODE5	"101"	DEV MANAGEMENT -> FOR FLASHING

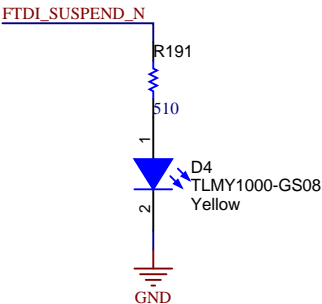
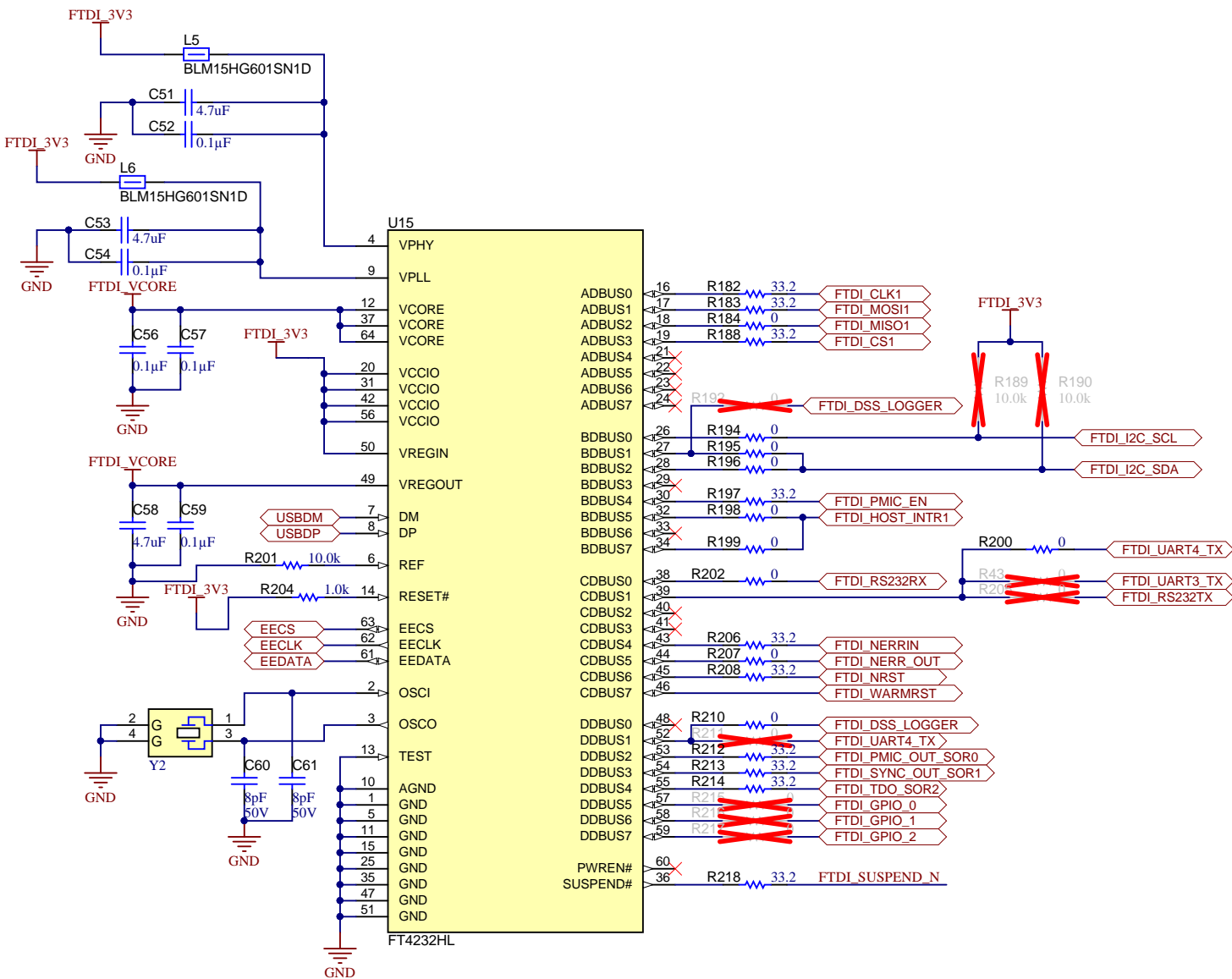


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Engineer: <a href="#">Chethan Kumar Y.B</a>	Contact: <a href="#">http://www.ti.com/support</a>	



FTDI INTERFACE



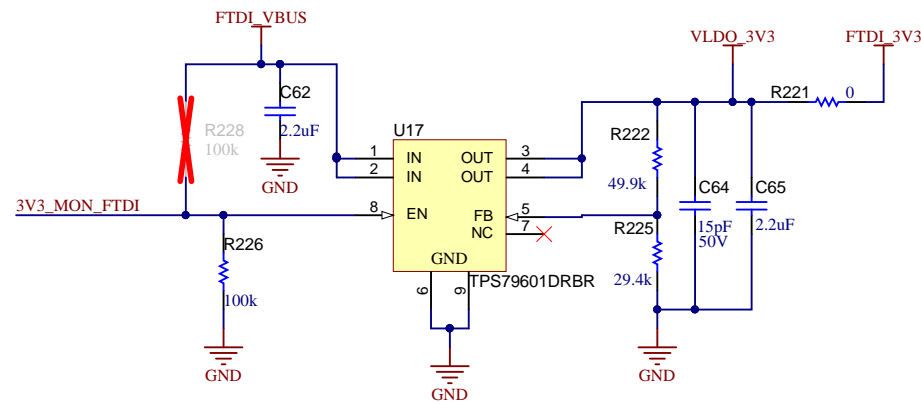
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Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 16 of 22
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Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

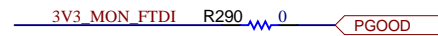
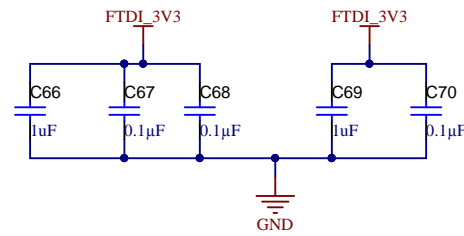


## FTDI POWER SECTION

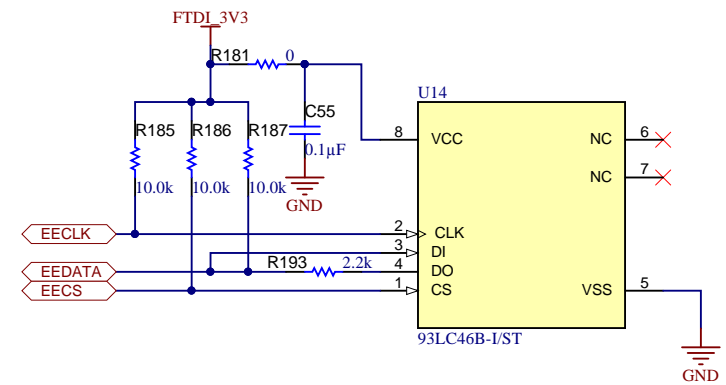
### 3.3V LDO FOR FTDI



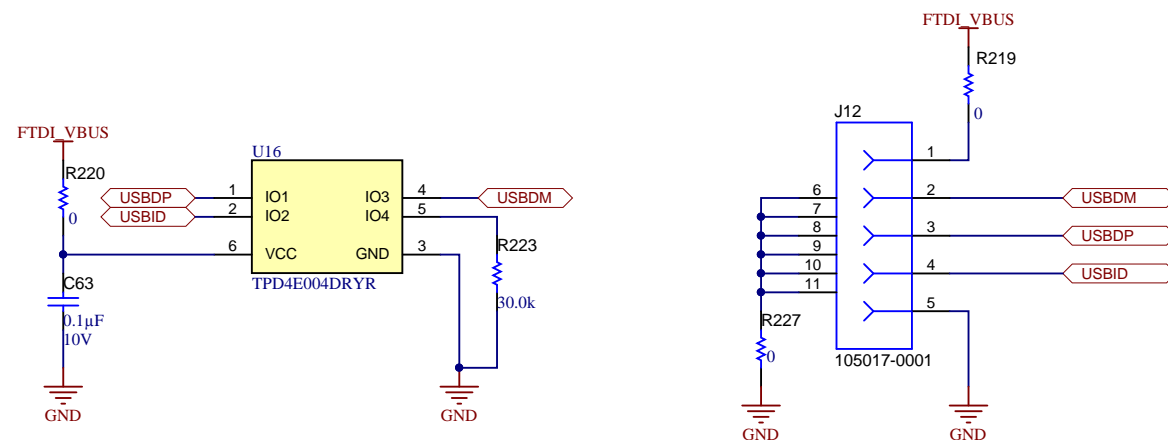
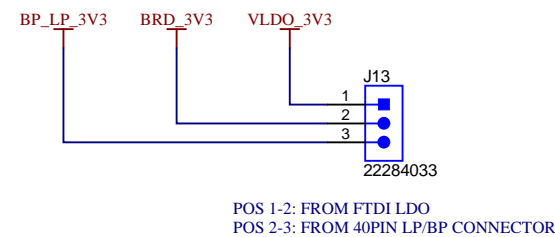
THIS 3.3V LDO WILL TURN ON  
EITHER FOR IWR6XXX MODULE BOARD  
OR CARRIER BOARD IS POWERED UP



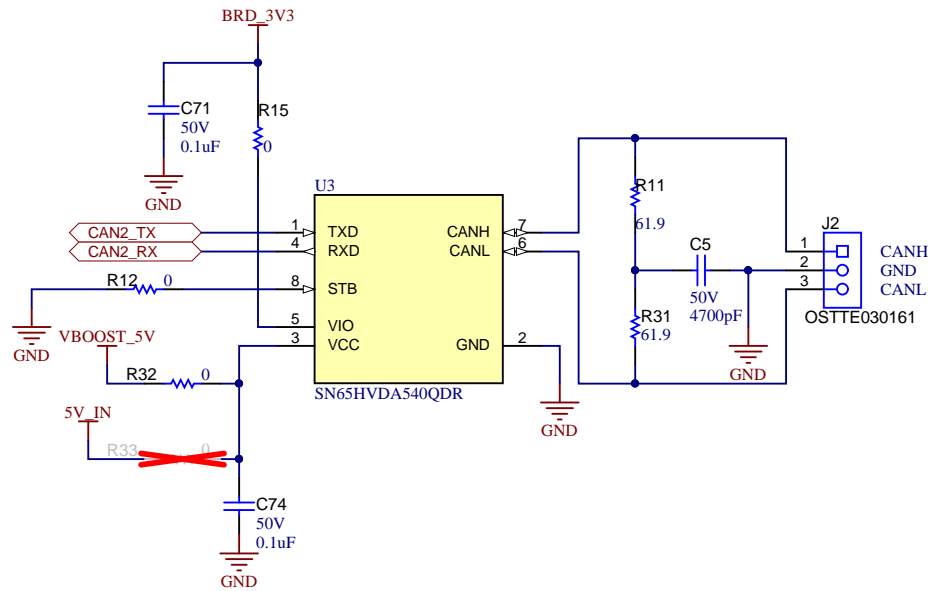
## FTDI EEPROM



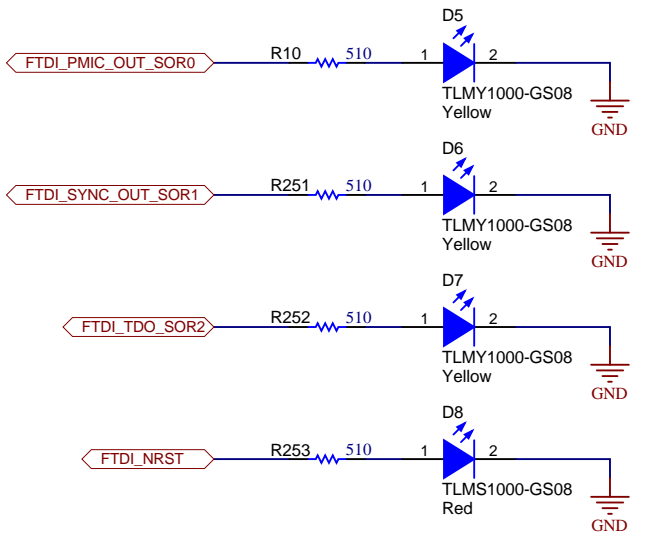
### 3.3V SUPPLY SELECTION JUMPER



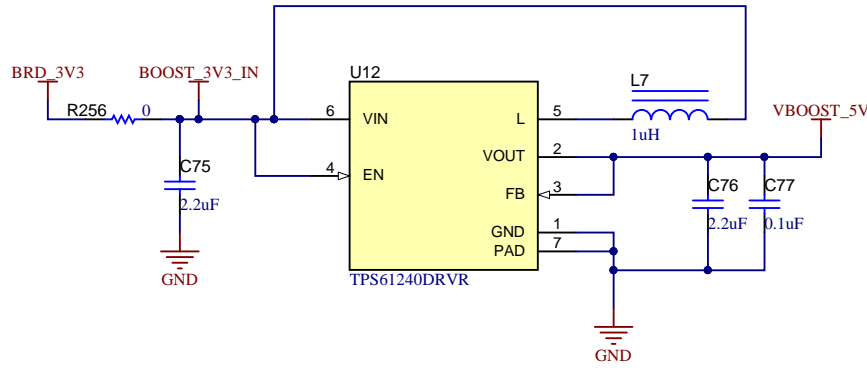
CAN TRANSCEIVER



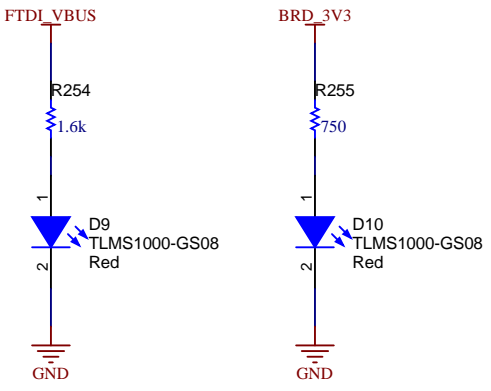
RESET & SOP STATUS INDICATION



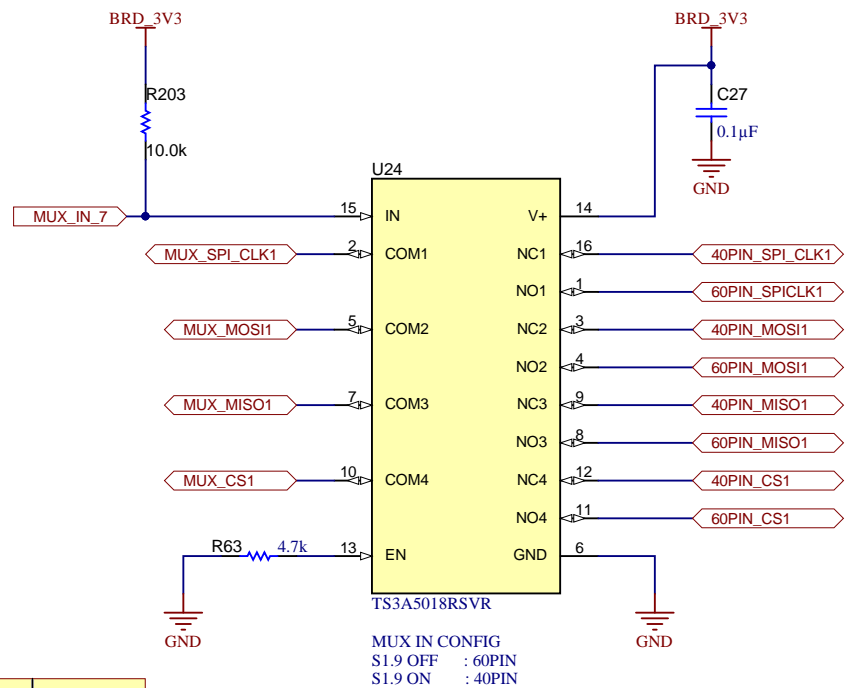
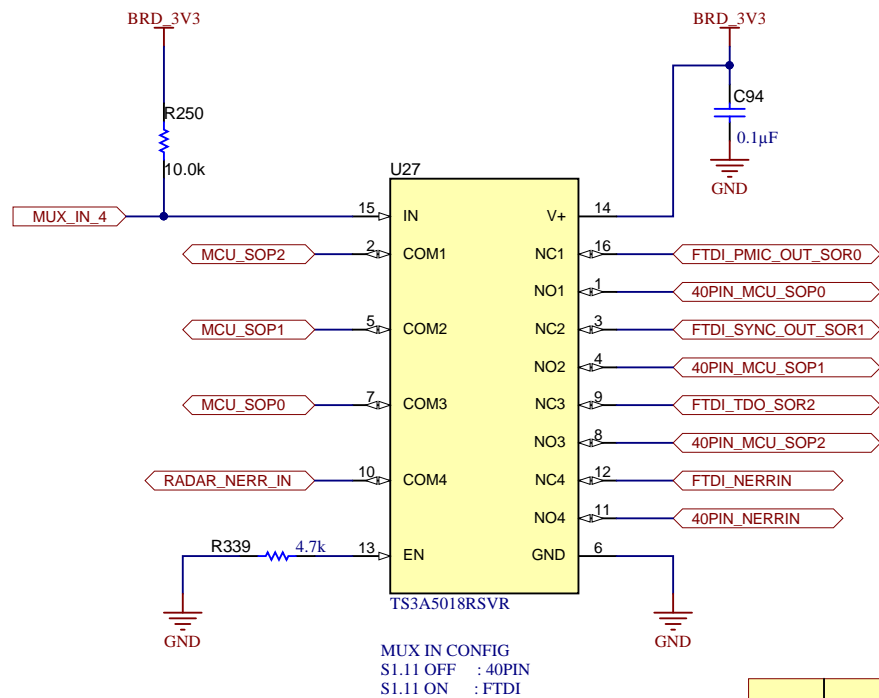
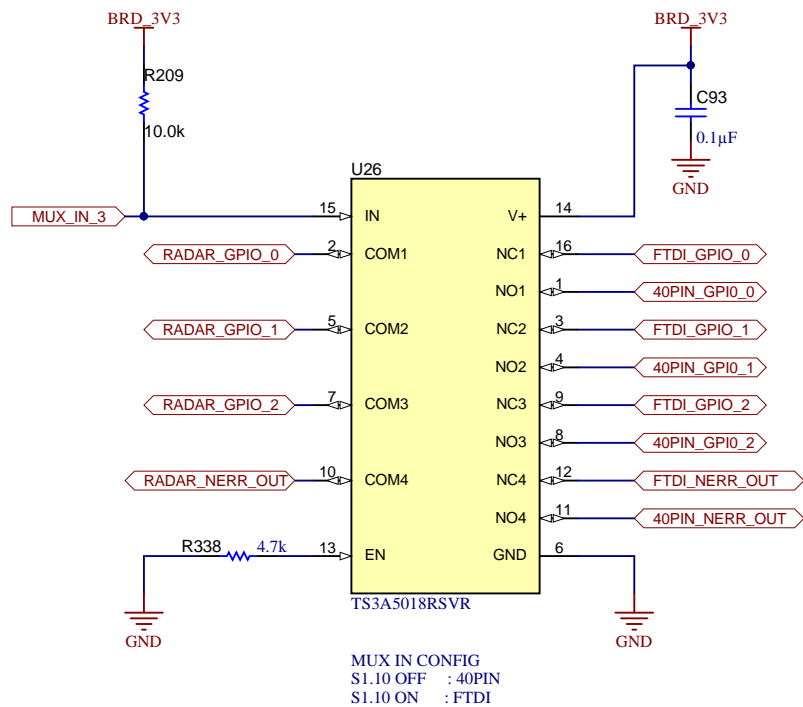
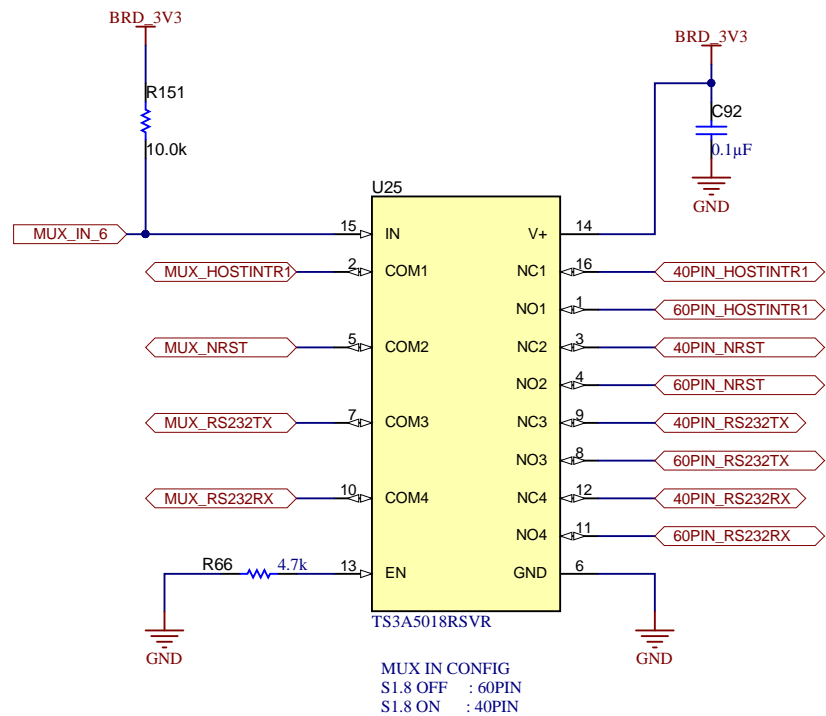
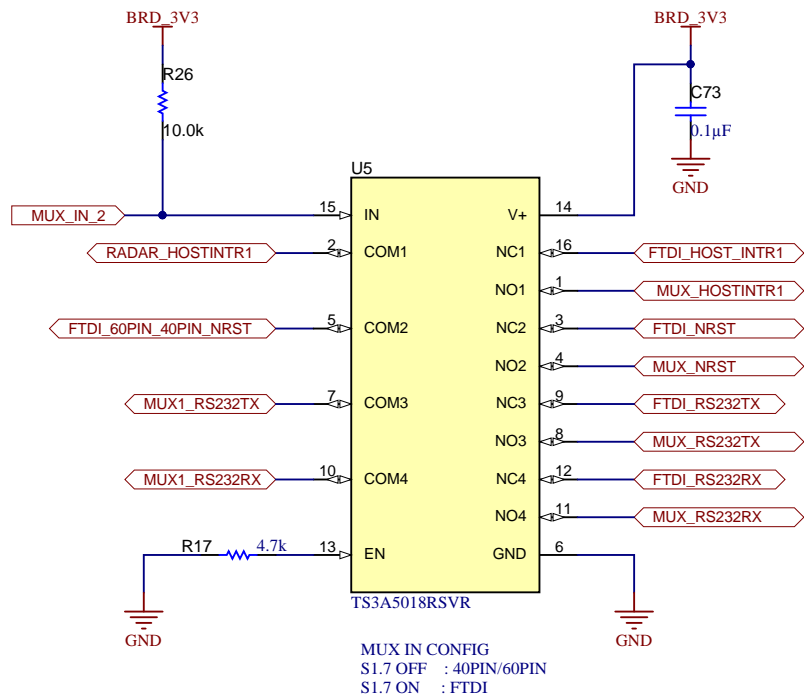
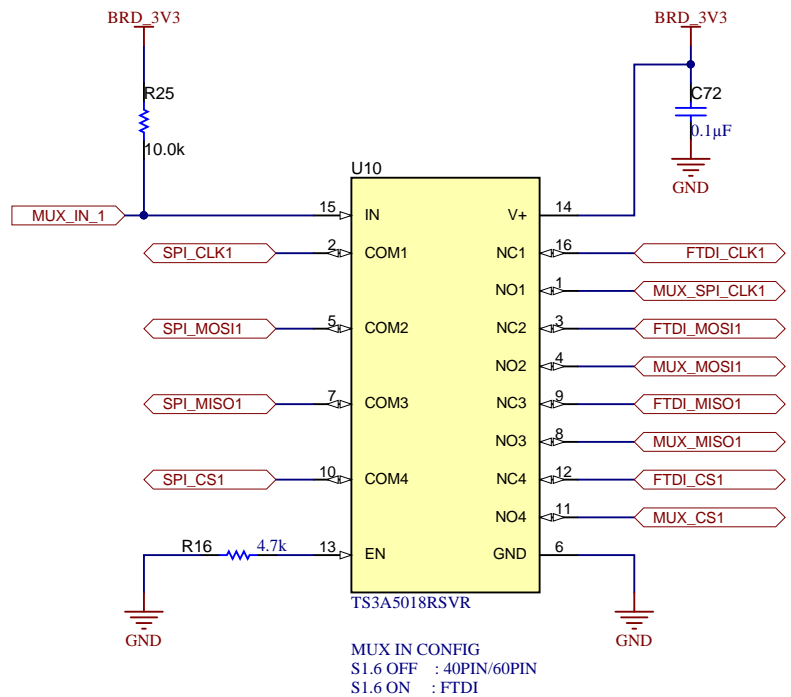
3.3V TO 5V BOOST CONVERTER



5V & 3.3V SUPPLY INDICATION



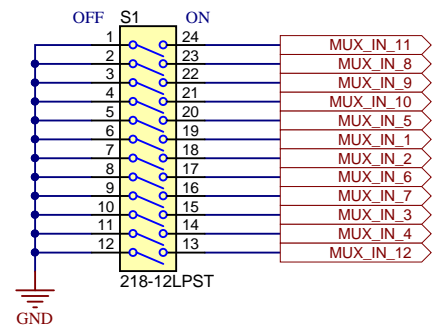
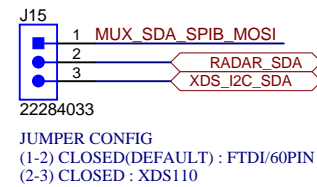
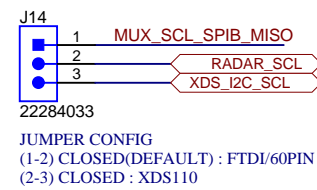
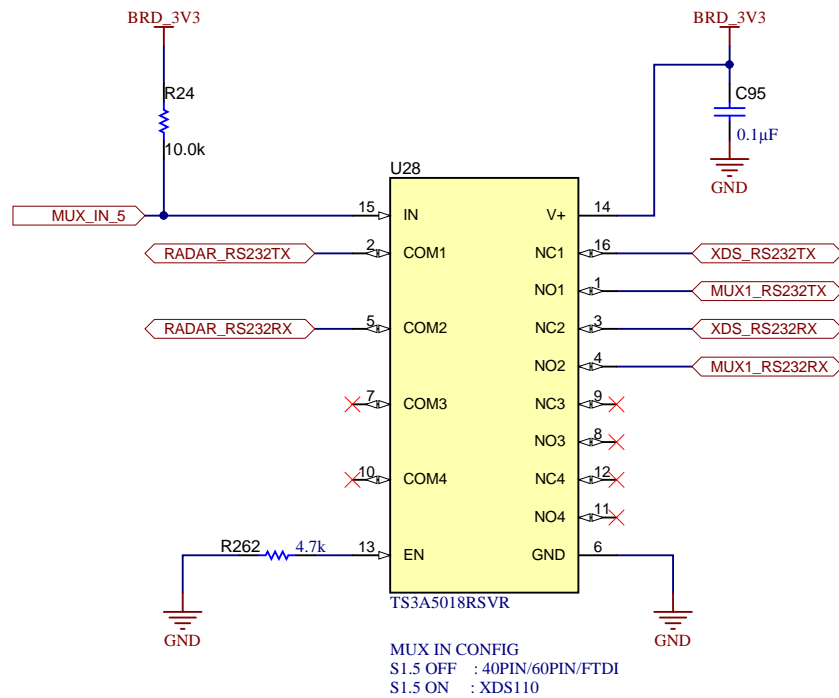
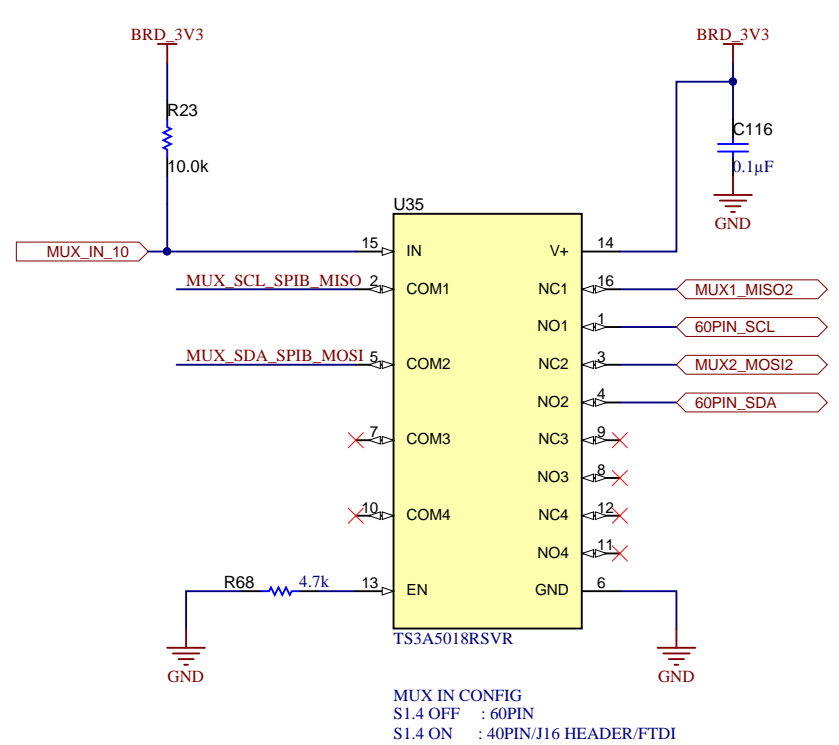
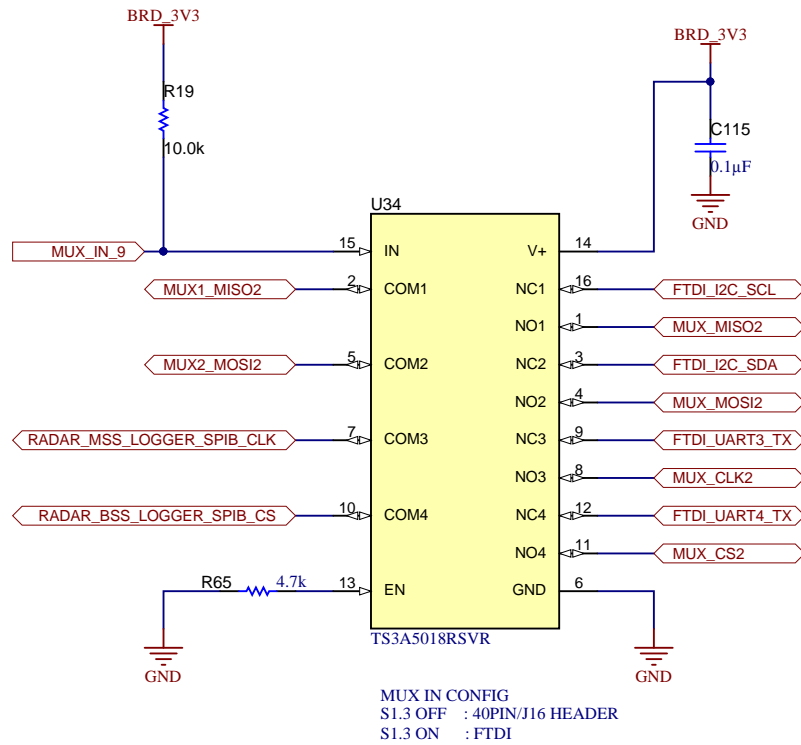
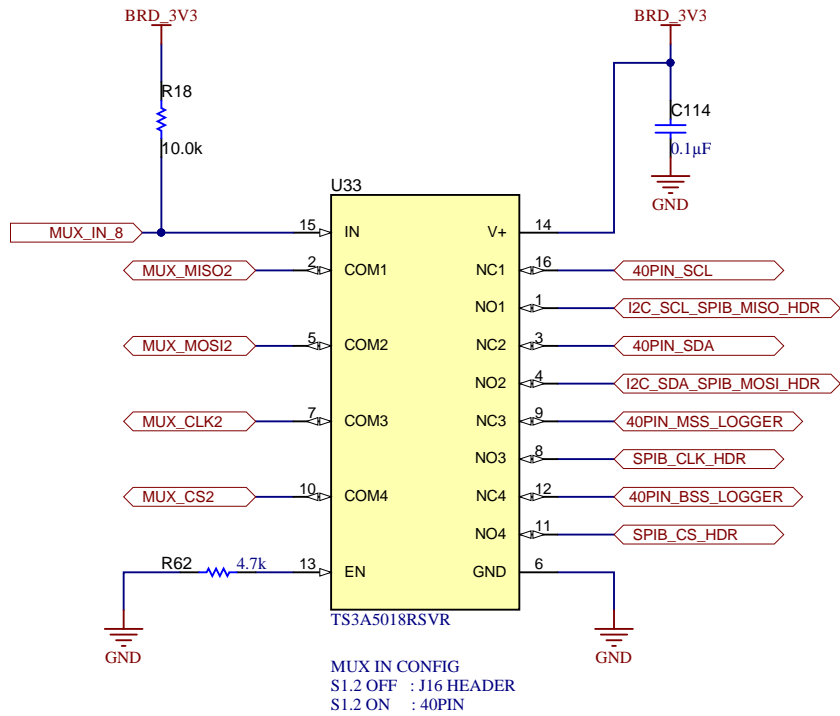
ANALOG MUX BETWEEN FTDI , DCA & 40PIN HDR 1/2



EN	IN	NO to COM/ COM to NO	NC to COM/ COM to NC
L	L	OFF	ON
L	H	ON	OFF
H	X	OFF	OFF

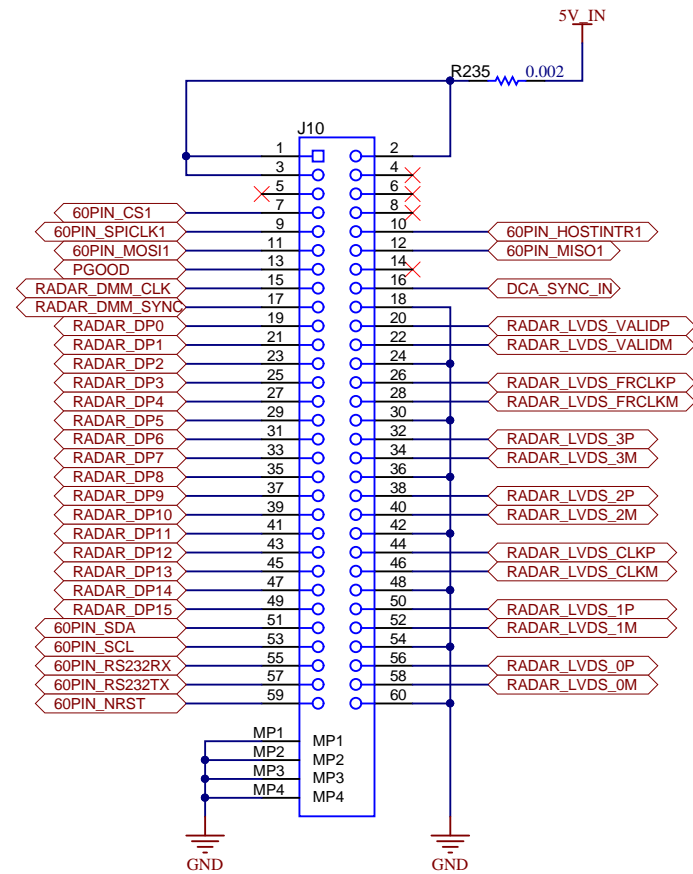
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# ANALOG MUX BETWEEN FTDI , DCA & 40PIN HDR (2/2)

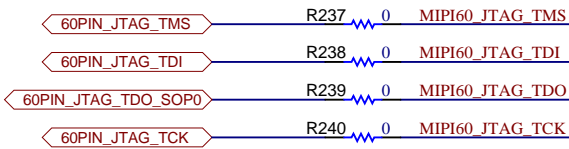
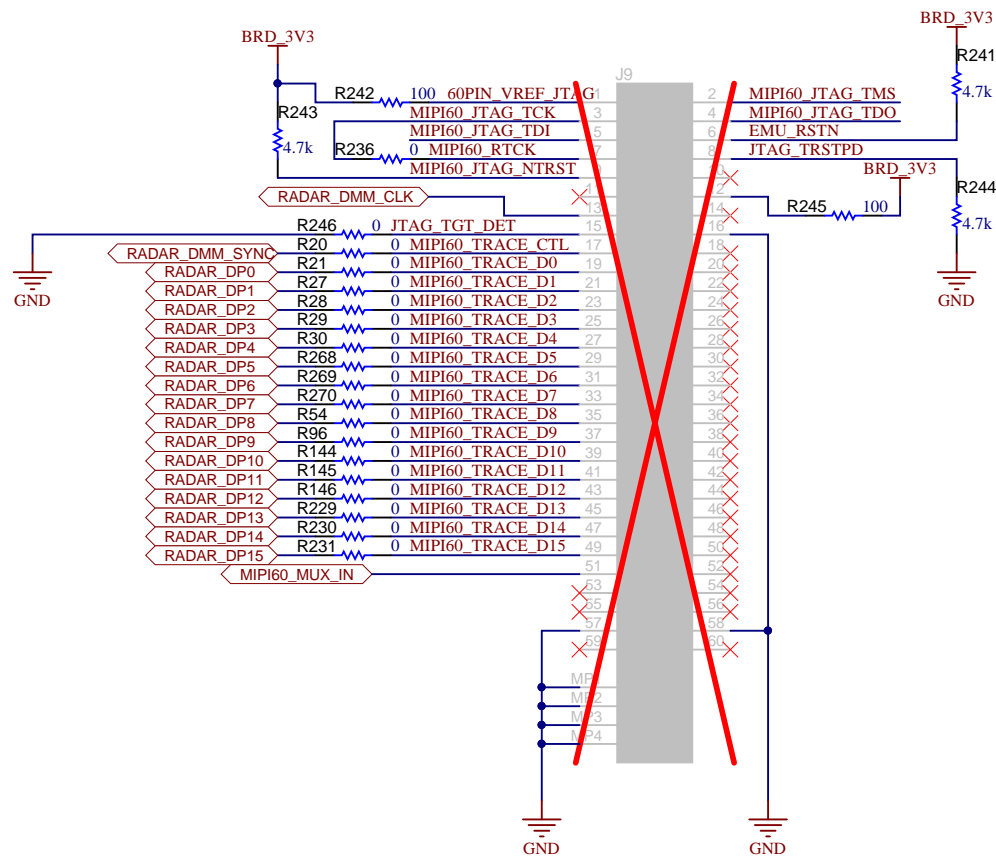




60PIN HD CONNECTOR FOR DCA1000



MIPI-60 DEBUG TRACE CONNECTOR



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