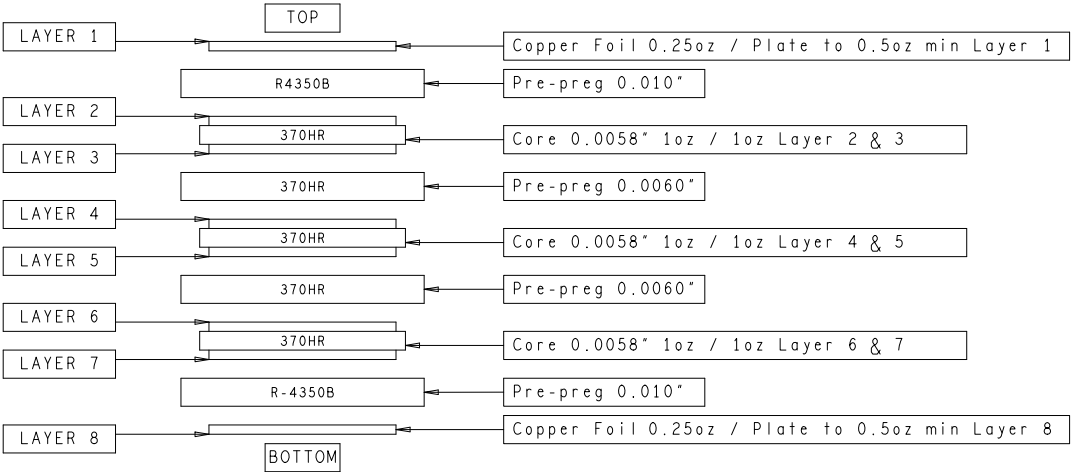


UNLESS OTHERWISE SPECIFIED. ALL NOTES ARE APPLICABLE.

1. APPLICATION DESIGN, MANUFACTURING AND INSPECTION DOCUMENTS.
IPC-2221A & IPC-2222 / DESIGN STANDARD FOR RIGID PRINTED CIRCUIT BOARDS AND RIGID PRINTED BOARD ASSEMBLIES.
IPC-6012B / QUALIFICATION AND PERFORMANCE SPECIFICATION FOR RIGID PRINTED BOARD.
IPC-A-600G / ACCEPTABILITY OF PRINTED BOARDS.
2. VIA 8, 10 & 12MIL SIZES APPLY AFTER PLATING. TOLERANCE TO BE +.003/- .010.
HOLE SIZE APPLY AFTER PLATING. TOLERANCE TO BE +/- .003.
3. REGISTRATION TOLERANCE: ARTWORK +/- .002.
ALL HOLE CENTERS +/- .005 FROM DIMENSION DATUM.
4. MINIMUM COPPER WALL THICKNESS SHALL BE .001 INCH.
FOR ALL PLATED THROUGH HOLES. BREAKOUT NOT ALLOWED.
5. PROCESS AND MATERIAL MUST CONFORM TO UL 796. MATERIAL MUST MEET OR EXCEED UL FLAMMABILITY RATING 94V-0.
MATERIAL: MULTI-LAYER (SEE DETAIL 'A')
SEE LAYER STACKUP FOR ALL PRE-PREG & CORE THICKNESSES, COPPER OZ AND MATERIAL. FINISHED BOARD THICKNESS: .062 +/- 10%
6. MANUFACTURE'S UL MARKING, FLAMMABILITY RATING, LOGO AND DATE CODE TO BE PLACED IN SILKSCREEN ON BOTTOM SIDE OF THE BOARD.
7. SMOBC/IMMERSION GOLD: 2 - 8 uIN OVER 118-236 uIN NICKEL PLATING.
8. SOLDERMASK BOTH SIDES USING TAIYO (OR EQUIVALENT)
COLOR = RED (0.001 TO 002" THICK OVER METAL.
9. SILKSCREEN BOTH SIDES USING WHITE NPI LEADFREE.
REGISTRATION TOLERANCE TO BE +/- .005.
INK IS NOT ALLOWED ON EXPOSED PLATED AREA.
10. P.C. BOARD TO BE FREE OF DIRT, OIL, FINGER PRINTS, ETC.
11. BOARD WARPAGE: WARP AND TWIST SHALL NOT EXCEED .007 INCH PER INCH MEASURED AT ANY LOCATION OR DIRECTION ON THE BOARD.
12. BOARD MUST BE 100% ELECTRICALLY TESTED TO ENSURE NO SHORTS OR OPEN CIRCUITS AT 20V.

13. ALL OUTER LAYERS USING A 19MIL TRACE WIDTH SHALL BE 50 OHMS SINGLE ENDED +/- 10%.
14. ALL OUTER LAYERS USING A 9MIL TRACE WIDTH AND 6MIL SPACING SHALL BE 100 OHMS DIFFERENTIAL +/- 10%.
15. MINIMUM COPPER CONDUCTOR WIDTH IS: 4MIL.
MINIMUM COPPER CONDUCTOR SPACING IS: 5MIL.
16. ALL INNER LAYER UNCONNECTED PADS SHALL BE REMOVED.
17. PWB MUST BE ROHS COMPLIANT AND SURVIVE LEAD FREE ASSEMBLY.
MAX REFLOW OF 230 DEGREES C (6 PASSES).
18. ALL THROUGH VIAS TO BE PLUGGED WITH NON-CONDUCTIVE EPOXY MATERIAL.
PLUGGED VIAS TO BE PLATED AFTER PLUGGING TO PRESENT FLAT SURFACE TO DEVICE.
NO POTHOLE.

USE DDI/VIASYSTEMS STACKUP JOB NAME: DAC38J84 R04350
THIS STACK UP DETAIL IS FOR REFERENCE ONLY



DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
*	8.0	PLATED	221
.	10.0	PLATED	1424
-	10.0	PLATED	3
-	12.0	PLATED	1754
◆	15.0	PLATED	80
•	38.0	PLATED	64
ε	40.0	PLATED	44
g	62.0	PLATED	1
g	67.0	PLATED	4
⊕	106.0	PLATED	2
⊗	120.0	PLATED	2
⊙	140.0	PLATED	1
"	39.0	NON-PLATED	2
v	50.0	NON-PLATED	2
v	53.0	NON-PLATED	24
Y	125.0	NON-PLATED	10

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES +/- .XX +/- .01 +/- .XXX +/- .005 +/-			CONTRACT NO.		TEXAS INSTRUMENTS INC.						
			APPROVALS		DATE		FABRICATION DRAWING DAC3XJ8X EVM				
			DRAWN JV SMITH		12-18-12						
			ENG M GUIBORD		12-18-12						
MATERIAL											
SEE NOTE 5											
FINISH					SIZE		CODE IDENT NO.		DRAWING NO.		REV.
SEE NOTE 7, 8, 9					B						D
DO NOT SCALE DRAWING					SCALE		NONE				SHEET 1 OF 1