

A

A

B

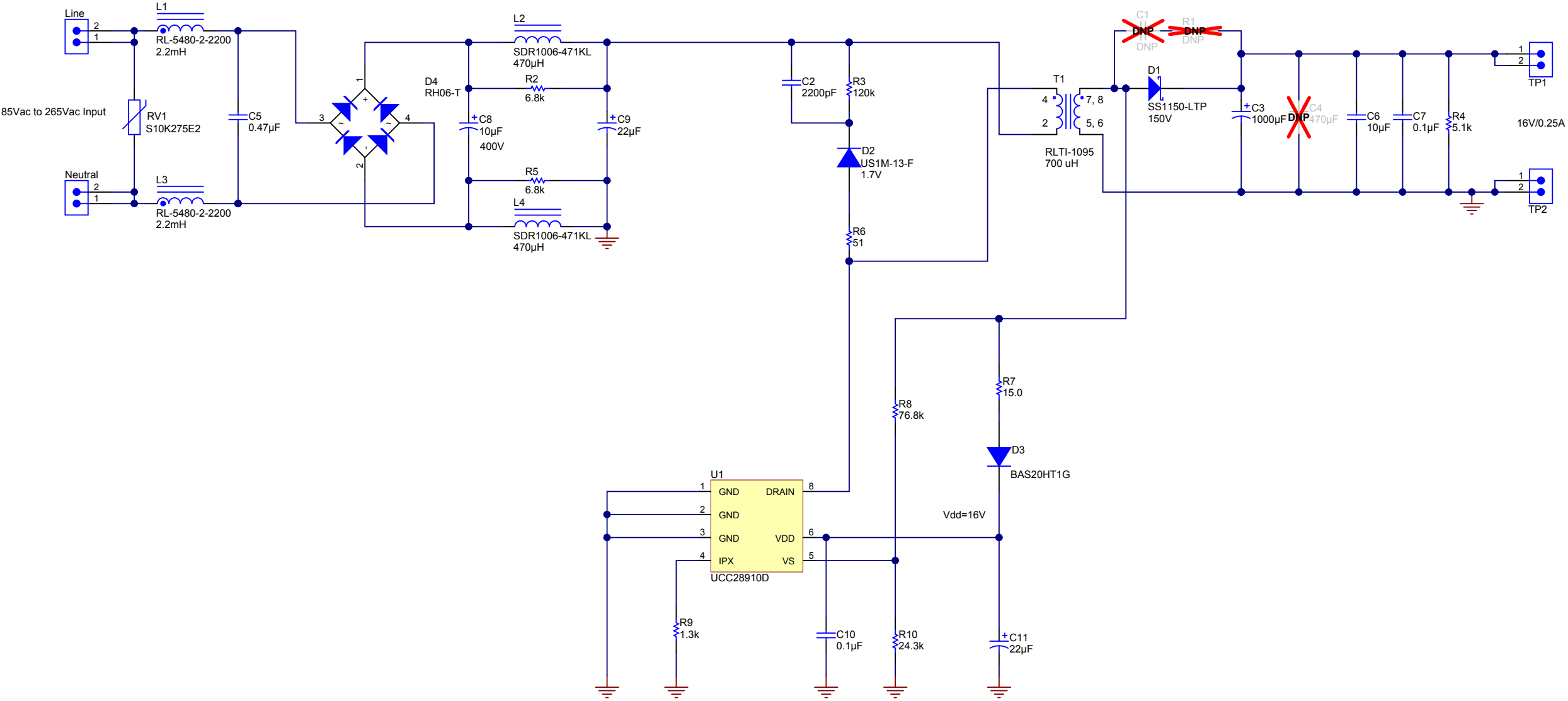
B

C

C




D

D



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A					A																
B					B																
C					C																
D					D																
<div><div><div>PCB Number: PMP9061 PCB Rev: A</div><div>PCB LOGO Texas Instruments</div><div>PCB LOGO ESD Susceptible</div><div>PCB LOGO DANGER HIGH VOLTAGE</div></div><div><div><div>Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div><div><table><tr><td>Number: PMP9061</td><td>Rev: A</td><td>Designed for: Public Release</td><td>Mod. Date: 7/25/2013</td></tr><tr><td>SVN Rev: Not in version control</td><td>Assembly Variant: 001</td><td colspan="2">Project Title: Universal input to 16V/0.25A output</td></tr><tr><td>Drawn By:</td><td>File: PMP9061_02.SchDoc</td><td>Sheet: 2 of 2</td><td>Size: B</td></tr><tr><td>Engineer: S Yu</td><td>Contact: http://www.ti.com/support</td><td colspan="2"> http://www.ti.com © Texas Instruments 2013</td></tr></table></div></div></div></div>						Number: PMP9061	Rev: A	Designed for: Public Release	Mod. Date: 7/25/2013	SVN Rev: Not in version control	Assembly Variant: 001	Project Title: Universal input to 16V/0.25A output		Drawn By:	File: PMP9061_02.SchDoc	Sheet: 2 of 2	Size: B	Engineer: S Yu	Contact: http://www.ti.com/support	 http://www.ti.com © Texas Instruments 2013	
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