

NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "P3P3V" represents connection to the +3.3V digital power plane.
2. The netname "P1P8V" represents connection to the +1.8V digital power plane.
3. The symbol ∇ represents connection to the digital ground plane.
4. A "Z" suffix on a signal name indicates an active low signal.
5. A "_S" suffix on a signal name indicates a trace that is between a driver and a series termination resistor.
6. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.

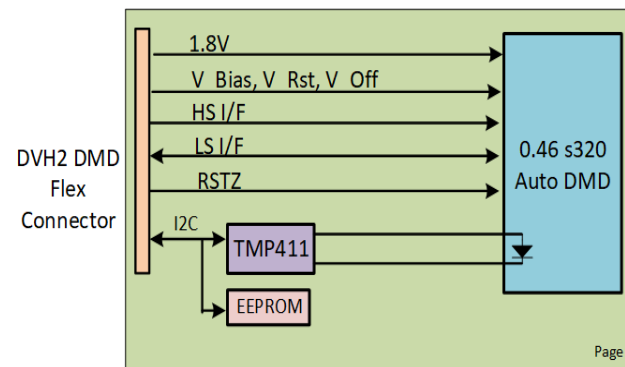
COMPUTER GENERATED DRAWING. DO NOT REVISE MANUALLY

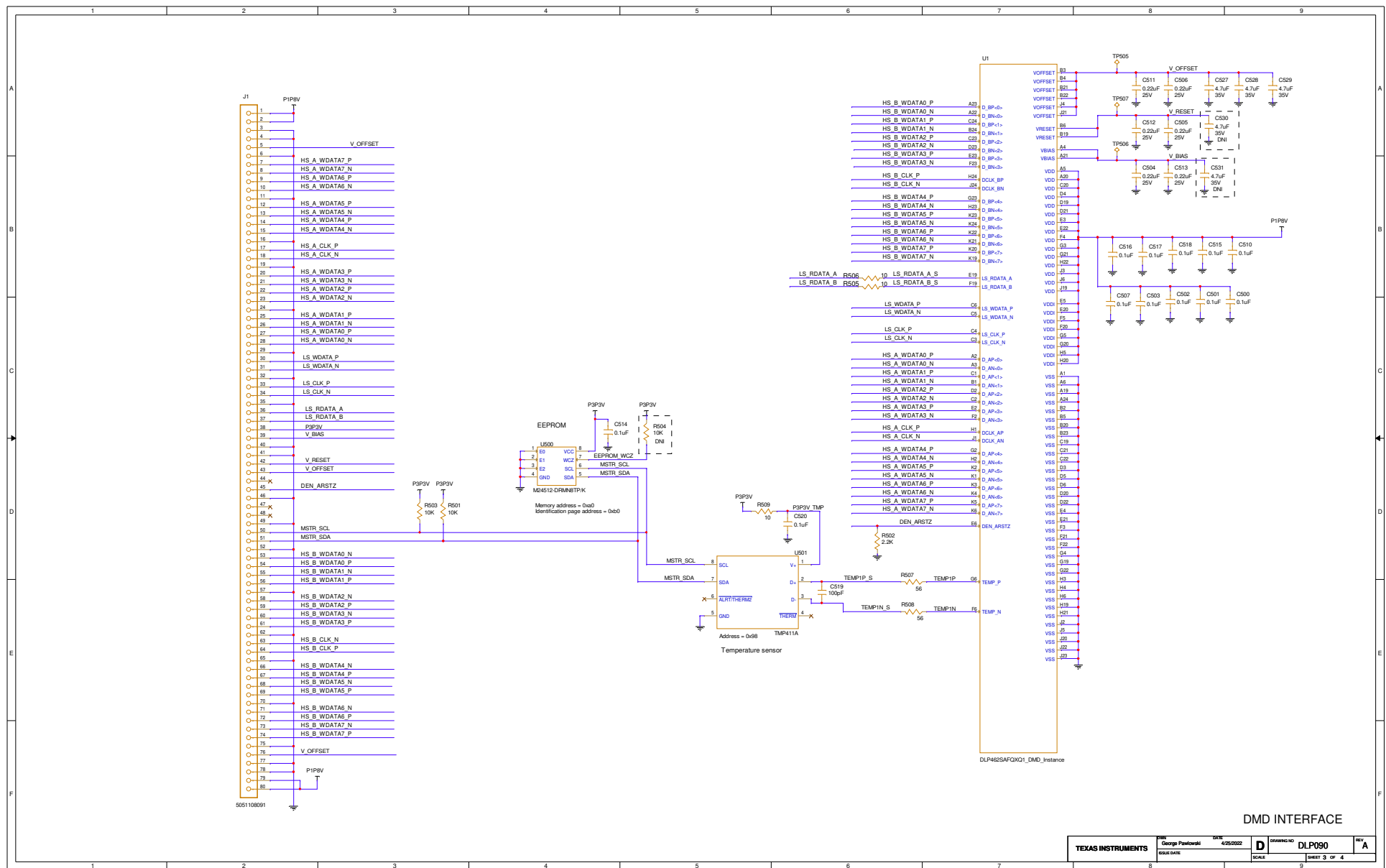
REVISIONS

| REV | DESCRIPTION | DATE | APPROVED |
|-----|-----------------|------------|----------|
| A | Initial Release | 02/14/2024 | |
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| | | ENGR | | | |
| | | SYST | | | |
| | | PRJ | | | |
| | | QA | | TITLE | |
| | | | | | ESD, DLP090 - DLP4620SQ1EVM DMD EVM |
| NEXT ASSY | USED ON | | | | D DRAWING NO DLP090 REV A |
| APPLICATION | | SW Cadence Capture 17.2 | | | SCALE SHEET 1 of 4 |





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| TEXAS INSTRUMENTS | OWN | DATE | C | DRAWING NO | DLP090 | REV | A |
| | George Pawlowski | 4/25/2022 | | | | | |
| | ISSUE DATE | SCALE | | SHEET 4 OF 4 | | | |