

\* Model Usage Notes:

\* A. Features have been modelled

- \* 1. Output Voltage Setting
- \* 2. EN on/off
- \* 3. Negative over current threshold
- \* 4. Overcurrent Protection(OCP)
- \* 5. 16 cycles of consecutive negative OCL results to device operating in PSM
- \* 6. Vin UVLO Protection

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\* B. Features have not been modelled

- \* 1. Operating Quiescent Current
- \* 2. Shutdown Current
- \* 3. Temperature dependent characteristics
- \* 4. Large Duty cycle operation

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\* C. Application Notes

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- \* 1. The parameter STEADY\_STATE has been used to reach the steady state faster
- \* Keep STEADY\_STATE = 0 to observe startup behaviour.
- \* Keep STEADY\_STATE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
- \* 2. When running with parameter STEADY\_STATE=1, use an initial condition of atleast 2V on the soft start capacitor
- \* to ensure SS\_END signal triggers internally.
- \* 3. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies.

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