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* Model Usage Notes:
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* A. Features have been modelled
* 1. Hiccup mode is modelled with the hiccup reset time of 1ms(actual is 135ms ,this was done to reduce simulation time)
* 2. FPWM mode of operation.
* 3. Output Voltage Setting
* 4. Current Sense and Positive Overcurrent Protection(OCP)
* 5. Prebias and bootstrap is modelled.
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* B. Features have not been modelled
* 1. Operating Quiescent Current
* 2. Shutdown Current
* 3. Temperature dependent characteristics
* 4. Inverting Buck Boost topology is not validated
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* C. Application Notes
* 1. The parameter MODE has been used to reach the steady state faster.
*    Keep MODE = 0 to observe startup behaviour.
*    Keep MODE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
* 2. The model is tested for corner conditions.
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