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* Model Usage Notes:
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* A. Features have been modelled
*   1. Fixed internal output voltage setting,Adjustable output voltage
*   2. Internal Fixed Soft-Start
*   3. RT pin Frequency selection
*   4. Operation mode selection using PFM_SYNC pin
*   5. Low-side FET Zero-Crossing
*   6. Current Sense and Positive Overcurrent Protection(OCP)
*   7. Internal Fixed compensation and External compensation
*   8. Power Good
*   9. Under Voltage Protection(UVP)
*  10. Out-Of-Bounds(OOB) Operation
*  11. Output Voltage Discharge
*  12. UVLO Protection
*  13. BOOT functionality
*  14. Remote Sense
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* B. Features have not been modelled
*   1. Operating Quiescent Current
*   2. Shutdown Current
*   3. Temperature dependent characteristics
*   4. Dropout mode
*   5. Active EMI filter
*   6. Dual Random Spread Spectrum
*   7. CNFG,SYNC,SYNCOUT pins are not modelled
*   8. Ground Pins have been tied to OV internally and hence model does not support Inverting
*       topologies.
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* C. Application Notes
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*   1. The parameter STEADY_STATE has been used to reach the steady state faster.
*       Keep STEADY_STATE = 0 to observe startup behaviour.
*       Keep STEADY_STATE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
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