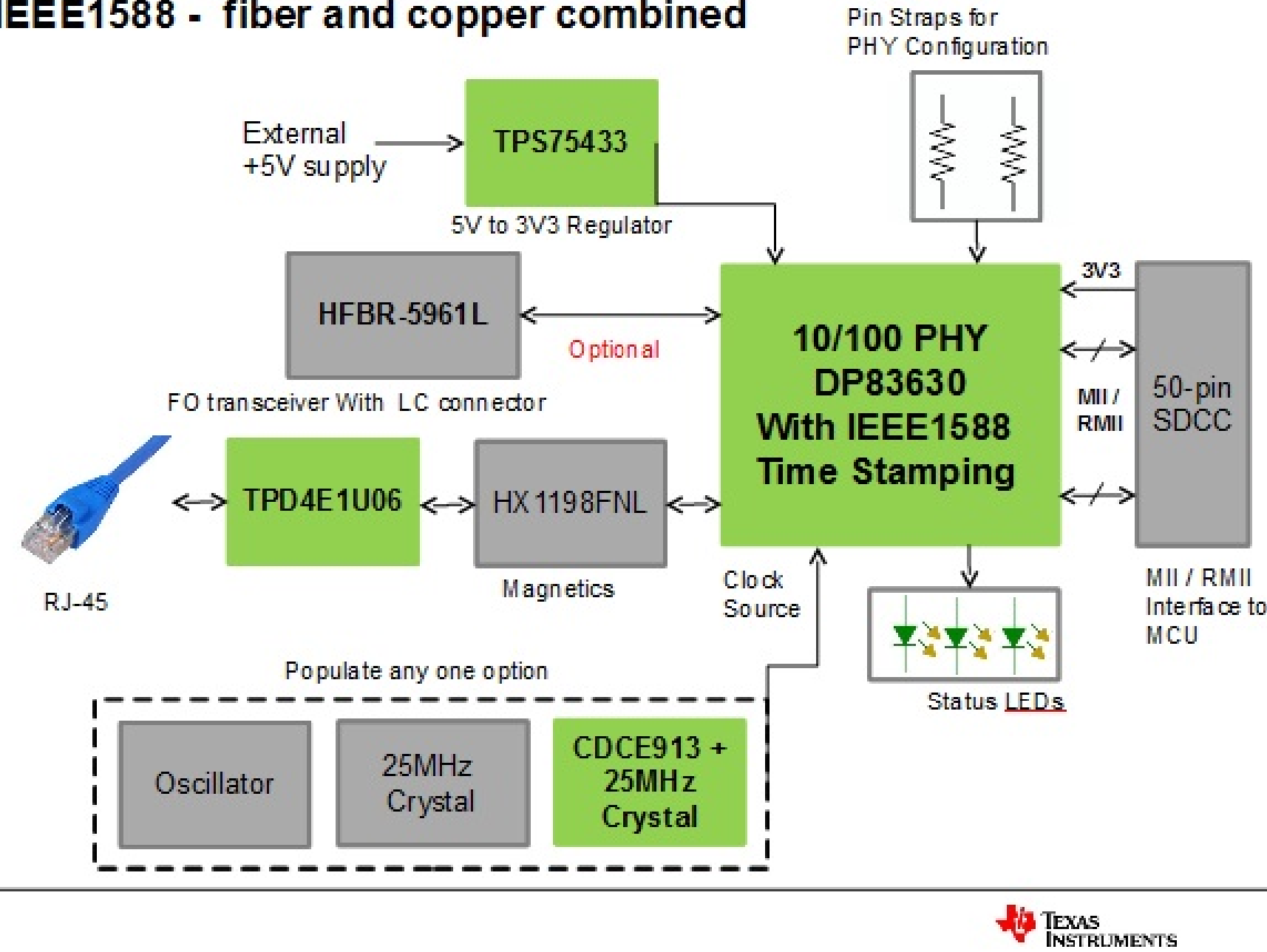


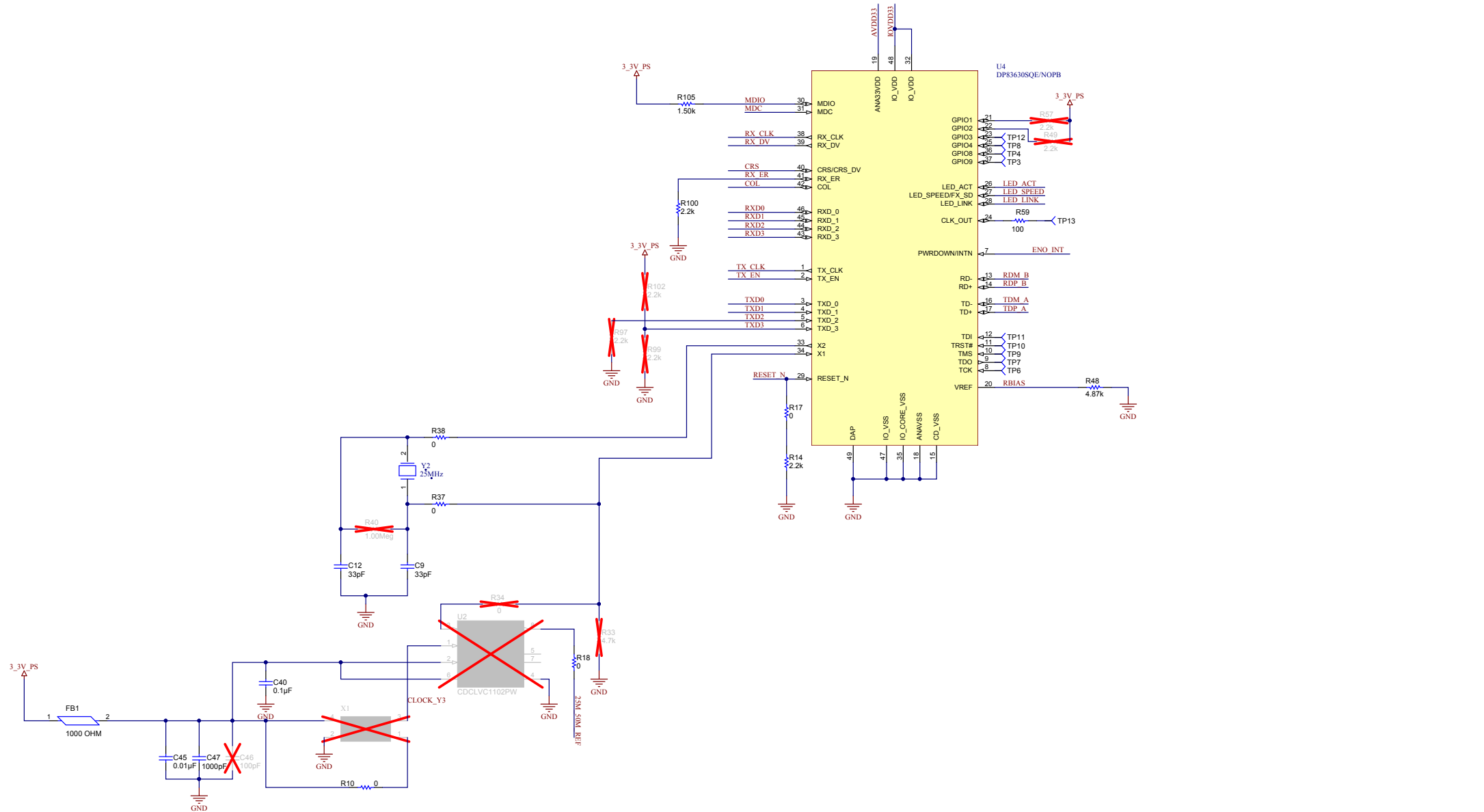
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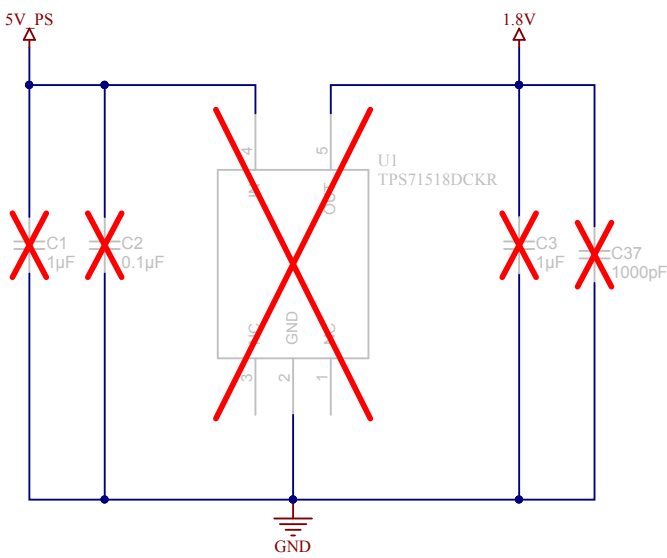
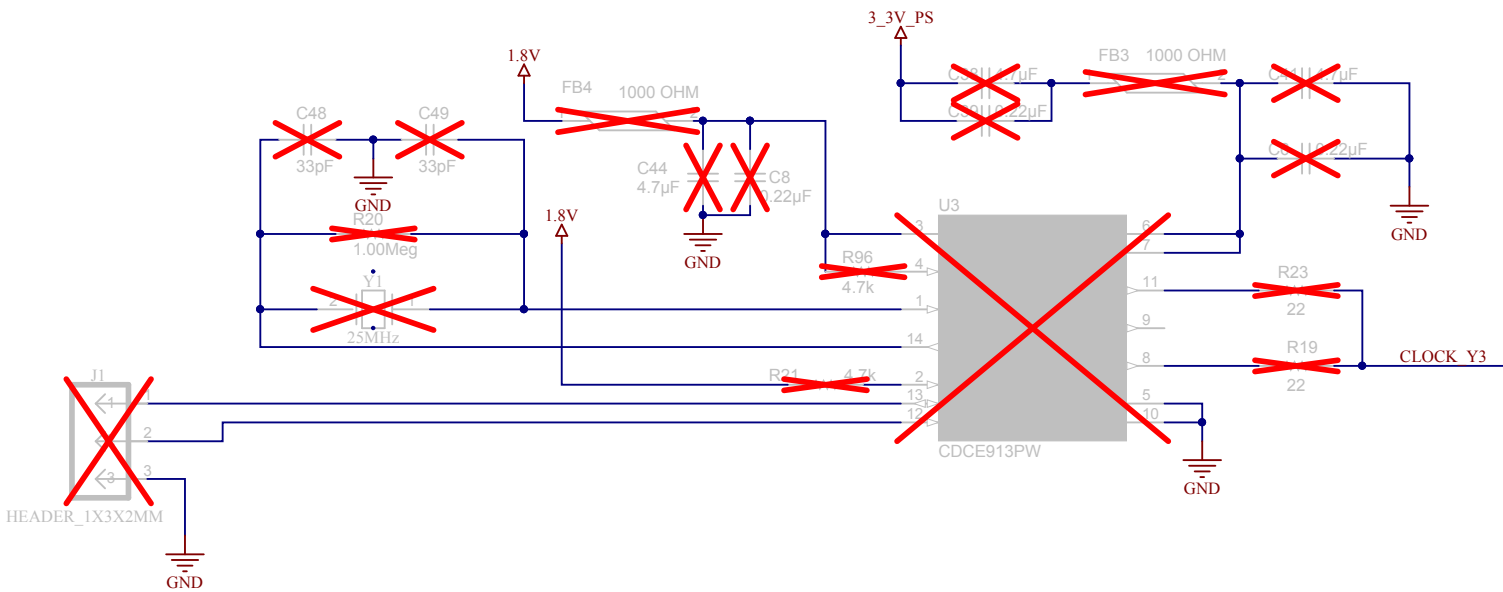
IEEE1588 - fiber and copper combined



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MUST BE CONNECTED WHEN WORKING IN RMII MODE





3_3V_PS
R76
0 CT INPUT1

CT INPUT1

Place capacitors,
inductors, and
resistors close to
transceiver.

J4

VEE RX
VCC RX
SD
RD-
RD+
M1
M2

HFBR-5961AL

EARTH

TDP A
R75
0

TDM A
R74
0

RDM B
R60
0

RDP B
R66
0

Place jumpers, capacitors and
resistors close to TLK105.

R71
49.9

R70
49.9

R61
130

R72
130

C29
0.1μF

C28
0.1μF

R79
130

R86
130

R84
130

LED SPEED
R114
0

R82
82

R83
82

R80
82

C33
10μF

C31
0.1μF

C30
0.1μF

C32
0.1μF

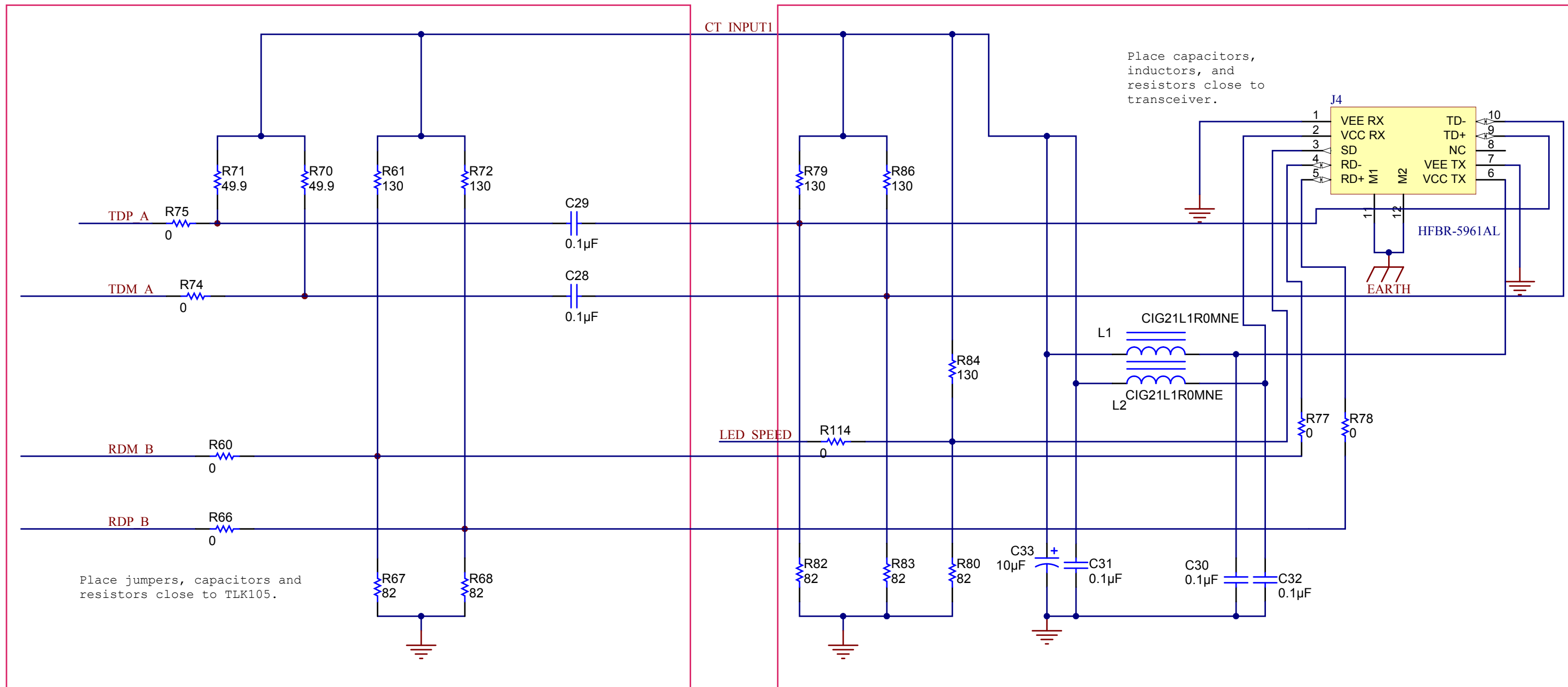
R77
0

R78
0

L1
CIG21L1R0MNE
L2
CIG21L1R0MNE

R67
82

R68
82

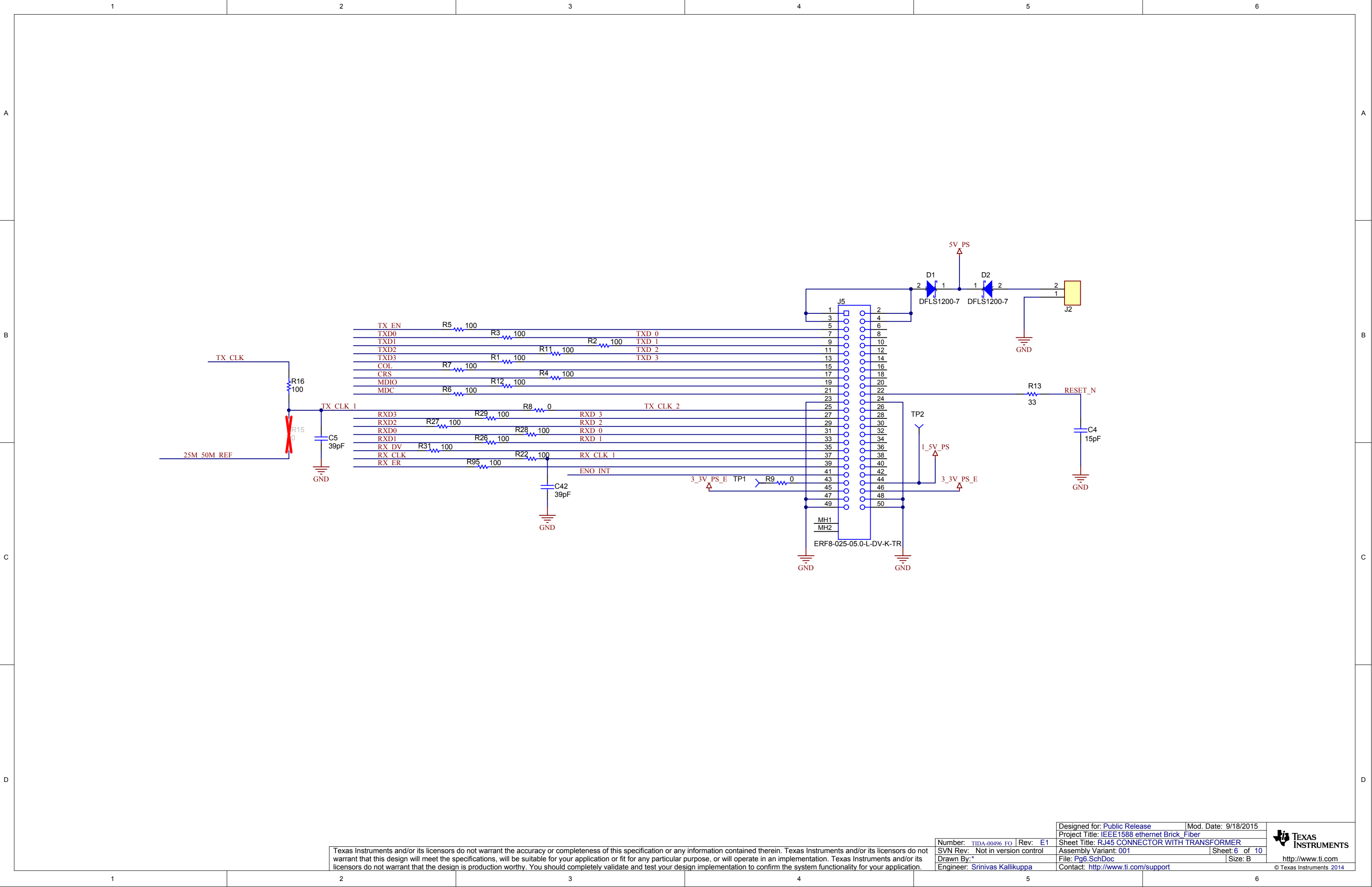


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Number: TIDA-00496 FO Rev: E1
SVN Rev: Not in version control
Drawn By:
Engineer: Srinivas Kallikuppa

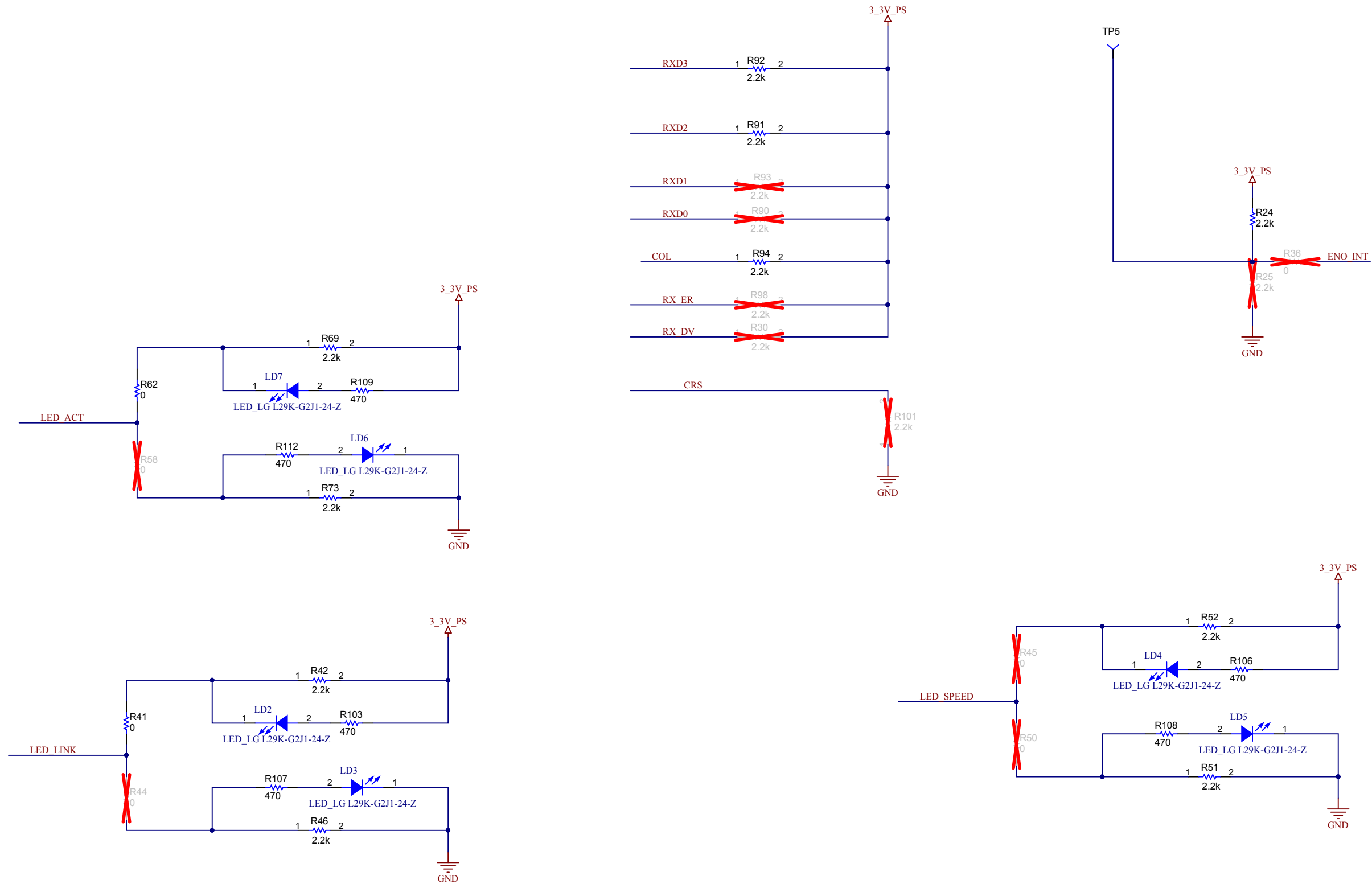
Designed for: Public Release Mod. Date: 9/18/2015
Project Title: IEEE1588 ethernet Brick Fiber
Sheet Title: FIBER OPTIC TRANSCEIVER-LC TYPE
Assembly Variant: 001 Sheet: 5 of 10
File: Pg5.SchDoc Size: A4
Contact: http://www.ti.com/support

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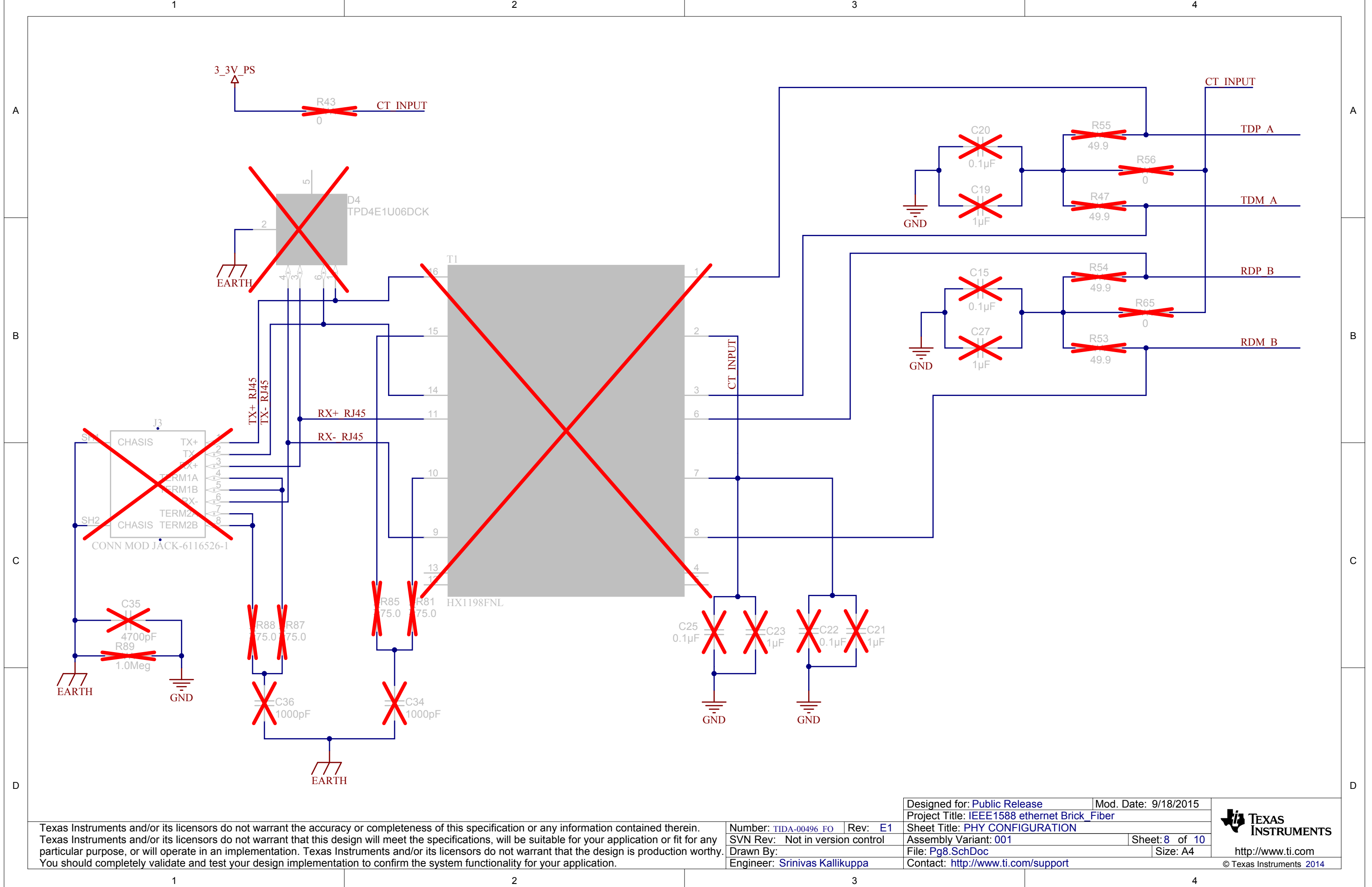
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CONFIGURATION PINS



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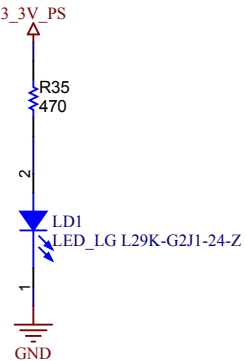


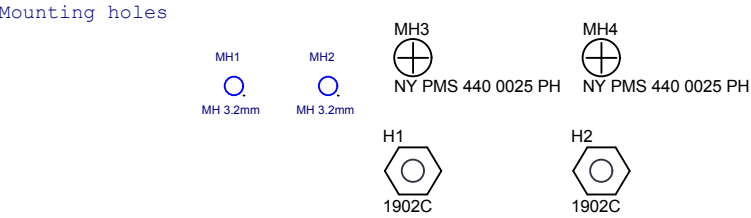
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Number: [TIDA-00496](#) FO Rev: [E1](#)
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Drawn By:
Engineer: [Srinivas Kallikuppa](#)

Designed for: Public Release	Mod. Date: 9/18/2015
Project Title: IEEE1588 ethernet Brick Fiber	
Sheet Title: PHY CONFIGURATION	
Assembly Variant: 001	Sheet: 8 of 10
File: Pg8.SchDoc	Size: A4
Contact: http://www.ti.com/support	

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PCB Number: TIDA-00496_FO
PCB Rev: E1

PCB
LOGO
Texas Instruments

LBL1

Size: 0.65" x 0.20 "

Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.