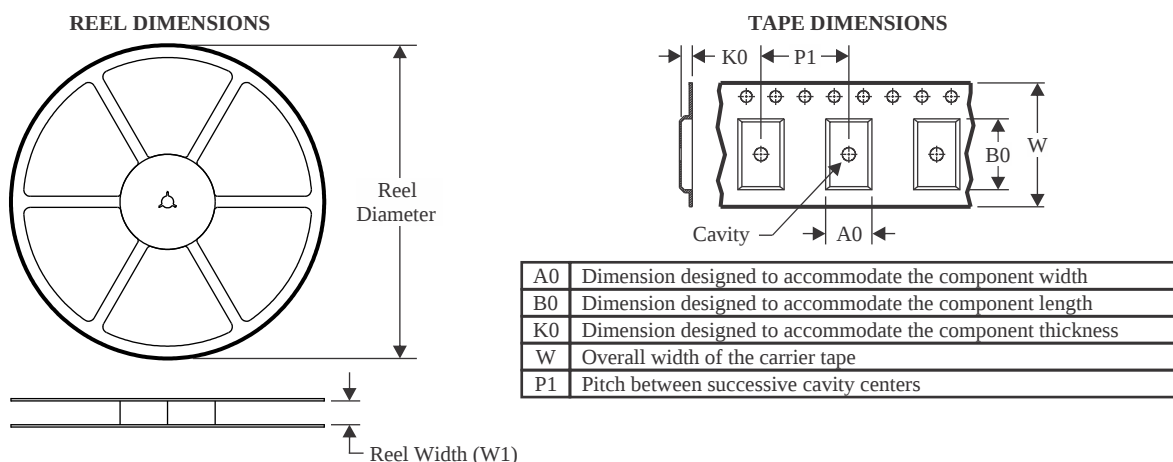
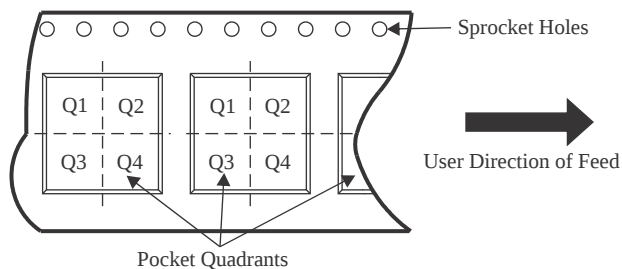


TAPE AND REEL INFORMATION



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

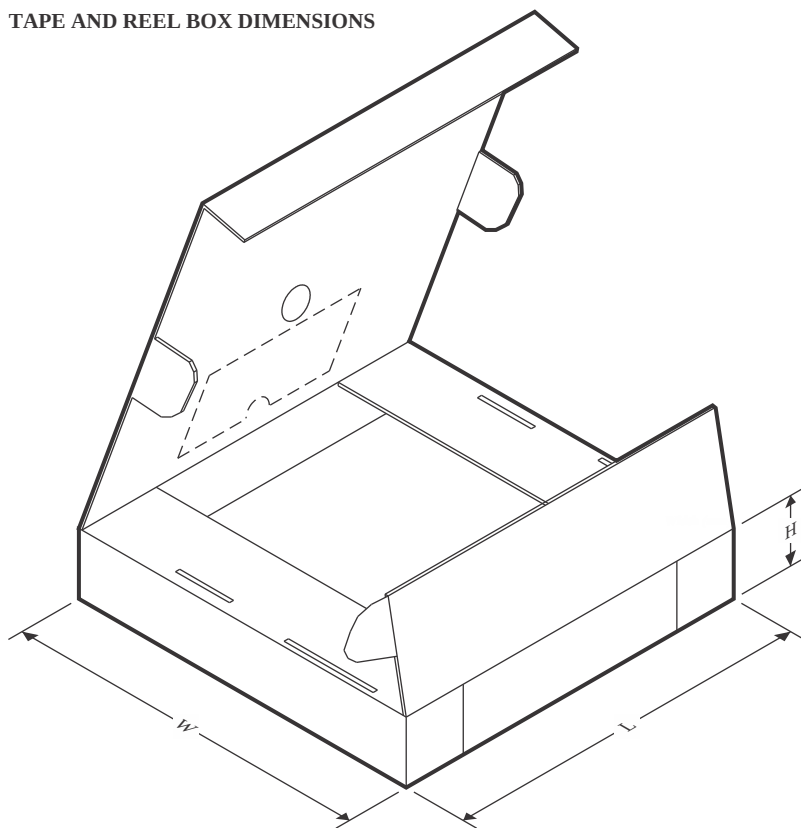


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLC27L2ACDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2ACDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2AIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2BCDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2BCDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2BIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2BIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2CDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2CPSR	SO	PS	8	2000	330.0	16.4	8.35	6.6	2.4	12.0	16.0	Q1
TLC27L2CPWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
TLC27L2IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L2IPWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1
TLC27L2MDRG4	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L7CDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L7CDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L7CPSR	SO	PS	8	2000	330.0	16.4	8.35	6.6	2.4	12.0	16.0	Q1

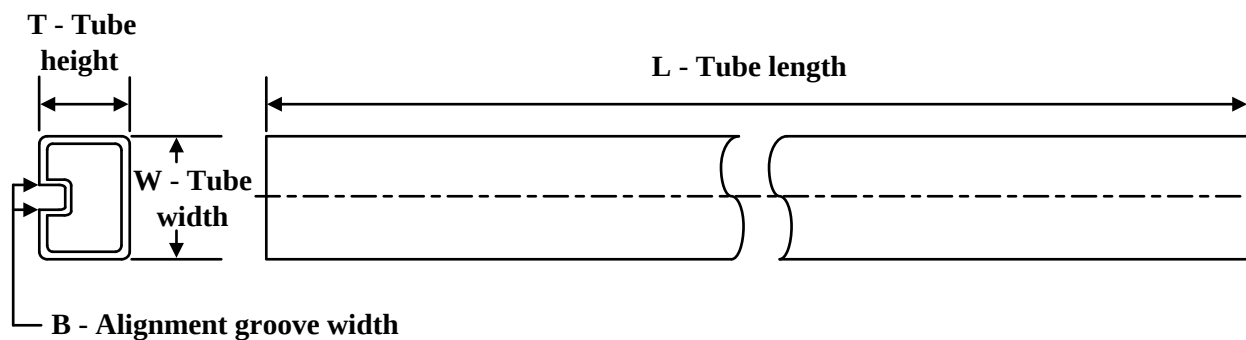
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLC27L7IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLC27L7IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLC27L2ACDR	SOIC	D	8	2500	340.5	338.1	20.6
TLC27L2ACDR	SOIC	D	8	2500	340.5	338.1	20.6
TLC27L2AIDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L2BCDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L2BCDR	SOIC	D	8	2500	340.5	338.1	20.6
TLC27L2BIDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L2BIDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L2CDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L2CPSR	SO	PS	8	2000	353.0	353.0	32.0
TLC27L2CPWR	TSSOP	PW	8	2000	353.0	353.0	32.0
TLC27L2IDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L2IPWR	TSSOP	PW	8	2000	353.0	353.0	32.0
TLC27L2MDRG4	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L7CDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L7CDR	SOIC	D	8	2500	340.5	338.1	20.6
TLC27L7CPSR	SO	PS	8	2000	353.0	353.0	32.0
TLC27L7IDR	SOIC	D	8	2500	353.0	353.0	32.0
TLC27L7IDR	SOIC	D	8	2500	340.5	338.1	20.6

TUBE


*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
TLC27L2ACP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2ACP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2ACPS	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L2ACPS.A	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L2ACPSG4	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L2AIP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2AIP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2BCP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2BCP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2BIP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2BIP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2CP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2CP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2CPS	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L2CPS.A	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L2IP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L2IP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L7CP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L7CP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L7CPS	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L7CPS.A	PS	SOP	8	80	530	10.5	4000	4.1
TLC27L7IP	P	PDIP	8	50	506	13.97	11230	4.32
TLC27L7IP.A	P	PDIP	8	50	506	13.97	11230	4.32