

Fact Sheet

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TMS320DM270 Product Fact Sheet

The new TMS320DM270 (DM270) programmable DSP-based digital media processor is the newest addition to Texas Instruments' digital media product family. Software support will be available for the major audio, video, imaging and voice compression standards (detailed below). Evaluation modules (EVMs), samples, online software libraries, documentation and technical training materials are also available.

DM270 Highlights

- DSP-based solution using a 0.13-micron process technology (1.5V core/3.3V IO)
- MPEG-4 real time video encoding/decoding at 30 fps, VGA resolution
- M-JPEG encoding/decoding 30 fps at VGA resolution
- Support for all major audio, video, imaging and voice encoding/decoding standards:
 - Video: H.263, M-JPEG, MPEG1, MPEG4, Nancy, WMV, DivX
 - Imaging: JPEG

- more -

- Audio: AAC, ATRAC3, MP3, WMA
- Voice: AMR, G.723.1, G.726, G.711
- ARM processor controls the system and includes interface to external host CPUs
- Card interface (CFC, MMC, MS, USB, SSFDC, SD, xD), USB, UART, Serial IF, GIO
- Small package (16x16mm and 12x12mm 288-Pin MicroStar™ BGA packages)

Other Features

- 80 MHz ARM7 with 8KB I-cache, 32KB on-chip RAM
- 90 MHz DSP processor 64KW on-chip RAM
- Programmable + hardware architecture to achieve both hardwired performance and software advantages
- Powerful SIMD iMX performs 4 parallel MAC operations (180MHz)
- Hardware Preview Engine for DSC (digital still camera) system
- Hardware Pixel Pre-Processor for DSC system
- Real time AF, AWB, AE statistics for DSC system
- Supports 16 bit AFE and 8/16 bit video input/output (CCIR601/656)
- Versatile on-chip OSD and NTSC/PAL Video Encoder
- Single channel composite analog and 8/16 bit digital RGB/YUV output
- Integrated peripherals
- Four timers/two UARTs/USB 1.1/32 GIOs
- Interface to 100MHz SDRAM/mobile SDRAM (up to 128MB, x32 or x16)

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