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ABSTRACT

The growing complexity of embedded control applications demands development methodologies that accelerate algorithm design, enable early-stage testing, and reduce time-to-market. Model-Based Design (MBD) with MATLAB/Simulink has emerged as a powerful paradigm that addresses this challenge, allowing engineers to design, simulate, and automatically generate production-ready embedded C code, catching design errors early in the development cycle.

Simultaneously, Texas Instruments' System Configuration Tool (SysConfig) has established itself as a unified graphical interface for peripheral and clock configuration across the entire TI microcontroller (? EP) portfolio. By providing a single, consistent configuration experience from pin multiplexing to driver setup, SysConfig eliminates hand-coded initialization boilerplate and ensures that hardware configuration intent is clearly captured, shareable, and reusable across projects and teams. This application note presents the integration of these two complementary technologies within the [Embedded Coder Support Package for Texas Instruments AM13x](#).

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1 Introduction

A central enabler of this integration is the shared syscfg configuration file between the MATLAB/Simulink model and Code Composer Studio (CCS) IDE project. This common configuration verifies that peripheral settings, pin assignments, and clock definitions remain *consistent and synchronized* across both workflows, eliminating redundant setup and reducing the risk of configuration mismatches between simulation and hardware.

This interoperability creates a natural bridge for teams transitioning from traditional hand-coded development to Model-Based Design — or conversely, for model-based developers who need to hand-off or collaborate with embedded software engineers working in CCS. Algorithmic IP developed and validated in Simulink can be seamlessly carried forward into a CCS-based integration environment, and vice versa, without redefining the underlying hardware configuration.

The result is a cohesive and flexible development ecosystem that significantly reduces integration friction, supports parallel development across disciplines, and ultimately accelerates the journey from concept to a verified, deployable embedded application on AM13x devices.

2 Prerequisites and Getting Started

- Installation steps and required tools are well documented in [GitHub - TexasInstruments/am13x-hsp](#)
- Videos tutorials and FAQs are shown in [\[FAQ\] Embedded Coder Support Package for Texas Instruments AM13x](#)

3 SysConfig Integration with MATLAB and Simulink

Traditionally model settings also configure peripherals and other resources, since SysConfig provides all these features now user needs to just supply a `.syscfg` file, either sourced from an existing SDK example or created interactively within Code Composer Studio. Embedded Coder build process launches SysConfig tool to generate all necessary peripheral initialization code. This generated code from SysConfig linked with the algorithm and scheduler code produced by Simulink, results in a complete application binary.

4 Model Anatomy and SysConfig Entry Point

This [led example](#) is one of the simplest example explaining the integration with SysConfig

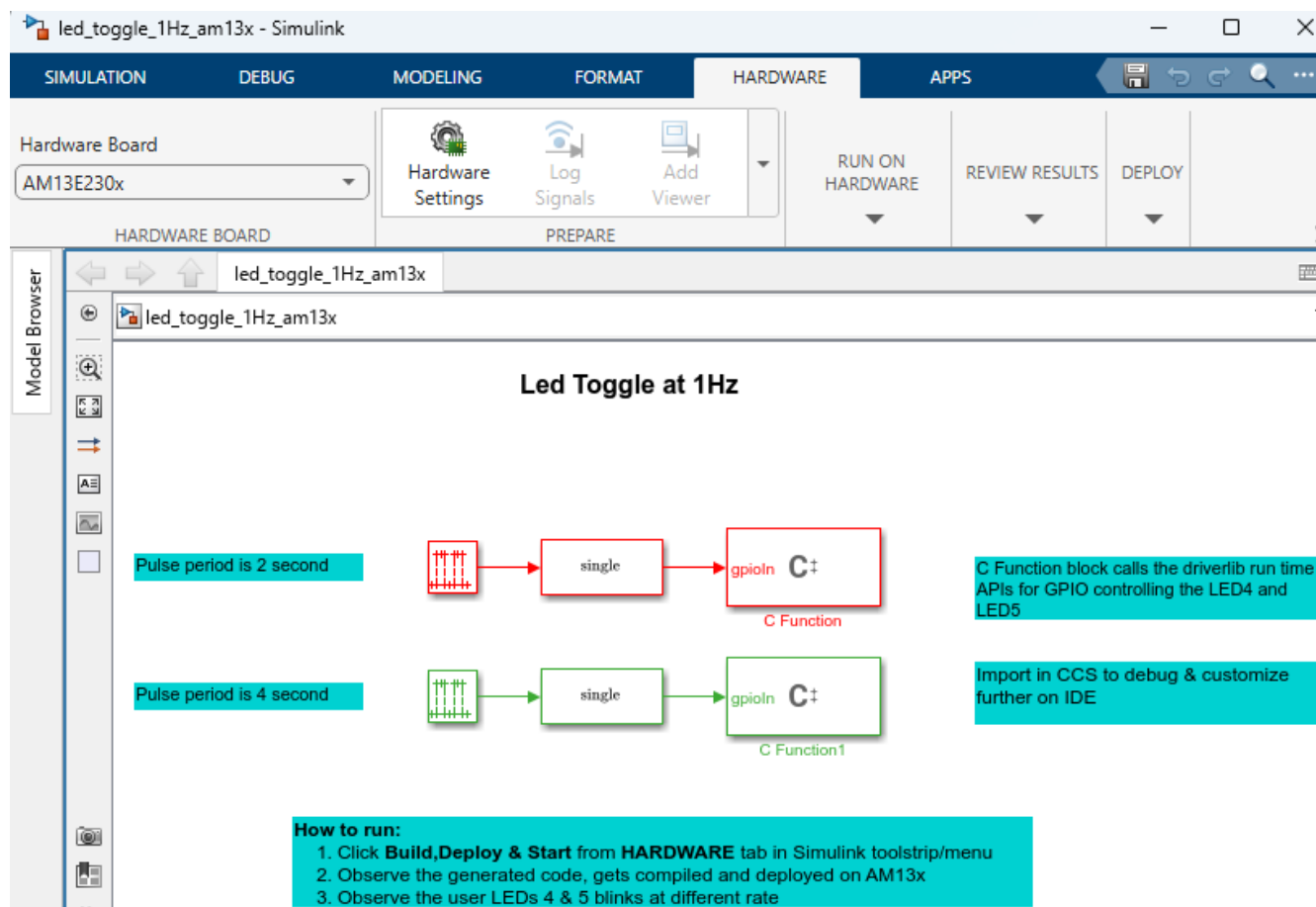


Figure 4-1. Simulink Block for Led Toggle Program

Hardware Settings/Model Settings showing the SysConfig entry point:

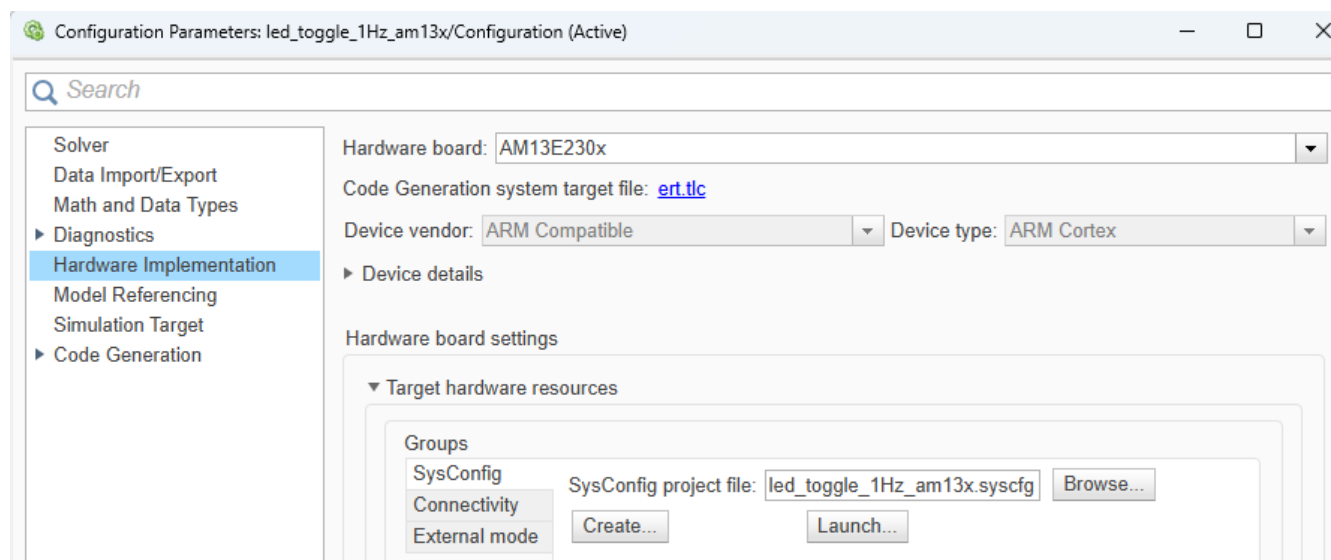


Figure 4-2. Hardware Target Selection

SysConfig launched from Simulink showing two LED configuration

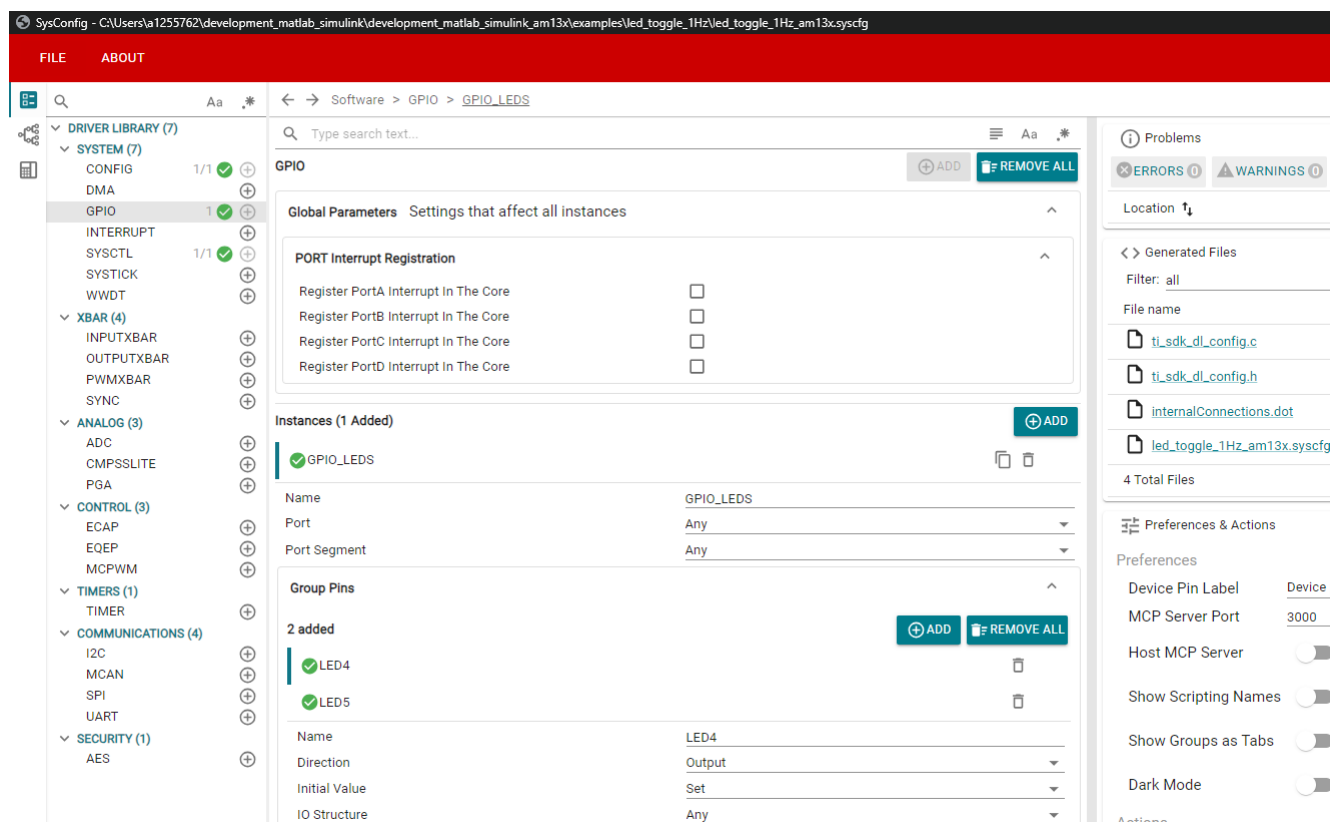
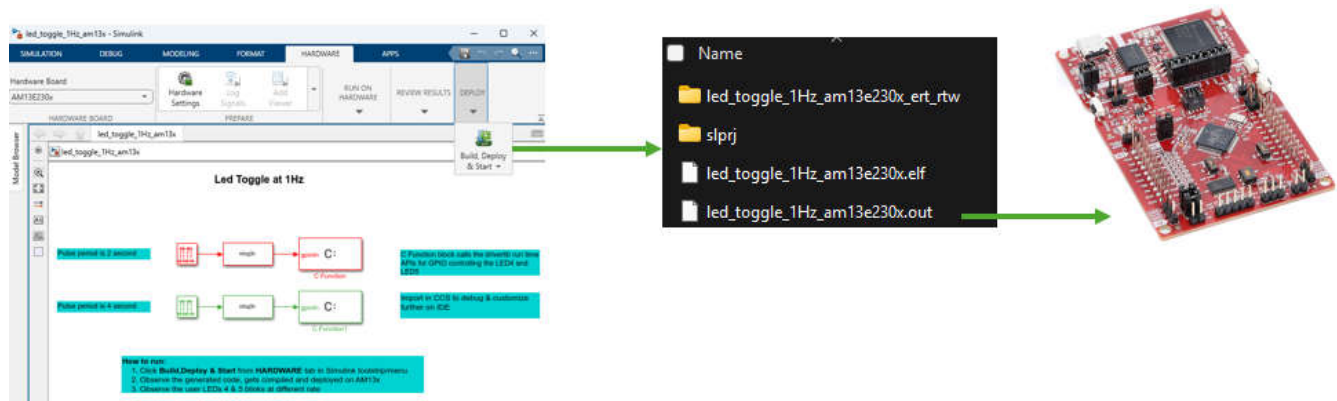


Figure 4-3. GPIO Sysconfig

5 Build and Deployment Overview

When the user clicks *Build, Deploy & Start* in Simulink, code generation, compilation, and deployment to the connected AM13x Launchpad execute automatically in a single operation. The generated output folder contains all artifacts required for the complete application build, organized as follows:



5.1 Codegen Structure

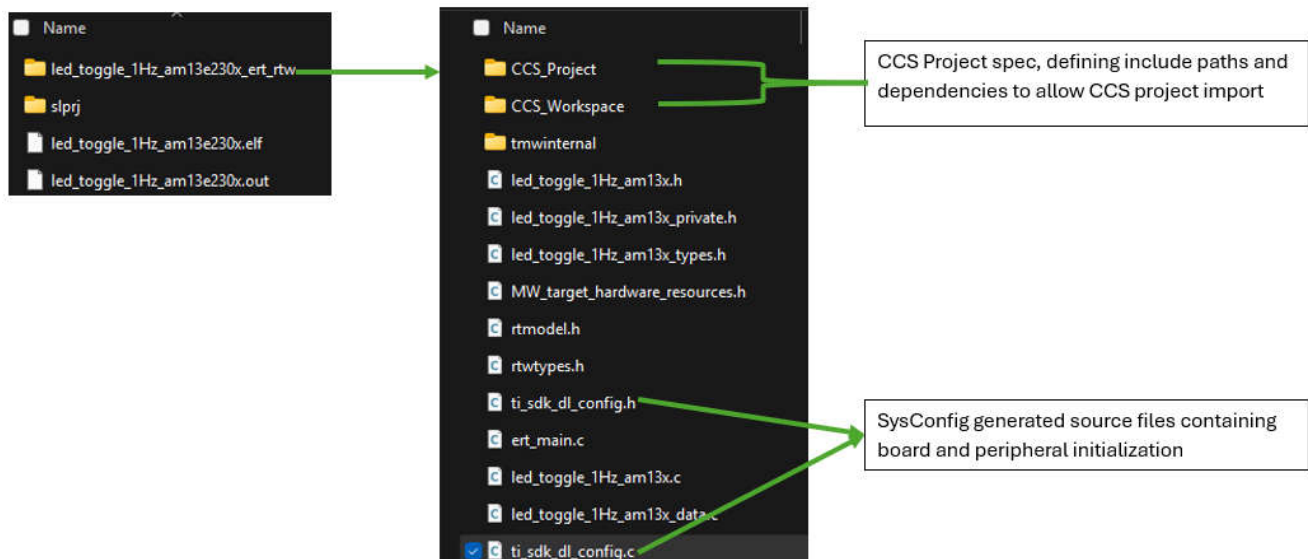


Figure 5-1. CCS Project Structure

Here is simplified explanation of application entry point main function defined in ert_main.c

```

1  int main(void)
2  {
3      /* Step 1: SysConfig-generated peripheral initialization */
4      SYSCFG_DL_init();
5
6      /* Step 2: Simulink-generated model initialization */
7      led_toggle_initialize();
8
9      /* Step 3: HSP scheduler initialization */
10     am13x_tick_init(BASE_RATE_TICKS);
11
12     /* Step 4: Enable interrupts – execution driven by SysTick_Handler */
13     __enable_irq();
14
15     while (1)
16     {
17         /* Idle loop */
18     }
19 }
20
21 /* Simulink-generated scheduler ISR (am13x_tick.c) */
22 void SysTick_Handler(void)
23 {
24     rt_OneStep(); /* Invokes led_toggle_step() at base sample time */
25 }

```

Figure 5-2. Program flow

5.2 Debug and Customize Simulink Generated Code in CCS

This section is explained in [Verification and Validation](#).

6 SysConfig for a Reference Motor Control Setup

Similar to the led example, [Motor control example](#) contains models and SysConfig files for more complex FoC based motor control application where more peripherals such as ADC, PWM, Interrupts, GPIOs are used.

6.1 AM13x Simulink Model

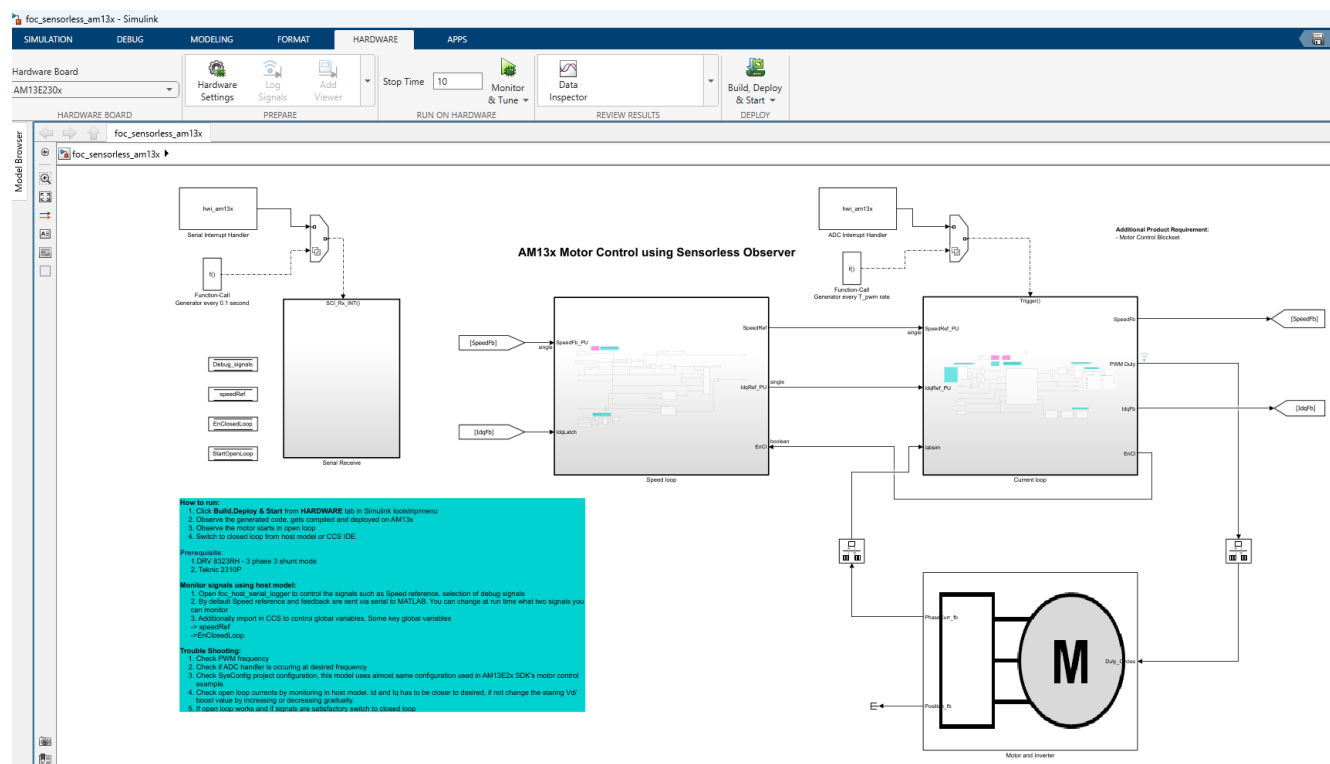
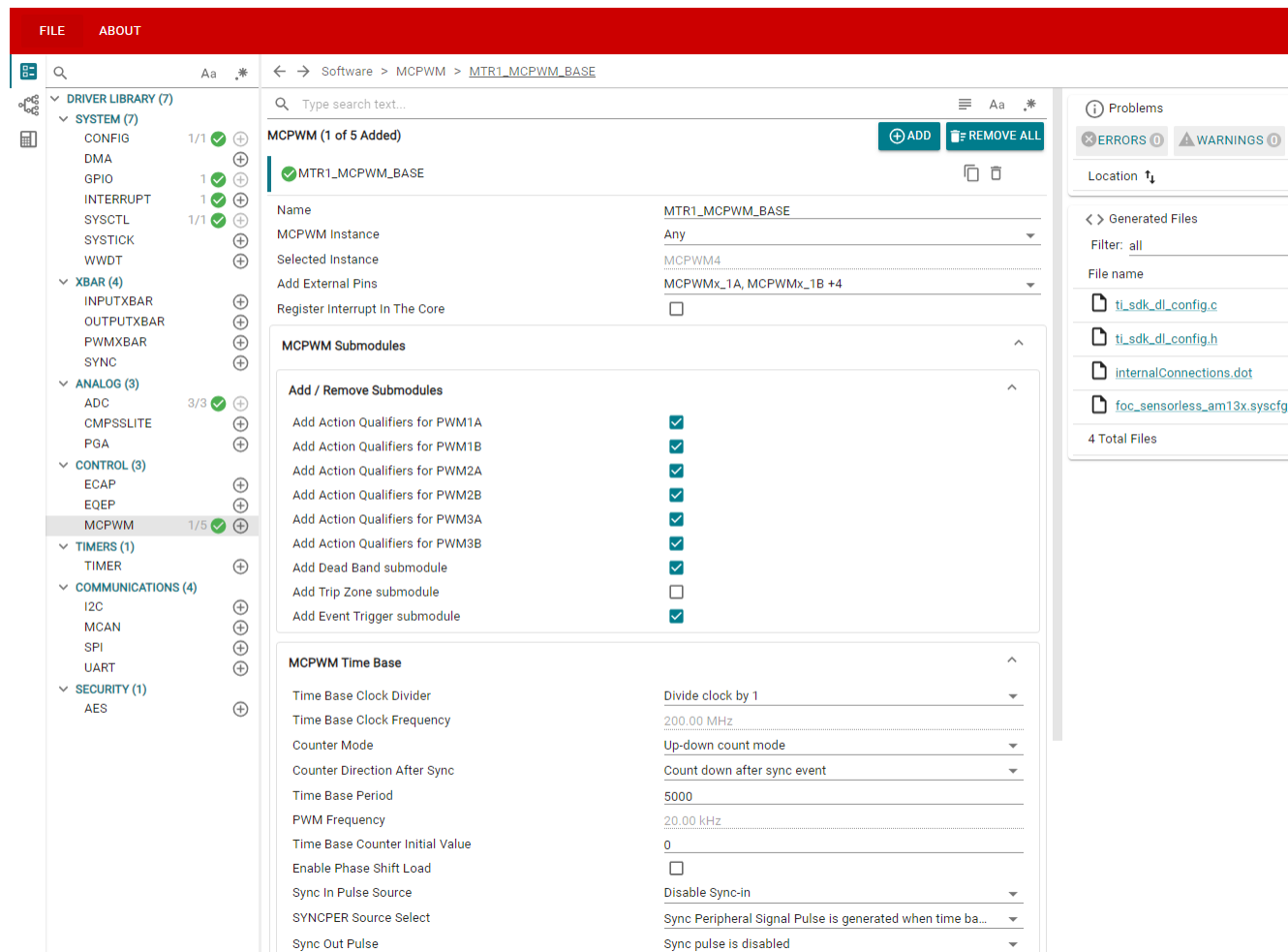


Figure 6-1. Simulink Block for Motor Control FoC

6.2 PWM Configuration in SysConfig



The screenshot displays the SysConfig interface for configuring the MCPWM (Motor Control PWM) module. The left sidebar shows the driver library with categories like SYSTEM, XBAR, ANALOG, CONTROL, TIMERS, COMMUNICATIONS, and SECURITY. The main area shows the MCPWM configuration for MTR1_MCPWM_BASE, including submodules and time base settings. The right sidebar shows generated files and a problems panel.

DRIVER LIBRARY (7)

- SYSTEM (7)
 - CONFIG 1/1 ✓
 - DMA 1 ✓
 - GPIO 1 ✓
 - INTERRUPT 1 ✓
 - SYSCTL 1/1 ✓
 - SYSTICK 1 ✓
 - WWDT 1 ✓
- XBAR (4)
 - INPUTXBAR
 - OUTPUTXBAR
 - PWMXBAR
 - SYNC
- ANALOG (3)
 - ADC 3/3 ✓
 - CMPSSLITE
 - PGA
- CONTROL (3)
 - ECAP
 - EQEP
 - MCPWM 1/5 ✓
- TIMERS (1)
 - TIMER
- COMMUNICATIONS (4)
 - I2C
 - MCAN
 - SPI
 - UART
- SECURITY (1)
 - AES

MCPWM (1 of 5 Added)

ADD REMOVE ALL

✓ MTR1_MCPWM_BASE

Name: MTR1_MCPWM_BASE

MCPWM Instance: Any

Selected Instance: MCPWM4

Add External Pins: MCPWMx_1A, MCPWMx_1B +4

Register Interrupt In The Core: ☐

MCPWM Submodules

Add / Remove Submodules

Submodule	Enabled
Add Action Qualifiers for PWM1A	☒
Add Action Qualifiers for PWM1B	☒
Add Action Qualifiers for PWM2A	☒
Add Action Qualifiers for PWM2B	☒
Add Action Qualifiers for PWM3A	☒
Add Action Qualifiers for PWM3B	☒
Add Dead Band submodule	☒
Add Trip Zone submodule	☐
Add Event Trigger submodule	☒

MCPWM Time Base

Time Base Clock Divider	Divide clock by 1
Time Base Clock Frequency	200.00 MHz
Counter Mode	Up-down count mode
Counter Direction After Sync	Count down after sync event
Time Base Period	5000
PWM Frequency	20.00 kHz
Time Base Counter Initial Value	0
Enable Phase Shift Load	☐
Sync In Pulse Source	Disable Sync-in
SYNCPER Source Select	Sync Peripheral Signal Pulse is generated when time ba...
Sync Out Pulse	Sync pulse is disabled

Generated Files

Filter: all

File name

- ti_sdk_dl_config.c
- ti_sdk_dl_config.h
- internalConnections.dot
- foc_sensorless_am13x.syscfg

4 Total Files

Figure 6-2.

6.3 ADC Configuration in SysConfig

The screenshot displays the SysConfig application interface. On the left, a tree view shows the component hierarchy: DRIVER LIBRARY (7), SYSTEM (7), XBAR (4), ANALOG (3), CONTROL (3), TIMERS (1), COMMUNICATIONS (4), and SECURITY (1). The ANALOG section is expanded, showing ADC (3/3), CMPSSLITE, and PGA. The ADC component is selected, and its configuration is shown in the central pane. The configuration includes:

- ADC (3 of 3 Added)**: A list of three ADC instances: ADC_0, ADC_1, and ADC_2.
- Name**: ADC_0
- ADC Instance**: ADC0
- Input Clock**: 200.00 MHz
- ADC Clock Prescaler**: ADCCLK = (input clock) / 4.0
- ADC Clock**: 50.00 MHz
- SOC Configurations**: Start of Conversion Configurations
 - Number Of SOCs To Be Configured**: 1
 - SOC0**:
 - SOC Name**: MTR1_IW_SEN
 - Independent Name Mode**: ☒
 - Channel**: Channel 12 / PA17
- SEQ Configurations**: Sequencer Configurations
- PPB Configurations**: Post Processing Blocks Configurations
- INT Configurations**: CPU Interrupts and DMA Events Configurations
 - ADC Interrupt Pulse Mode**: End of conversion
 - ADC Interrupt Cycle Offset**: 0
 - Enable ADC Interrupts**: INT1
 - Enable DMA Interrupts**: None

On the right side of the interface, there is a **Problems** section showing 0 errors and 0 warnings. Below it, a **Generated Files** section lists the following files:

- ti_sdk_dl_config.c
- ti_sdk_dl_config.h
- internalConnections.dot
- foc_sensorless_am13x.syscfg

At the bottom of the right sidebar, it indicates **4 Total Files**.

Figure 6-3.

7 Summary

This application note has presented a structured methodology for integrating TI SysConfig with the MATLAB/Simulink Model-Based Design workflow targeting the AM13x microcontroller family. SysConfig serves as the single definition of all peripheral/MCU configuration verifying consistency between hand-coded and model-generated embedded software.

8 References

Texas Instruments, [Matlab BlockSet Training](#), training.

Texas Instruments, [\[FAQ\] Embedded Coder Support Package for Texas Instruments AM13x](#), FAQ.

Texas Instruments, [AM13E2X-SDK](#), product page.

Texas Instruments, [AM13E2X-HSP](#), hardware support package.

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