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* Model Usage Notes:
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* A. Features have been modelled
*   1. Switching Characteristics and variation with VIN
*   2. Power Good functionality and delay.
*   3. VIN UVLO and high side Current limit.
*   4. Zero current detection and FPWM mode.
*   5. Hiccup and foldback during current limit.
*   6. Low side valley current limit is implemented.
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* B. Features have not been modelled
*   1. Operating Quiescent Current
*   2. Shutdown Current
*   3. Temperature dependent characteristics
*   4. Current through Pins EN,MODE_SYNC,COMP_FSET,FB are not modelled.
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* C. Application Notes
*   1. The parameter SS has been used to reach the steady state faster.
*      Keep SS = 0 to observe startup behaviour.
*      Keep SS = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
*   2. Ground pins have been tied to 0V internally.
*      Therefore, this model cannot be used for inverting topologies
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