# Temperature Sensing NTC Circuit With MSP430™ Smart Analog Combo



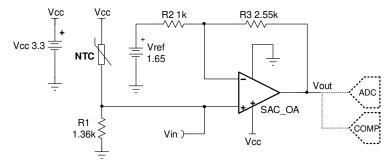
## **Design Goals**

Temperature		Output Voltage		Supply		
T <sub>Min</sub>	T <sub>Max</sub>	V <sub>outMin</sub>	V <sub>outMax</sub>	V <sub>cc</sub>	V <sub>ee</sub>	V <sub>ref</sub>
25°C	50°C	0.2 V	3.1 V	3.3 V	0 V	1.65 V

#### **Design Description**

Some MSP430™ microcontrollers (MCUs) contain configurable integrated signal chain elements such as opamps, DACs, and programmable gain stages. These elements make up a peripheral called the Smart Analog Combo (SAC). For information on the different types of SACs and how to leverage their configurable analog signal chain capabilities, visit MSP430 MCUs Smart Analog Combo Training. To get started with your design, download the Temperature Sensing NTC Circuit Design Files.

This temperature sensing circuit uses a resistor in series with a negative-temperature-coefficient (NTC) thermistor to form a voltage divider, which produces an output voltage that is linear over temperature. The circuit uses the MSP430FR2311 SAC\_L1 op-amp in a noninverting amplifier configuration with inverting reference to offset and gain the signal, which helps to use the full ADC resolution and increase measurement accuracy. (Note: The MSP430FR2355 features four SAC\_L3 peripherals which each contain a built-in DAC and PGA, providing a single-chip solution for generating Vref and measuring the thermistor circuit.) The output of the integrated SAC op-amp can be sampled directly by the on-board ADC or monitored by the on-board comparator for further processing inside the MCU.



#### **Design Notes**

- The connection, Vin, is a negative temperature coefficient output voltage. To measure the output voltage of a PTC thermistor, switch the position of R<sub>1</sub> and the thermistor.
- V<sub>ref</sub> can be generated using one of the integrated SAC\_L3 DACs in the MSP430FR2355 or a voltage divider.
   If a voltage divider is used the equivalent resistance of the voltage divider will influence the gain of the circuit.
- Using high value resistors can degrade the phase margin of the amplifier and introduce additional noise in the circuit. It is recommended to use resistor values of approximately 10 k $\Omega$  or less.
- If the solution is implemented using the MSP430FR2311, the SAC\_L1 op-amp is configured in general purpose mode to measure the thermistor circuit.
- If the solution is implemented using the MSP430FR2355, one SAC\_L3 peripheral is configured in DAC
  mode to generate the reference voltage and another is configured in general purpose mode to measure the
  thermistor circuit.



## **Design Steps**

$$V_{out} = V_{cc} \times \frac{R_1}{R_{NTC} + R_1} \times \frac{R_2 + R_3}{R_2} - \frac{R_3}{R_2} \times V_{ref}$$

1. Calculate the value of R<sub>1</sub> to produce a linear output voltage. Use the minimum and maximum values of the NTC to obtain a range of values for R<sub>1</sub>.

$$R_{NTC\ max} = R_{NTC\ @25^{\circ}C} = 2.252 \ k\Omega, \quad R_{NTC\ min} = R_{NTC\ @50^{\circ}C} = 819.7\Omega$$

$$R_1 = \sqrt{R_{NTC @25^{\circ}C} \times R_{NTC @50^{\circ}C}} = \sqrt{2.252 \text{ k}\Omega \times 819.7 \Omega} = 1.359 \text{ k}\Omega \approx 1.36 \text{k}\Omega$$

2. Calculate the input voltage range.

$$V_{inMin} = V_{cc} \times \frac{R_1}{R_{NTC max} + R_1} = 3.3 \text{ V} \times \frac{1.36 \text{ k}\Omega}{2.252 \text{ k}\Omega + 1.36 \text{ k}\Omega} = 1.2418 \text{ V}$$

$$V_{inMax} = V_{cc} \times \frac{R_1}{R_{NTC \ min} + R_1} = 3.3 \text{ V} \times \frac{1.36 \text{ k}\Omega}{819.7 \Omega + 1.36 \text{ k}\Omega} = 2.0582 \text{ V}$$

3. Calculate the gain required to produce the maximum output swing.

$$G_{ideal} = \frac{V_{outMax} - V_{outMin}}{V_{inMax} - V_{inMin}} = \frac{3.1 \text{ V} - 0.2 \text{ V}}{2.0582 \text{ V} - 1.2418 \text{ V}} = 3.5519 \frac{\text{V}}{\text{V}}$$

4. Select R<sub>2</sub> and calculate R<sub>3</sub> to set the gain in Step 3.

$$Gain = \frac{R_2 + R_3}{R_2}$$

 $R_2 = 1 k\Omega$  (Standard value)

$$R_3 = R_2 \times (G_{ideal} - 1) = 1 \text{ k}\Omega \times (3.5519 \frac{V}{V} - 1) = 2.5519 \text{ k}\Omega$$

Choose 
$$R_3 = 2.55 \text{ k}\Omega$$

5. Calculate the actual gain based on standard values of R<sub>2</sub> and R<sub>3</sub>.

$$G_{actual} = \frac{R_2 + R_3}{R_2} = \frac{1 \text{ k}\Omega + 2.55 \text{ k}\Omega}{1 \text{ k}\Omega} = 3.55 \frac{\text{V}}{\text{V}}$$

6. Calculate the output voltage swing based on the actual gain.

$$V_{out\_swing} = (V_{inMax} - V_{inMin}) \times G_{actual} = (2.0582 \text{ V} - 1.2418 \text{ V}) \times 3.55 \frac{V}{V} = 2.9 \text{ V}$$

7. Calculate the maximum output voltage when the output voltage is symmetrical around mid-supply.

$$V_{outMax} = V_{mid-supply} + \frac{V_{out\_swing}}{2} = \frac{V_{\textit{CC}} - V_{ee}}{2} + \frac{V_{out\_swing}}{2} = \frac{3.3 \text{ V} - 0 \text{ V}}{2} + \frac{2.9 \text{ V}}{2} = 3.1 \text{ V}$$

Calculate the reference voltage.

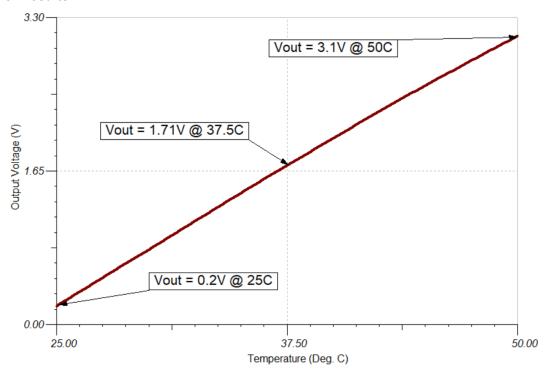
$$V_{outMax} = V_{inMax} \times G_{actual} - \frac{R_3}{R_2} \times V_{ref}$$

3.1 V = 2.0582 V 
$$\times$$
 3.55  $\frac{V}{V} - \frac{2.55 \; k\Omega}{1 \; k\Omega} \times V_{ref}$ 

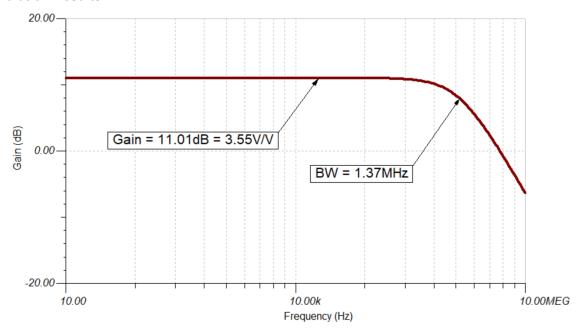
$$V_{\text{ref}} = \frac{2.0582 \text{ V} \times 3.55 \frac{\text{V}}{\text{V}} - 3.1 \text{ V}}{\frac{2.55 \text{ k}\Omega}{1 \text{ k}\Omega}} = 1.65 \text{ V}$$

## **Design Simulations**

#### **DC Transfer Results**



## **AC Simulation Results**



# **Target Applications**

- Field temperature transmitters
- Thermostats
- Thermometers
- Thermistor probes
- System temperature monitor

#### References

- 1. MSP430 MCUs Smart Analog Combo Training
- 2. Analog Engineer's Circuit Cookbooks
- 3. MSP430FR2311 TINA-TI Spice Model
- 4. MSP430 Temp Sense NTC Circuit Code Examples and SPICE Simulation File

# **Design Featured Op Amp**

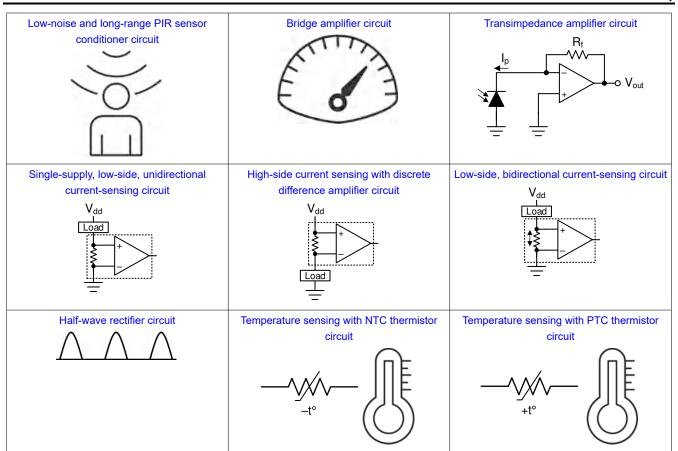
MSP430FRxx Smart Analog Combo				
	MSP430FR2311 SAC_L1	MSP430FR2355 SAC_L3		
V <sub>cc</sub>	2.0 V to 3.6 V			
V <sub>CM</sub>	-0.1 V to V <sub>CC</sub> + 0.1 V			
V <sub>out</sub>	Rail-to-rail			
V <sub>os</sub>	±5 mV			
A <sub>OL</sub>	100 dB			
	350 μA (high-speed mode)			
Iq	120 μA (low-power mode)			
l <sub>b</sub>	50 pA			
UGBW	4 MHz (high-speed mode)	2.8 MHz (high-speed mode)		
OOD!	1.4 MHz (low-power mode)	1 MHz (low-power mode)		
SR	3 V/µs (high-speed mode)			
JK	1 V/µs (low-power mode)			
Number of channels	1	4		
	MSP430FR2311	MSP430FR2355		

# **Design Alternate Op Amp**

MSP430FR2311 Transimpedance Amplifier				
V <sub>cc</sub>	2.0 V to 3.6 V			
V <sub>CM</sub>	-0.1 V to V <sub>CC</sub> /2 V			
V <sub>out</sub>	Rail-to-rail			
V <sub>os</sub>	±5 mV			
A <sub>OL</sub>	100 dB			
	350 μA (high-speed mode)			
Iq	120 μA (low-power mode)			
ı.	5 pA (TSSOP-16 with OA-dedicated pin input)			
l <sub>b</sub>	50 pA (TSSOP-20 and VQFN-16)			
UGBW	5 MHz (high-speed mode)			
UGBW	1.8 MHz (low-power mode)			
SR	4 V/µs (high-speed mode)			
SK.	1 V/µs (low-power mode)			
Number of channels	1			
MSP430FR2311				



www.ti.com Revision History



# **Revision History**

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

## Changes from October 19, 2019 to March 9, 2020

**Page** 

Added Related MSP430 Circuits section......1

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