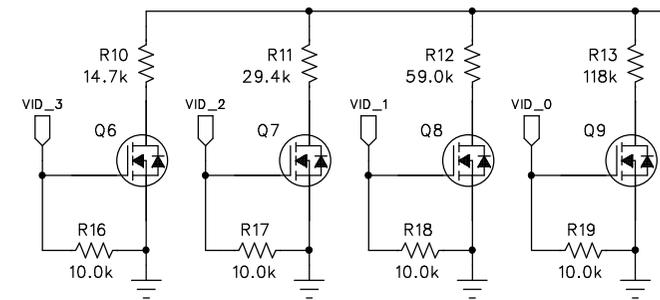
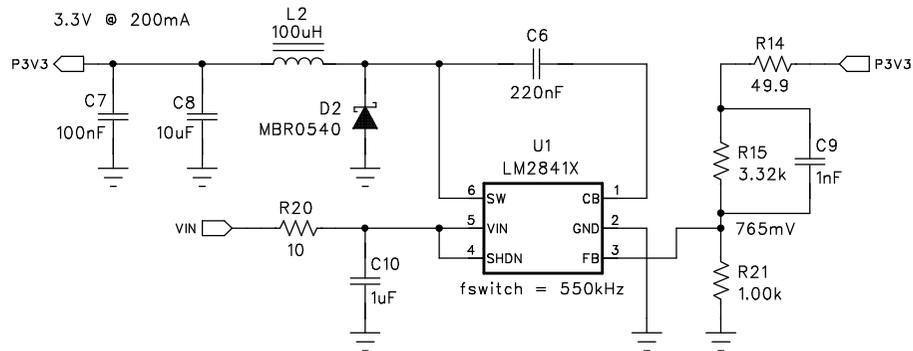
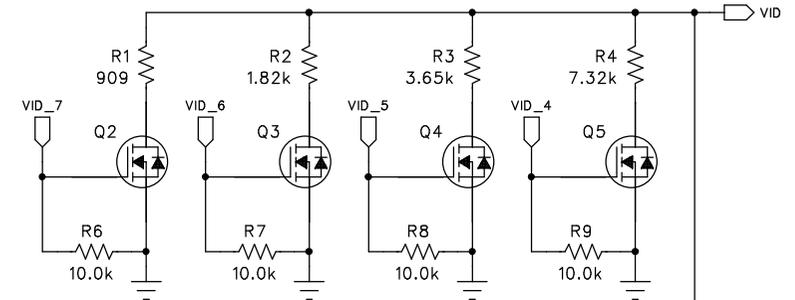
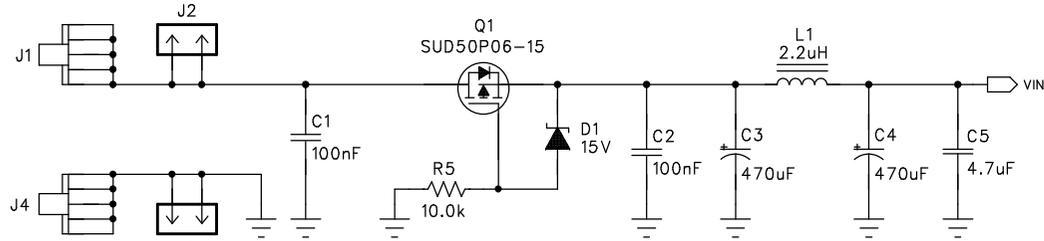


Input Filter, Bias Supply, VID-Interface

Input
20..28V, 24V nom.

8-Bit VID-Interface for 2..15V output
7: 909R, 6: 1.82k, 5: 3.65k, 4: 7.32k
3: 14.7k, 2: 29.4k, 1: 59.0k, 0: 118k



Notes

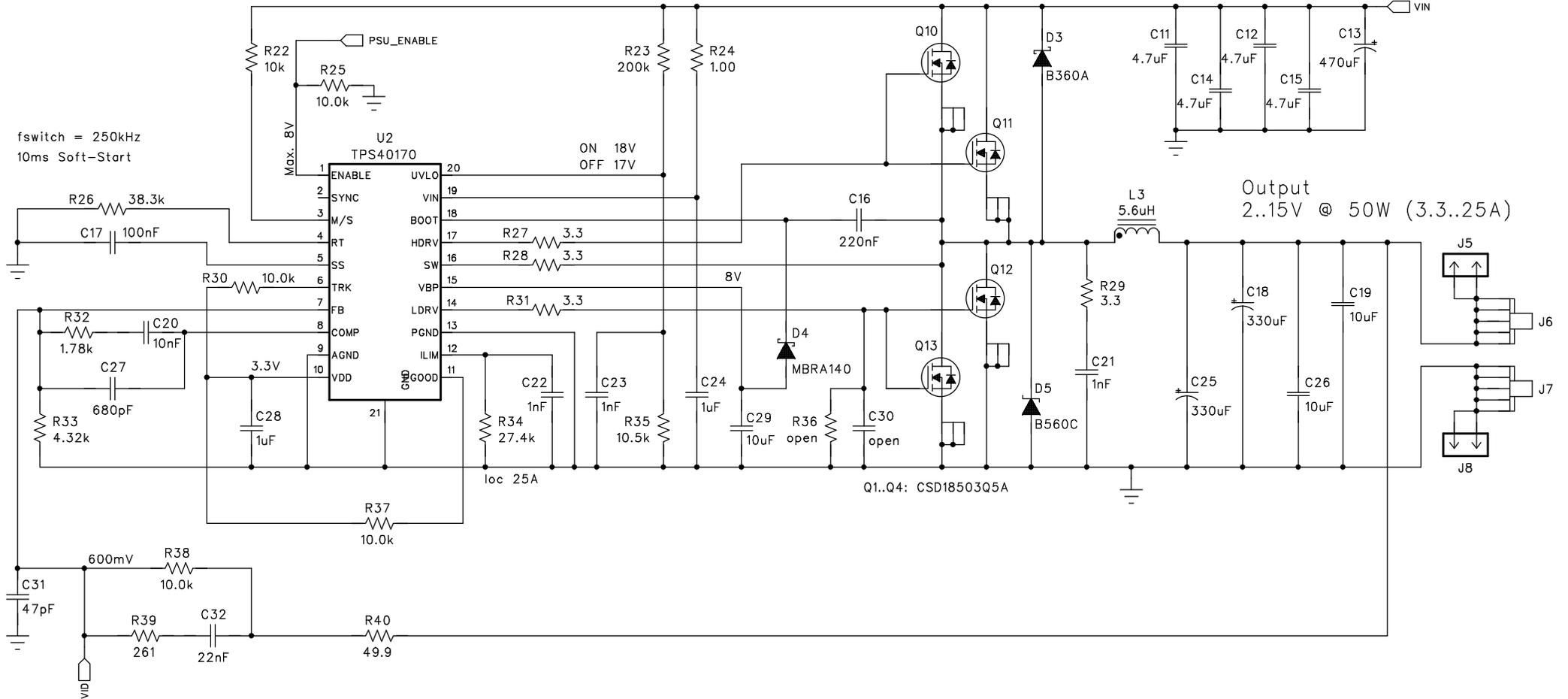
- * R14 for test purpose only
- * Populate either J1/J4 or J2/J3



Title Cranking Simulator		
Size B	Number PMP7233	Rev A
Date 2/18/2013	Drawn by M. Ulmann	
Filename PMP7233RevA.sch	Sheet 1 of 4	

Engineer Matthias Ulmann

Adjustable Synchronous Buck Converter



Notes

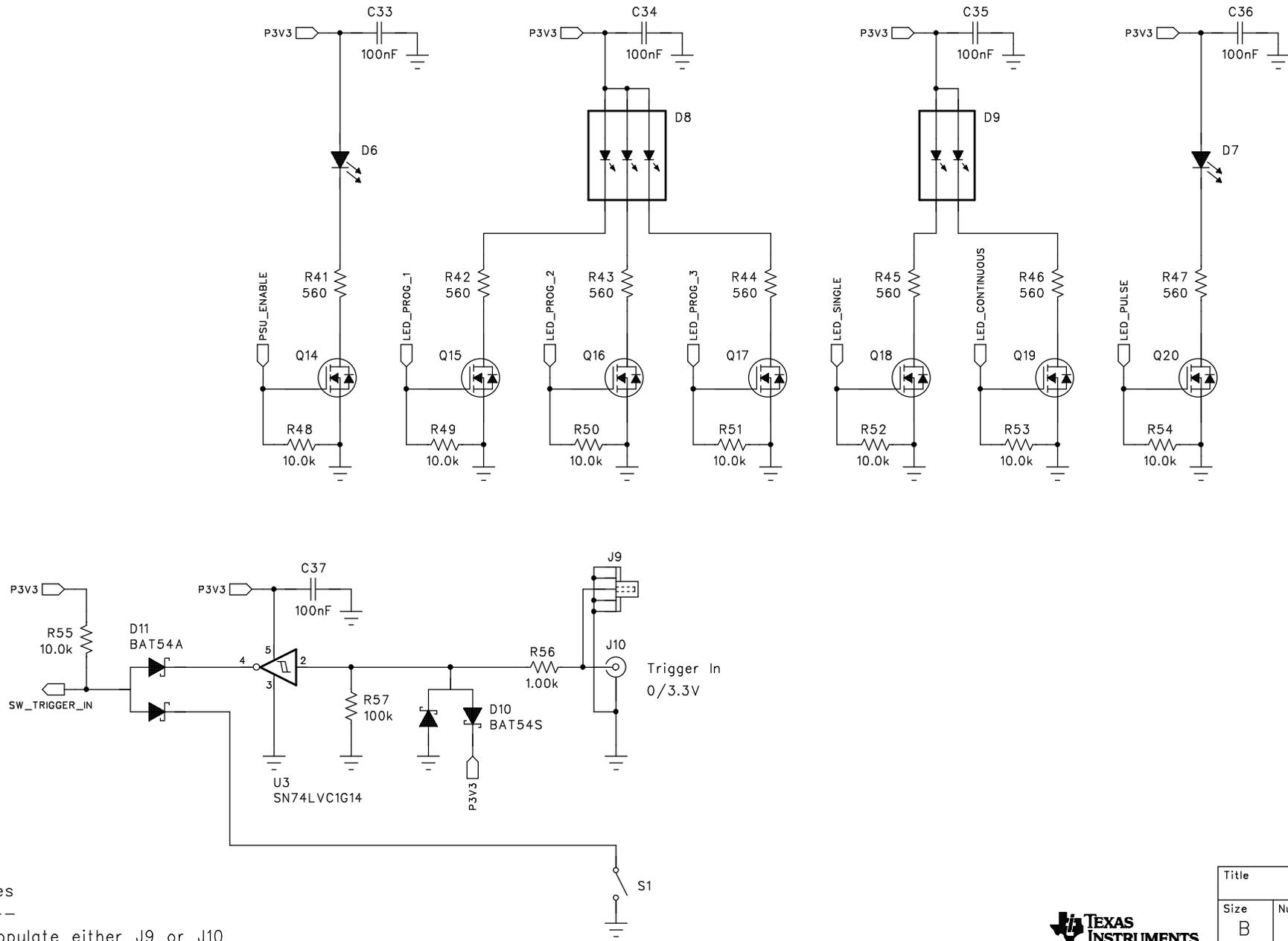
- * R40 for test purpose only
- * Labels of R22/R26 on PCB swapped
- * Populate either J6/J7 or J5/J8



Title		
Cranking Simulator		
Size	Number	Rev
B	PMP7233	A
Date	2/18/2013	Drawn by M. Ulmann
Filename	PMP7233RevA.sch	Sheet 2 of 4

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LEDs, Trigger Input



Notes

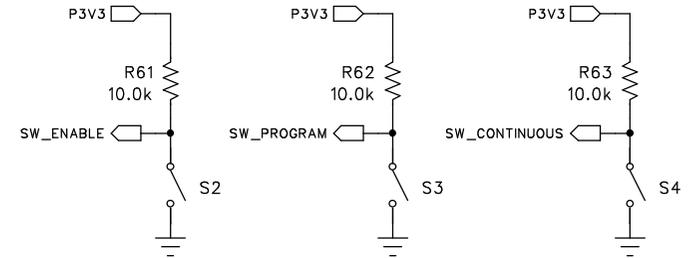
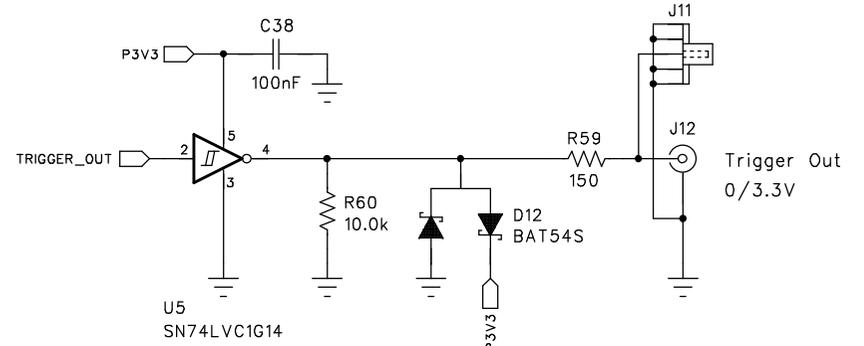
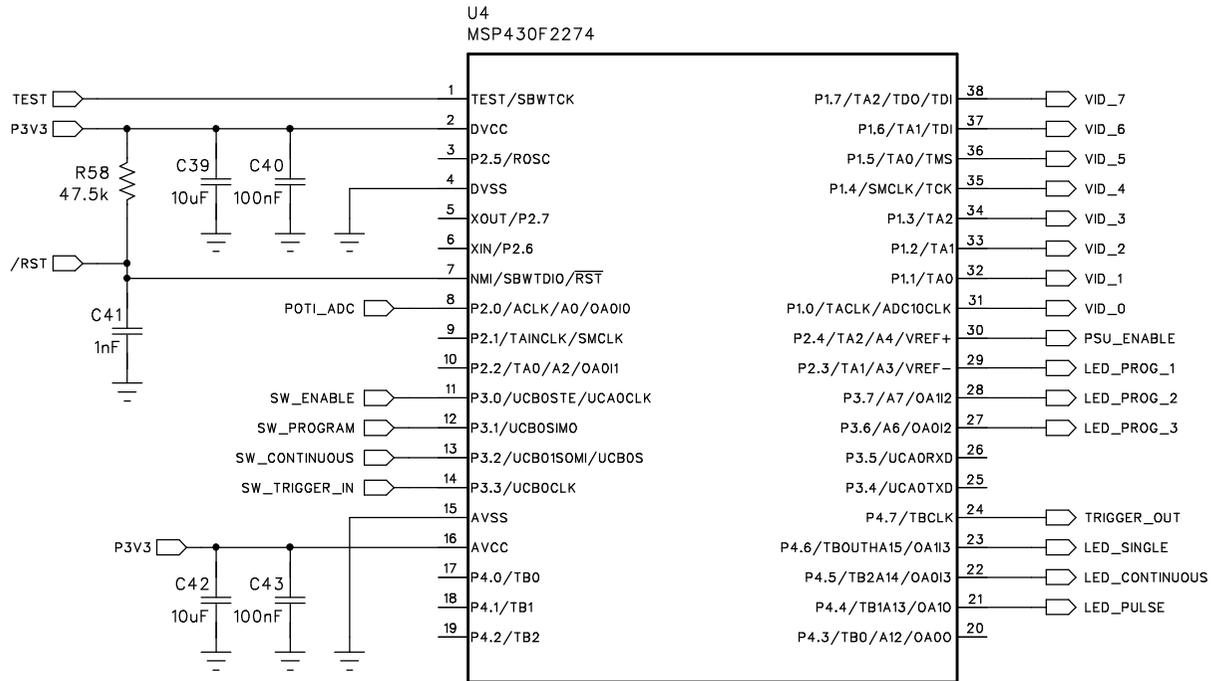
 * Populate either J9 or J10



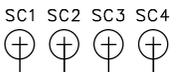
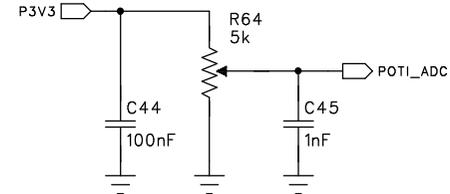
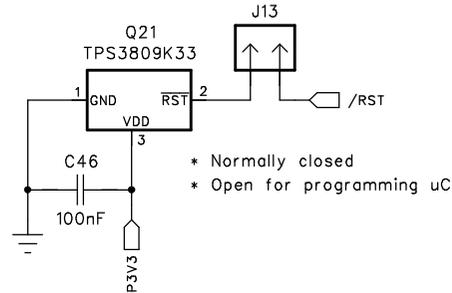
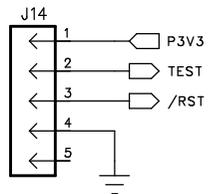
Title			Cranking Simulator		
Size	Number	Rev			
B	PMP7233	A			
Date	2/18/2013	Drawn by		M. Ulmann	
Filename	PMP7233RevA.sch	Sheet		3 of 4	

Engineer Matthias Ulmann

Microcontroller, Trigger Output, Switches, Poti, JTAG Interface, Reset Circuit



2-Wire JTAG Interface (Spy-Bi-Wire)



Notes

* Populate either J11 or J12

Title			Cranking Simulator		
Size	Number	Rev			
B	PMP7233	A			
Date	2/18/2013	Drawn by		M. Ulmann	
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